

# EC5720ETTTS-16.384M

[Click part number to visit Part Number Details page](#)

## REGULATORY COMPLIANCE (Data Sheet downloaded on Dec 5, 2018)


[Click badges to download compliance docs](#)

Regulatory Compliance standards are subject to updates by governing bodies. Click the badges to download the latest compliance docs for this part number directly from Ecliptek.



## ITEM DESCRIPTION

Quartz Crystal Clock Oscillators XO (SPXO) LVCMOS (CMOS) 2.5Vdc 4 Pad 2.0mm x 2.5mm Ceramic Surface Mount (SMD) 16.384MHz  $\pm 20$ ppm -40°C to +85°C

## ELECTRICAL SPECIFICATIONS

|                                       |  |
|---------------------------------------|--|
| Nominal Frequency                     | 16.384MHz  |
| Frequency Tolerance/Stability         | $\pm 20$ ppm Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Shock, and Vibration) |
| Operating Temperature Range           | -40°C to +85°C   |
| Supply Voltage                        | 2.5Vdc $\pm 5\%$   |
| Input Current                         | 4.5mA Maximum  |
| Output Voltage Logic High (Voh)       | 90% of Vdd Minimum (IOH= -4mA)   |
| Output Voltage Logic Low (Vol)        | 10% of Vdd Maximum (IOL= +4mA)   |
| Rise/Fall Time                        | 10nSec Maximum (Measured at 20% to 80% of waveform)  |
| Duty Cycle                            | 50 $\pm 5$ (%) (Measured at 50% of waveform)   |
| Load Drive Capability                 | 15pF Maximum   |
| Output Logic Type                     | CMOS   |
| Pin 1 Connection                      | Tri-State (High Impedance)   |
| Tri-State Input Voltage (Vih and Vil) | 90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)   |
| Standby Current                       | 10 $\mu$ A Maximum (Disabled Output: High Impedance)   |
| RMS Phase Jitter                      | 1pSec Maximum (12kHz to 20MHz offset frequency)  |
| Start Up Time                         | 10mSec Maximum   |
| Storage Temperature Range             | -55°C to +125°C  |

## ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

|                              |   |
|------------------------------|---|
| ESD Susceptibility           | MIL-STD-883, Method 3015, Class 1, HBM: 1500V |
| Fine Leak Test               | MIL-STD-883, Method 1014, Condition A         |
| Flammability                 | UL94-V0                                       |
| Gross Leak Test              | MIL-STD-883, Method 1014, Condition C         |
| Mechanical Shock             | MIL-STD-883, Method 2002, Condition B         |
| Moisture Resistance          | MIL-STD-883, Method 1004                      |
| Moisture Sensitivity         | J-STD-020, MSL 1                              |
| Resistance to Soldering Heat | MIL-STD-202, Method 210, Condition K          |
| Resistance to Solvents       | MIL-STD-202, Method 215                       |
| Solderability                | MIL-STD-883, Method 2003                      |
| Temperature Cycling          | MIL-STD-883, Method 1010, Condition B         |
| Vibration                    | MIL-STD-883, Method 2007, Condition A         |

## EC5720ETTTS-16.384M [Click part number to visit Part Number Details page](#)

### MECHANICAL DIMENSIONS (all dimensions in millimeters)



| PIN | CONNECTION     |
|-----|----------------|
| 1   | Tri-State      |
| 2   | Case/Ground    |
| 3   | Output         |
| 4   | Supply Voltage |

| LINE | MARKING  |
|------|--|
| 1    | <b>E16.3</b><br><i>E=Ecliptek Designator</i>                   |
| 2    | <b>XXXXX</b><br><i>XXXXX=Ecliptek Manufacturing Identifier</i> |

### Suggested Solder Pad Layout

All Dimensions in Millimeters



All Tolerances are ±0.1

# EC5720ETTTS-16.384M [Click part number to visit Part Number Details page](#)

## OUTPUT WAVEFORM & TIMING DIAGRAM



# EC5720ETTTS-16.384M

[Click part number to visit Part Number Details page](#)

## Test Circuit for CMOS Output



Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value  $C_L$  includes sum of all probe and fixture capacitance.

## X-ON Electronics

Largest Supplier of Electrical and Electronic Components

*Click to view similar products for [Standard Clock Oscillators](#) category:*

*Click to view products by [Ecliptek](#) manufacturer:*

Other Similar products are found below :

[EP1400SJTSC-125.000M](#) [601137](#) [601252](#) [CSX750FBC-24.000M-UT](#) [CSX750FBC-33.333M-UT](#) [CSX750FCC-3.6864M-UT](#) [F335-12](#) [F335-25](#) [F535L-50](#) [DSC506-03FM2](#) [ASA-20.000MHZ-L-T](#) [ASA-25.000MHZ-L-T](#) [ASA-27.000MHZ-L-T](#) [ASV-20.000MHZ-LR-T](#) [ECS-2018-160-BN-TR](#) [EL13C7-H2F-125.00M](#) [MXO45HS-2C-66.6666MHZ](#) [NBXDBB017LN1TAG](#) [NBXHBA019LN1TAG](#) [SiT1602BI-22-33E-50.000000E](#) [SIT8003AC-11-33S-2.04800X](#) [SiT8256AC-23-33E-156.250000X](#) [SIT8918AA-11-33S-50.000000G](#) [SM4420TEV-40.0M-T1K](#) [SMA4306-TL-H](#) [F335-24](#) [F335-40](#) [F335-50](#) [F535L-10](#) [F535L-12](#) [F535L-16](#) [F535L-24](#) [F535L-27](#) [F535L-48](#) [CSX750FBC-20.000M-UT](#) [CSX-750FBC33333000T](#) [CSX750FBC-4.000M-UT](#) [CSX750FBC-7.3728M-UT](#) [CSX750FBC-8.000M-UT](#) [CSX-750FCC14745600T](#) [CSX750FCC-16.000M-UT](#) [CSX-750FCC40000000T](#) [CSX750FCC-4.000M-UT](#) [ASA-22.000MHZ-L-T](#) [ASA2-26.000MHZ-L-T](#) [ASA-40.000MHZ-L-T](#) [ASA-48.000MHZ-L-T](#) [ASA-60.000MHZ-L-T](#) [ASF1-3.686MHZ-N-K-S](#) [XLH735025.000JU4I8](#)