

ChipMax-2

ChipMax2 is a low cost and high-performance universal device programmer for PC USB 2.0 interface.

It programs a 64 Mbit flash memory in 42 seconds. **ChipMax2** supports over 13000 programmable devices with low voltage (1.8 V). **ChipMax2** is the state-of-art universal programmer offers you the most advanced programming facilities with the most user friendly interface.



PRODUCT HIGHLIGHTS

- Universal Programmer with 48-pin Textool Socket
- B/P/V takes 85 Sec. for 8 of 64 Mbit Flash Memory
- Support PC with USB 2.0 Port
- Win 95 / 98 / NT / 2000 / XP
- Internal (110 250 VAC) switching power supply

DEVICES PROGRAMMED

- M25Pxx, MX25Lxx, S25LFxx, SST25VFxxx
- NAND Flash Memory
 Samsung K9Kxxx, K5Axxx, K5Fxxx,
 M-System MDOC-256 / 512 G
 Toshiba TC58xxx
 St-Micro NAND01G
- 28Fxxx, 29Fxxx, 38Fxxx, 29LVxxx, 26LVxxx, 29GLxxx, 29ALxxx, 29Wxxx, 36xxx, 32HFxxx, 34VFxxx, 37VFxxx, 39SFxxx, 39VFxxx, 45LFxxx, 49LFxxx, 50FWxxx
- 27xxx and 27Cxxx series, from 16 Kbit to 32Mbit with 8-bit/16-bit
- EEPROM 27Exxx, 28xxx, and 28Cxxx series
- 256 Kbit to 32 Mbit 28Fxxx, 29Fxxx, 29Cxxx, 29BVxxx, 29LVxxx, 29Wxxx, 49Fxxx series (1.8, 2.7, 3.3, 5.0, or 12 Volt)
- 24Cxx, 24Fxx, 25Cxx, 59Cxx, EPC1/1213/1648, and 93Cxx
- ispLSI(10/20)xx, ispGAL22V10, ispGDS1(2)x, LC40(41)xx M4Axxx series
- PALCEs, GALs: 16V8, 20V8, 22V10, 20RA10, 26V12 series
- DS12xx, DS13xx, DS15xx, DS16xx series
- PEELs 153, 173, 253, 273, 18CV8, 20CG10 series
- EPLDs PLCxxx, PLSxxx, PLUSxxx, Epxxx, EPCxxx, EPMxxx, PLDxxx, 5Cxxx, 85Cxxx series
- ATF16/20/22Vxx, ATV750/1500/2500
- MACH1xx/2xx/4xx, and xxx-SP series
- MAX5000, MAX7000 series
- 8741, 8742, 8748, 8749 series
- 87C51/52, -FA, -FB, -FC, '528, '652, '654, '54, 89Cxx, 89Sxx, 89LVxx, 89Cxxxx, DCF85xx, 85(87/89)LPCxxx, PXAxx series 87C751/752
- ST62xx, ST7xx, ST9xx, ST10xx series
- XC17Sxxx, 17Vxxx, 72xx, 73xx, XCF01 / 02 / 04 series
- PIC12Cxxx, PIC16xxx, 17xxx, 18xxx series

- MC68705, MC68HC705, MC68HC711, MC68HC908, MC9S08
- Z86Cxx, Z86Exx series

DEVICES TESTED

■ TTL type : 54, 74(S, LS, L, H, HC) series

■ CMOS type: 40, 45 series

■ Dynamic Memory: 4164 - 1MBit

■ Static Memory: 6116 - 6256

User definable test pattern generation

KEY FEATURES

<Software>

- Supports full WINDOW 95/98/2000/NT and XP.
- Auto search device select function supports E(E)PROMs
 Microcontrollers.
- Device insertion test identifies improperly inserted device before programming.
- Check for incorrect device insertion, backward, incorrect position, and poor pin contact.
- High-speed device function tests and user creatable test library.
- Device Operations: Read,Blank check, Program, Verify, Checksum, Data compare, Security, Auto(blank check-program-verify), Option Bit program.
- Display programming parameters and optional bit information on the screen.
- Set device/buffer address ranges before programming devices.
- Extensive on-line F1 help system provides text and graphics.
- User-changeable programming parameters.
- Built-in editor for both buffer date and test vectors.
- Support Binary and all hex files (POF and JEDEC, Intel Hex, Motorola S Records, Tekhex, straight hex, hex-space, Extended Tekhex, and others; automatic file type recognition) with Load, Edit, and Save commands.
- Distribution of 16- and 32- bit data into 8-bit portions.

ChipMax-2



<Hardware>

- One on-board FPGA for extremely fast communication.
- Supports real low-voltage support : 5, 3.3, 2.7 and 1.8 volt for programming power.
- Detects all pin locations for poor or damaged pin contacts.
- External START key allows production programming mode.
- Internal universal power supply,110-240 VAC (no separate power supply required in foreign country).
- Current limiting protects hardware circuit from improperly inserted or defective chips and operation errors.
- Standard 48-pin ZIF(Zero Insertion Force) socket accepts both 300mil and 600 mil DIP devices.
- True universal pin driver hardware.
- Support a high-speed USB2.0 port for PC interface.
- Hardware diagnostic program exams all socket-pin drivers before using programmer.
- Gang Program Mode allows programmers up to 8 units as concurrent programming system.

(START ALL key enable to program the programmers simultaneously)

PROGRAMMING SPEED (100 % data in memory) min:sec.00

Device P/N	Read	Blank	Prog	Verify	Erase
AM29F032B	4.70	4.80	56.24	4.73	38.16
AM29LV128ML	6.70	6.65	03:26.00	6.65	
AM29LV256MH	17.41	16.00	04:06.16	16.77	-
AM29LV641DH	4.20	4.43	42.02	4.44	-
AM29LV641DH (TopMax)	01:40.00	01:38.00	04:48.54	01:42.00	
E28F128J3A	9.93	7.49	1:55	7.75	01:43.30
E28F128J3C	9.93	7.49	02:01.80	7.75	30.31
E28F640J3C	4.27	4.09	70.02	4.19	17.5
K8D3216UTC-T	2.47	2.47	22.17	2.51	•
MX26L004BQC	0.81	0.81	28.24	0.81	31.87
MX29LV641MH	-	4.86	01:10.40	4.83	
RD38F2040	3.09	3.09	18.71	3.09	53.47
RD38F4050	18.81	18.08	02:55.4	18.30	04:41.8

^{*} Programming Speed With 100% Data in Memory , Times in sec.

TECHNICAL SPECIFICATION

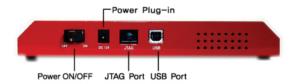
ChipMax2 Dimension:

■ 1 circuit board with heavy duty case

■ Module: 9.5"(L) x 7.0"(W) x 1.5"(H)

■ Weight: 1.75 lbs

■ Built-in Socket: Textool 48 pin ZIF



SYSTEM REQUIREMENT

- TYPE: 386, 486, Pentium or compatibles. PS/2, Portable(notebook) computer.
- A hard disk drive (4 Megabytes) is recommended for software installation / set up.
- PC RAM size: 512K of conventional memory
- I/O PORT: USB 1.1 or 2.0 port
- OS: WINDOWS 95 / 98 / 2000 / NT / XP or greater

SAFETY STANDARD

■ CE compliant

STANDARD ACCESSAORIES

- AC/DC Adapter(110 to 240 VAC)
- USB Cable
- Software CD and User's Guide



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