

# **REAL TIME CLOCK MODULE (SPI-Bus)**

# **RX-4574LC**

•Built in frequency adjusted 32.768 kHz crystal unit.

 Interface Type : 3-wire serial interface Operating voltage range : 1.6 V to 5.5 V

•Wide Timekeeper voltage range: 1.3 V to 5.5 V Low backup current

: 0.35 μA / 3 V (Typ.) •32.768 kHz frequency output function: C-MOS output With Control Pin

•The various functions include full calendar, alarm, timer.







**Product Number** 



RX-4574LC: Q414574C2000300

## **Block diagram**

### 32./68 kHz CONTROL LINE HDH OSC DIVIDER CLOCK and CALENDAR **FOUT** OUTPUT TIMER **FCON** CONTROLLER REGISTER / TIRQ **NTERRUPTS** ALARM CONTROLLER REGISTER / AIRQ DATA CONTROL BUS REGISTER CLK INTERFACE CIRCUIT SHIFT REGISTER CE 0

### Overview

- 32.768 kHz frequency output function
   FOUT pin output (C-MOS output), CL=30 pF
   Output frequency is selectable from 1/30 Hz to 32.768 kHz (32 Values)

### Timer function

- Timer function which can be set up between 1/4096 second and 255 minutes.
- t is recorded automatically to TF-bit at the time of event occurrence, and it's possible to output with /T RQ pin output (open-drain output).

  • Selectable one time mode or repeat mode.

### Alarm function

- · Alarm function can be set to any combination of day of week, hour, or minute.
  • t is recorded automatically to AF-bit at the time of event
- occurrence, and it's possible to output with /AIRQ pin output (open-drain output).
- \* Functions are compatible with RTC-4574 SA / JE / NB.

### Pin Function

Signal Name	Input / Output	Function
CE0	Input	The chip enabled input pin 0, (Built-in pull-down resistance) When both CEO and CE1 pins are at the "H" level, access to this Real time clock module becomes possible.
CE1	Input	The chip enabled input pin 1.  When the CE1 pin is at the HIGH level, the FOUT pin is in the output state.
CLK	Input	The shift clock input pin for serial data transfer.
DATA	Bi-directional	The data input / output pin for serial data transfer.
FOUT	Output	This pin outputs the reference clock signal at 32.768 kHz ( C-MOS output ). High impedance at the time of output off.
FCON	Input	The input pin for the FOUT output control.
/ AIRQ	Output	The open drain output pin for alarm and time update interrupts.
/ T RQ	Output	The open drain output pin for timer interrupt.
V <sub>DD</sub>	_	Connected to a positive power supply.
GND	_	Connected to a ground.

### Terminal connection / External dimensions



\*Stop using the glue

Any glue must never use it after soldering LC-package to a circuit board. This product
has glass on the back side of a package. When glue invasions between circuit board
side and glass side, then glass cracks by thermal expansion of glue. In this case a
crystal oscillation stops. Consider glue abolition or glue do not touch to LC-package

# Specifications (characteristics)

# ■ Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Power voltage	VDD		1.6	3.0	5.5	٧
Clock voltage	Vclk	I	1.3	3.0	5.5	٧
Operating temperature	Topr	1	-40	+25	+85	ပ္

### ■ Frequency characteristics

Item	Symbol	Conditions	Rating	Unit
Frequency tolerance	Δf/f	Ta = +25 °C VDD = 3 0 V	B: 5 ± 23 *	× 10⁻⁵
Oscillation	tsta	Ta = +25 °C V <sub>DD</sub> = 1 6 V	1 Max.	s
Start-up time		Ta = -40 °C to +85 °C VDD = 1 6 V	3 Max.	s

<sup>\*</sup>Equivalent to ±1 minute of monthly deviation

# \* Refer to application manual for details.

■ Current consumption characteristics				Ta = -40 °C to +85 °C				
tem	Symbol	Conditions		Min.	Тур.	Max.	Unit	
	Івк	CE0, CE1 = GND FOUT ;output OFF ( Hi - z )	VDD = 5 V	-	0.45	09	μΑ	
Current			VDD = 3 V	-	0 35	0.7		
Consumption	32k	CE0 = GND CE1 = Vpp	VDD = 5 V	1	8.0	20.0	μΑ	
		FOUT; 32.768 kHz output ON CL = 30 pF	VDD = 3 V	-	5.0	12.0		

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►Pb free.



► Complies with EU RoHS directive.

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▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



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