

## REAL TIME CLOCK MODULE (I<sup>2</sup>C-Bus) **High-Stability**



### Product Number RX-8025SA AA : Q41802552000100

SEIKO EPSON CORPORATION

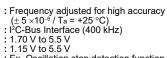
RX-8025SA AC : Q41802551000200

## **RX-8025SA**

•Built-in 32.768 kHz crystal unit

- Interface Type
- •Operating voltage range •Wide voltage for timekeeping

- \* The I<sup>2</sup>C-Bus is a trademark of NXP Semiconductors





#### **Block diagram** • Features built-in 32.768 kHz crystal unit • Frequency adjusted for high accuracy. ( $\pm$ 5 $\times 10^{-6}$ / Ta = +25 $^{\circ}C$ ) Alarm\_W Register ( Min,Hour,Week ) FOUT Comparator\_W 32 kHz VDD Output Control Voltage Detect (Equivalent to ±13 seconds of monthly deviation) FOE Varm\_D Registe (Min,Hour) ₹ omparator\_D The various detection function Power supply voltage monitoring function (with selectable detection threshold) Stop detection function Time Counter 卓 050 Div (Sec.Min.Hour.Week.Dav.Month.Year Power-on reset detection function Alarm function and Periodic interrupt function Address Address Register osc SCI The periodic interrupt outputs Decoder Detec I/O Dual Alarm function. (Date of the week , Hour , minute) (Month, Day, Hour, Minute ) Contro SDA / INTA Interrupt Control Shift Register / INTB GND

#### Pin Function

Signal Name	Input / output	Function				
SCL	Input	Serial clo	ck input p	bin		
SDA	<b>Bi-directional</b>	Data inpu	it and out	put pin		
FOUT	Output	32.768 kHz clock output pin with the output control function. (C-MOS)				
	Output	FOE input	/CLEN1 bit	/CLEN2 bit	FOUT output	
	Input	L	Х	Х	OFF (LOW)	
FOE			0	0	32.768 kHz	
		н	0	1	32.768 kHz	
				1	0	32.768 kHz
					OFF(LOW)	
/ INTA	Output	Interrupt output A pin ( N-ch open drain )				
/ INTB	Output	Interrupt output B pin ( N-ch open drain )				
TEST	—	<ul> <li>* Used by the manufacture for testing.</li> <li>(Do not connect externally.)</li> </ul>				
Vdd	_	Connected to a positive power supply.				
GND	—	Connected to a ground.				

### Specifications (characteristics)

Recommended Operating Conditions						
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Power voltage	Vdd	_	1.7	3.0	5.5	V
Clock voltage	VCLK	_	1.15	3.0	5.5	V
Operating temperature	TOPR	_	-40	+25	+85	°C

#### Frequency characteristics

Item	Symbol	Conditions	Range	Unit
Frequency tolerance	∆f/f	Ta = +25 °C VDD = 3.0 V	AA: 5 ± 5 *1) AC: 0 ± 5 *2)	× 10 <sup>-6</sup>
Oscillation start-up time	<b>t</b> sta	Ta = +25 °C VDD = 2.0 V	1 Max.	s
Frequency voltage characteristics	f/V	Ta = +25 °C VDD = 2.0 V to 5.5 V	±1 Max.	× 10 <sup>-6</sup>

\*1) \*2) Equivalent to ±13 seconds of monthly deviation (excluding offset).

#### Terminal connection / External dimensions (Unit:mm) RX - 8025 SA 1. N.C. 14. N.C. 22 Ť. ----2. SCL SDA 13. . 3. FOUT 12. / INTB \_ 4. N.C. 11. GND 5.0 TEST 10. / INTA 5. 3.2 VDD 9. N.C. 6. 7. FOE 8. N.C. SOP - 14 pin

The metal case inside of the molding compound may be exposed on the top or bottom of this product. This purely cosmetic and does not have any e ect on quality, reliability or electrical spec

#### \* Refer to application manual for details.

■ Current consumption characteristics T <sub>a</sub> = -40 °C to +85 °C							
Item	Symbol	Conditions		Min.	Тур.	Max.	Unit
	Івк	fsc∟ = 0Hz FOE = GND	VDD = 5 V	-	0.60	1.80	
Curent Consumption		FOUT ; output OFF(LOW)	VDD = 3 V	-	0.48	1.20	μA
	132k		VDD = 5.5 V	-	3.0	6.5	μA

Power supply detection voltage				Ta = -30 °C to +70 °C			
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	
High-voltage mode	Vdeth	VDD pin	1.90	2.10	2.30	V	
Low-voltage mode	VDETL	Vdd pin	1.15	1.30	1.45	V	

**Overview** 

# PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

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