

### **CRYSTAL OSCILLATOR (SPXO)**

**OUTPUT: CMOS** 





Product Number (please contact us) SG2016CAN: X1G004801xxxx00 SG-210STF: X1G004171xxxx00 SG3225CAN: X1G005961xxxx15 SG5032CAN: X1G004451xxxx00 SG7050CAN: X1G004481xxxx00

# SG2016 / 3225 / 5032 / 7050CAN SG-210STF

Frequency
 Supply voltage
 Function
 Operating temperature
 20 standard frequencies
 1.8 V to 3.3 V Typ.
 Standby(ST)
 40 °C to +105 °C











SG2016CAN (2.0 x 1.6 mm)

N SG-210STF m) (2.5 x 2.0 mm)

SG3225CAN (3.2 x 2.5 mm)

SG5032CAN (5.0 x 3.2 mm)

SG7050CAN (7.0 x 5.0 mm)

### Specifications (characteristics)

Item	Symbol	Specifications			Conditions / Remarks		
Output frequency	fo	14.7456 MHz 16 25 MHz 26	MHz 20 MHz 2 MHz 27 MHz 3	2 MHz 12.288 MHz 4 MHz 24.576 MHz 2 MHz 33.33 MHz 0 MHz 72 MHz			
		1.60 V to 3.63 V			4 MHz ≤ fo ≤ 50 MHz, T_use = +105 °C Max.		
Supply voltage	Vcc	1.71 V to 3.63 V			fo = 72 MHz, T_use = +85 °C Max. Refer to Figure 1		
		2.25 V to 3.63 V			fo = 72 MHz, T_use = +105 °C Max.		
Storage temperature	T stg	-55 °C to +125 °C			SG2016CAN, SG3225CAN		
Storage temperature	1_Sig	-40 °C to +125 °C			All others		
Operating temperature	T_use	-20 °C to +70 °	C, -40 °C to +85 °C, -	40 °C to +105 °C	See of figure *1		
Frequency tolerance	f tol	±25 × 10 <sup>-6</sup>			-20 °C to +70 °C		
Frequency tolerance	1_101		$\pm 50 \times 10^{-6}$		-40 °C to +85 °C, -40 °C to +105 °C		
		V <sub>CC</sub> = 1.8 V ± 10 %	V <sub>CC</sub> = 2.5 V ± 10 %	V <sub>CC</sub> = 3.3 V ± 10 %			
		1.5 mA Max.	1.6 mA Max.	1.8 mA Max.	No load condition, 4 MHz $\leq$ fo $\leq$ 20 MHz		
Current consumption	Icc	1.8 mA Max.	2.0 mA Max.	2.2 mA Max.	No load condition, 20 MHz < fo ≤ 40 MHz		
		2.1 mA Max.	2.4 mA Max.	2.6 mA Max.	No load condition, 40 MHz < fo ≤ 50 MHz		
		2.4 mA Max.	2.8 mA Max.	3.0 mA Max.	No load condition, fo = 72 MHz		
Stand-by current	I_std	2.1 µA Max.	2.1 μA Max. 2.5 μA Max. 2.7 μA Max.		ST =GND		
Symmetry	SYM		45 % to 55 %		50 % V <sub>CC</sub> level, L_CMOS ≤ 15 pF		
	V <sub>OH</sub>		90 % V <sub>CC</sub> Min.		1.8 V ± 10 % 2.5 V ± 10 % 3.3 V ± 10 % lon -1.5 mA -3 mA -4 mA		
Output voltage	V <sub>OL</sub>	10 % V <sub>CC</sub> Max.			I <sub>OH</sub>   -1.5 mA   -3 mA   -4 mA		
Output voltage	V <sub>OH-2</sub>	V <sub>CC</sub> - 0.4 V Min.			1.8 V±10 % 2.5 V±10 % 3.3 V±10 %		
	V <sub>OL-2</sub>	0.4 V Max.			I <sub>OH</sub>		
Output load condition (CMOS)	L_CMOS	15 pF Max.					
	V <sub>IH</sub>	80 % V <sub>CC</sub> Min.			ST terminal		
Input voltage	VIL	20 % V <sub>CC</sub> Max.			- Si terminai		
Rise time and Fall time	tr / tf	3 ns Max. 3.5 ns Max. (@1.8 V±10 %)			20 % V <sub>CC</sub> to 80 % V <sub>CC</sub> level, L_CMOS = 15 pF		
Start-up time	t_str	3 ms Max.			T = 0 at 90 % V <sub>CC</sub>		
Frequency aging	f_age	±3 × 10 <sup>-6</sup> / year Max.			+25 °C, First year		

[Model: SG2016/3225/5032/7050CAN]

Product name (SG2016 C AN 25.000000MHz  $\underline{T}$   $\underline{J}$   $\underline{H}$   $\underline{A}$  (Standard form)  $\underline{0}$   $\underline{0}$   $\underline{0}$   $\underline{0}$   $\underline{0}$ 

- ①Model ②Output(C: CMOS) ③Frequency ④Supply voltage
- ⑤Frequency tolerance ⑥Operating temperature range
- ⑦Internal identification code("A" is default)

⊕Su	pply voltage *See Figure 1	
Т	1.8 V to 3.3 V Typ.	
K	2.5 V to 3.3 V Typ.	

⑤Frequency tolerance / ⑥Operating temperature range			
DB*	±25 × 10 <sup>-6</sup> / -20 °C to +70 °C		
JG	±50 × 10 <sup>-6</sup> / -40 °C to +85 °C		
JH	±50 × 10 <sup>-6</sup> / -40 °C to +105 °C		

<sup>\*</sup> Please refer to Product number list on Full Data Sheet for available frequencies

[Model: SG-210STF]

③Sι	upply voltage	*See Figure 1	_
Т	1.8 V to 3.3	V Typ.	

④Frequency ⑤Frequency tolerance

⑤Frequency tolerance			
S*	±25 × 10 <sup>-6</sup> / -20 °C to +70 °C		
L	±50 × 10 <sup>-6</sup> / -40 °C to +85 °C		
Υ	±50 × 10 <sup>-6</sup> / -40 °C to +105 °C		

<sup>\*</sup> Please refer to Product number list on Full Data Sheet for available frequencies

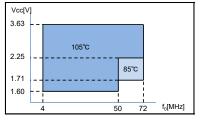
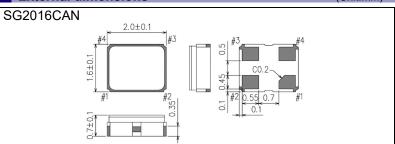


Figure 1 : The upper limit of Operating temperature and the related conditions

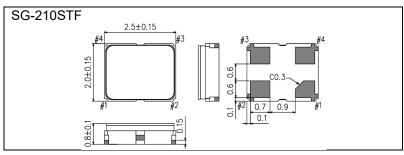
Please note that Supply voltage range ( $V_{\rm CC}$ ) depends on Output frequency (fo) and upper limit of Operationg temperature (T\_use Max.).

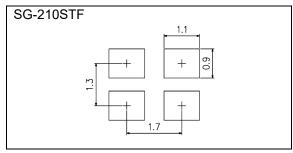
### **External dimensions**

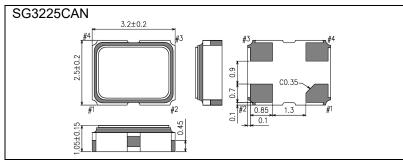
(Unit:mm)

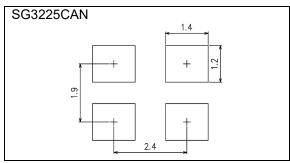


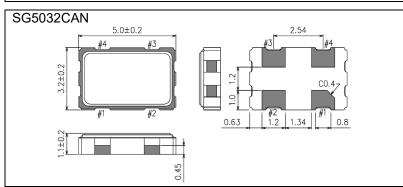
# Footprint (Recommended) (Unit:mm) SG2016CAN

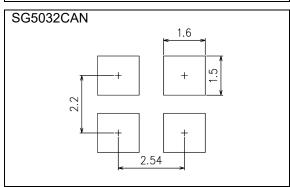


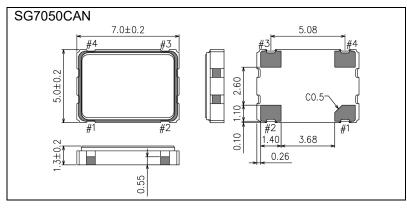


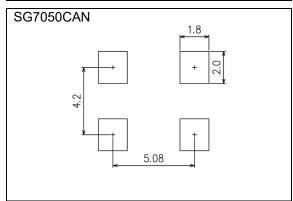












### Pin Map

Pin	Connection	Function				
		ST terminal				
1	ST		ST function	Oscillator circuit	Output	
	31		HIGH or "open"	Oscillation	Specified frequency: Enable	
			LOW	Oscillation stop	High impedance: Disable	
2	GND	Ground				
3	OUT	Clock o	utput			
4	V <sub>cc</sub>	Power s	supply			

■Notes: To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

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►Pb free.



► Complies with EU RoHS directive.

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