

CRYSTAL OSCILLATOR (SPXO)

OUTPUT: CMOS





Product Number (please contact us) SG2016CAN: X1G004801xxxx00 SG-210STF: X1G004171xxxx00 SG3225CAN: X1G005961xxxx15 SG5032CAN: X1G004451xxxx00 SG7050CAN: X1G004481xxxx00

SG2016 / 3225 / 5032 / 7050CAN SG-210STF

Frequency
 Supply voltage
 Function
 Operating temperature
 20 standard frequencies
 1.8 V to 3.3 V Typ.
 Standby(ST)
 Operating temperature
 -40 °C to +105 °C











SG2016CAN (2.0 x 1.6 mm)

SG-210STF (2.5 x 2.0 mm)

SG3225CAN (3.2 x 2.5 mm)

SG5032CAN (5.0 x 3.2 mm)

SG7050CAN (7.0 x 5.0 mm)

Specifications (characteristics)

Item	Symbol	Specifications				Conditions / Remarks				
Output frequency	fo	14.7456 MHz 16 25 MHz 26	MHz 10 MHz MHz 20 MHz MHz 27 MHz MHz 48 MHz	12 M 24 M 32 M 50 M	ИНz 24.576 N ИНz 33.33 М	ИНz 1Hz				
Supply voltage	Vcc	1.60 V to 3.63 V 1.71 V to 3.63 V 2.25 V to 3.63 V					$ \begin{array}{llllllllllllllllllllllllllllllllllll$			
Storage temperature	T_stg	-55 °C to +125 °C -40 °C to +125 °C					SG2016CAN All others			
Operating temperature	T_use	-20 °C to +70 °C, -40 °C to +85 °C, -40 °C to +105 °C					See of figure *1			
		±25 × 10 ⁻⁶				-20 °C to +70 °C				
Frequency tolerance	f_tol	±50 × 10 ⁻⁶				-40 °C to +85 °C, -40 °C to +105 °C				
		V _{CC} = 1.8 V ± 10 %	$V_{CC} = 2.5 \text{ V} \pm 1$	0 %	V _{CC} = 3.3 V ± 1	0 %	,			
	Icc	1.5 mA Max.	1.6 mA Max		1.8 mA Max	ζ.	No load condition, 4 MHz ≤ fo ≤ 20 MHz			
Current consumption		1.8 mA Max.	2.0 mA Max		2.2 mA Max	ί.	No load condition, 20 MHz < fo ≤ 40 MHz			
		2.1 mA Max.	2.4 mA Max		2.6 mA Max	ί.	No load condition, 40 MHz < fo ≤ 50 MHz			
		2.4 mA Max.	2.8 mA Max		3.0 mA Max	ί.	No load	condition, fo = 7	72 MHz	
Stand-by current	I_std	2.1 µA Max.	2.5 µA Max.		2.7 µA Max.		ST =GND			
Symmetry	SYM	45 % to 55 %				50 % V _C	c level, L_CMO	S ≤ 15 pF		
	Voн	90 % Vcc Min.					1.8 V ± 10 %	2.5 V ± 10 %	3.3 V ± 10 %	
	VoL	10 % V _{CC} Max.				I _{OH}	-1.5 mA 1.5 mA	-3 mA 3 mA	-4 mA 4 mA	
Output voltage	V _{OH-2}	V _{CC} - 0.4 V Min.					1.8 V±10 %	2.5 V±10 %	3.3 V±10 %	
	V _{OL-2}	0.4 V Max.					I _{OH}	-3 mA 3 mA	-4 mA 4 mA	-6 mA 6 mA
Output load condition (CMOS)	L_CMOS	15 pF Max.					- OL	-		
lanut valta sa	V _{IH}	80 % Vcc Min.					ST terminal			
Input voltage	V _{IL}	20 % V _{CC} Max.								
Rise time and Fall time	tr / tf	3 ns Max. 3.5 ns Max. (@1.8 V±10 %)					20 % V _{CC} to 80 % V _{CC} level, L_CMOS = 15 pF			
Start-up time	t_str	3 ms Max.					T = 0 at 90 % Vcc			
Frequency aging	f_age	±3 × 10 ⁻⁶ / year Max.				+25 °C, First year				

[Model: SG2016/3225/5032/7050CAN]

 Product name
 SG2016 C AN (25.000000MHz T J H A)

 (Standard form)
 ① ② ③ ④⑤⑥⑦

 ①Model ②Output(C: CMOS)
 ③Frequency ④Supply voltage

⑤Frequency tolerance ⑥Operating temperature range

⑦Internal identification code("A" is default)

O						
 ④ Su	pply voltage	*See Figure 1				
Т	1.8 V to 3.3					
K	2.5 V to 3.3	V Typ.				

⑤Frequency tolerance / ⑥Operating temperature range				
DB*	±25 × 10 ⁻⁶ / -20 °C to +70 °C			
JG	±50 × 10 ⁻⁶ / -40 °C to +85 °C			
JH	±50 × 10 ⁻⁶ / -40 °C to +105 °C			

^{*} Please refer to Product number list on Full Data Sheet for available frequencies

[Model: SG-210STF]

4 Frequency 5 Frequency tolerance

3Su	pply voltage *See Figure 1		
Т	1.8 V to 3.3 V Typ.		

⑤Frequency tolerance				
S*	±25 × 10 ⁻⁶ / -20 °C to +70 °C			
L	±50 × 10 ⁻⁶ / -40 °C to +85 °C			
Υ	±50 × 10 ⁻⁶ / -40 °C to +105 °C			

^{*} Please refer to Product number list on Full Data Sheet for available frequencies

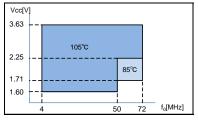
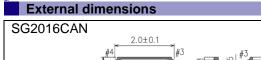
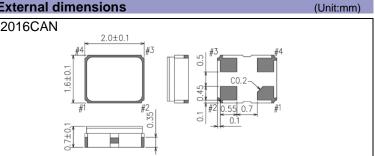
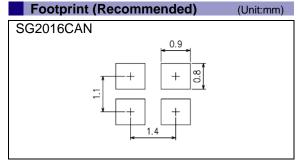


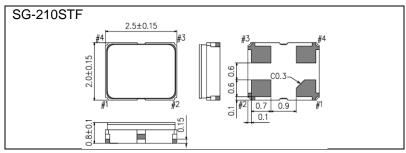
Figure 1 : The upper limit of Operating temperature and the related conditions

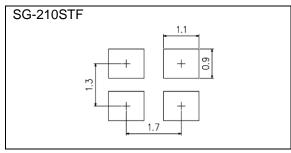
Please note that Supply voltage range (Vcc) depends on Output frequency (fo) and upper limit of Operationg temperature (T_use Max.).

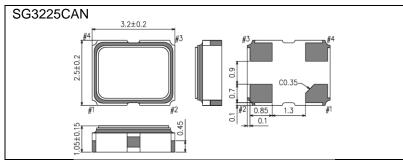


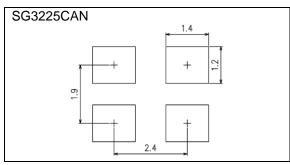


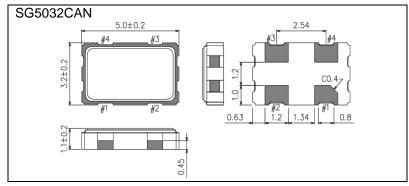


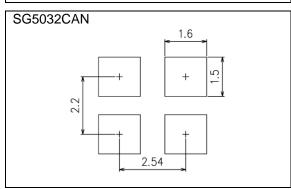


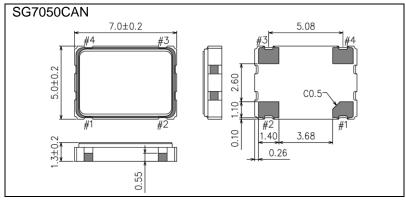


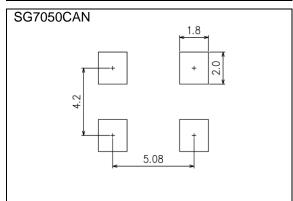












Pin Map

Pin	Connection	Function						
1 111	Connection							
1	ST	ST term	ninai			_		
			ST function	Oscillator circuit	Output			
			HIGH or "open"	Oscillation	Specified frequency: Enable			
			LOW	Oscillation stop	High impedance: Disable			
2	GND	Ground	Ground					
3	OUT	Clock or	Clock output					
4	V _{cc}	Power s	upply					

■Notes: To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

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►Pb free.



► Complies with EU RoHS directive.

*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.





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