Crystal oscillator

SEIKO EPSON CORPORATION



Rem	Symbol	SG3225EAN / SG5032EAN / SG7050EAN	SG3225VAN / SG5032VAN / SG7050VAN				
Output frequency range	fo	73.5 MHz t	o 700 MHz	Please contact us about available frequencies.			
Supply voltage	Vcc	K: 2.5 V - 10 %	to 3.3 V + 10 %				
Storage temperature	T_stg	-40 °C to	+125 °C	Storage as single product.			
Operating temperature	T_use	B: -20 °C to +70 °C,	G: -40 °C to +85 °C				
Frequency tolerance	f_tol	J: \pm 50 \times 10 ⁻⁶ , E: \pm 30	0 × 10 ⁻⁶ , C: ± 20 × 10 ⁻⁶				
Current consumption	lcc	65 mA Max.	30 mA Max.	OE = Vcc, L_ECL = 50 Ω or L_LVDS = 100 Ω			
Disable current	I_dis	20 m/	A Max.	OE = GND			
Symmetry	SYM	45 % to 55 %		At outputs crossing point			
Output voltage (LV-PECL)	Vон	Vcc - 1.0 V to Vcc - 0.8 V	_	DC characteristics			
	Vol	Vcc - 1.78 V to Vcc - 1.62 V	_				
Output voltage (LVDS)	Vod	_	250 mV to 450 mV	Vod1, Vod2			
	dVod	_	50 mV Max.	dVop = Vop1-Vop2 Vos1, Vos2 DC characteristics			
	Vos	-	1.15 V to 1.35 V				
	dVos	-	150 mV Max.	dVos = Vos1-Vos2			
Output load condition	L_ECL	50 Ω	_	Terminated to Vcc -2.0 V			
(ECL) / (LVDS)	L_LVDS	-	100 Ω	Connected between OUT to OUT			
Input voltage	Viн	70 % V	cc Min.	OE terminal			
	VIL	30 % Vcc Max.					
Rise time / Fall time	tr / tr	350 ps Max.	300 ps Max.	300 ps Max. LV-PECL: Between 20 % and 80 % of (VOH-VOL). LVDS: Between 20 % and 80 % of Differential Output peak to peak voltage			
Start-up time	t_str	3 ms	Max.	Time at minimum supply voltage to be 0 s			
Phase Jitter	tPJ	0.6 ps	Max.*1	Offset frequency: 12 kHz to 20 MHz			
Frequency aging	f_aging	$\pm 5 \times 10^{-6}$ /	year Max.	+25 °C, First year, Vcc = 2.5 V, 3.3 V			
				*1 0.9 ps Max. (fo = 243 N	1Hz ~ 250 MHz, 486 MHz ~ 500 N		

Product Name (Standard form)

1

④SupplyK 2.5

<u>SG3225 E AN 156.250000MHz K J G A</u>

2 3 4567

(1)Model (2)Output (E: LV-PECL, V: LVDS) (3)Frequency (4)Supply voltage (5)Frequency tolerance
 (6)Operating temperature (7)Internal identification code ("A" is default)

(56: CG is not available)

voltage	⑤Frequency tolerance		Operating temperature	
V ~ 3.3 V	J	±50 × 10 ⁻⁶	В	-20 ℃ ~ +70 ℃
	Е	±30 × 10 ⁻⁶	G	-40 ℃ ~ +85 ℃
	С	±20 × 10 ⁻⁶		



Not to scale.



To maintain stable operation, provide a 0.01 μF to 0.1 μF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

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	Contains Pb in products exempted by EU RoHS directive.
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For Automotive	► Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.
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