Clock OSC

SG-210SCBA

Product name SG-210SCBA 24.576000 MHz L Conforms to AEC-Q200

Product Number / Ordering code X1G004591A052xx

Please refer to the 8.Packing information about xx (last 2 digits)

Output waveform CMOS

Pb free / Complies with EU RoHS directive

Reference weight Typ. 15 mg

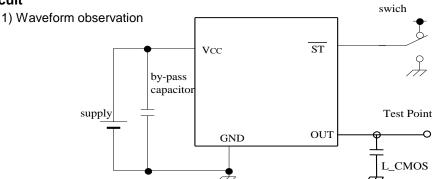
1.Absolute maximum ratings	;					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.3	,	+5	V	-
Storage temperature	T_stg	-40	-	+125	°С	Storage as single product
Input voltage	Vin	-0.3	-	Vcc+0.3	V	ST terminal

2.Specifications(charac	teristics)					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Output frequency	f0		24.576000		MHz	
Supply voltage	Vcc	2.7	3.3	3.6	V	-
Operating temperature	T_use	-40	-	+85	۰C	-
Frequency tolerance	f_tol	-50	-	50	x10 ⁻⁶	T_use
Current consumption	Icc	-	-	3	mA	No load condition
Stand-by current	I_std	-	-	1.0	μΑ	ST = GND
Symmetry	SYM	45	-	55	%	50% Vcc Level L_CMOS=<15pF
Output voltage	V _{OH}	0.9Vcc	-	-		IOH=-1mA
	V_{OL}	-	-	0.1Vcc		IOL=1mA
Output load condition	L_CMOS	-	-	15	pF	CMOS Load
Input voltage	V_{IH}	0.8Vcc	-	-		ST terminal
	V_{IL}	-	-	0.2Vcc		ST terminal
Rise time	t _r	-	-	3	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Fall time	tf	-	-	3	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Start-up time	t_str	-	-	3	ms	t = 0 at 0.9Vcc
Jitter	t _{DJ}	-	0	-	ps	Deterministic Jitter Vcc=3.3
	t _{RJ}	-	2.3	-	ps	Random Jitter Vcc=3.3V
	t _{RMS}	-	2.2	-	ps	δ(RMS of total distribution) Vcc=3.3V
	t _{p-p}	-	19	•	ps	Peak to Peak Vcc=3.3V
	t _{acc}	-	2.7	-	ps	Accumulated Jitter(δ) n=2 to 50000 cycles
Phase jitter	t _{PJ}	-	0.3	-	ps	Off set Frequency: 12kHz to 20MHz Vcc=3.3V
Phase noise	L(f)	-	-	-	dBc/Hz	-
		-	-100	-	dBc/Hz	Off set 10Hz Vcc=3.3V
		-	-129	-	dBc/Hz	Off set 100Hz Vcc=3.3V
		-	-144	-	dBc/Hz	Off set 1kHz Vcc=3.3V
		-	-153	-	dBc/Hz	Off set 10kHz Vcc=3.3V
		-	-159	-	dBc/Hz	Off set 100kHz Vcc=3.3V
		-	-163	_	dBc/Hz	Off set 1MHz Vcc=3.3V
Frequency aging	f_age	-3	-	3	x10 ⁻⁶	@+25°C first year
		-	-	-		-

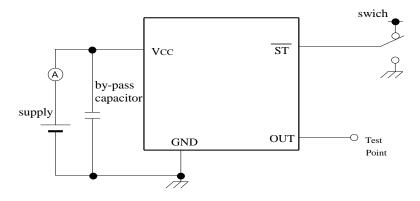
3.Timing chart

VCC 80 % VCC 20 % VCC GND tr tf SEIKO EPSON CORPORATION SEIKO EPSON CORPORATION

4.Test circuit

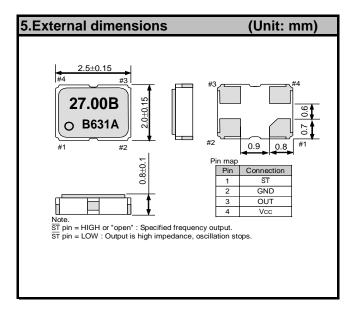


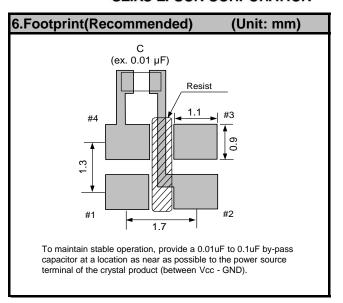
2) Current consumption

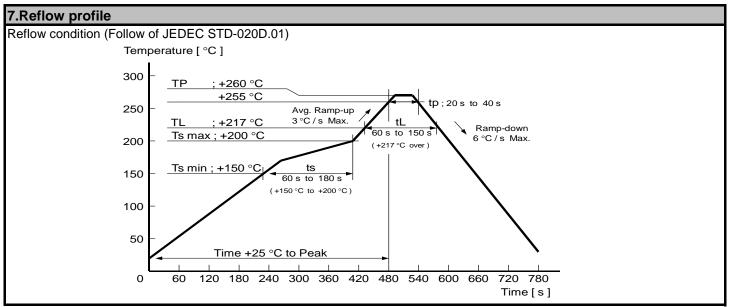


*Current consumption under the disable function should be = GND.

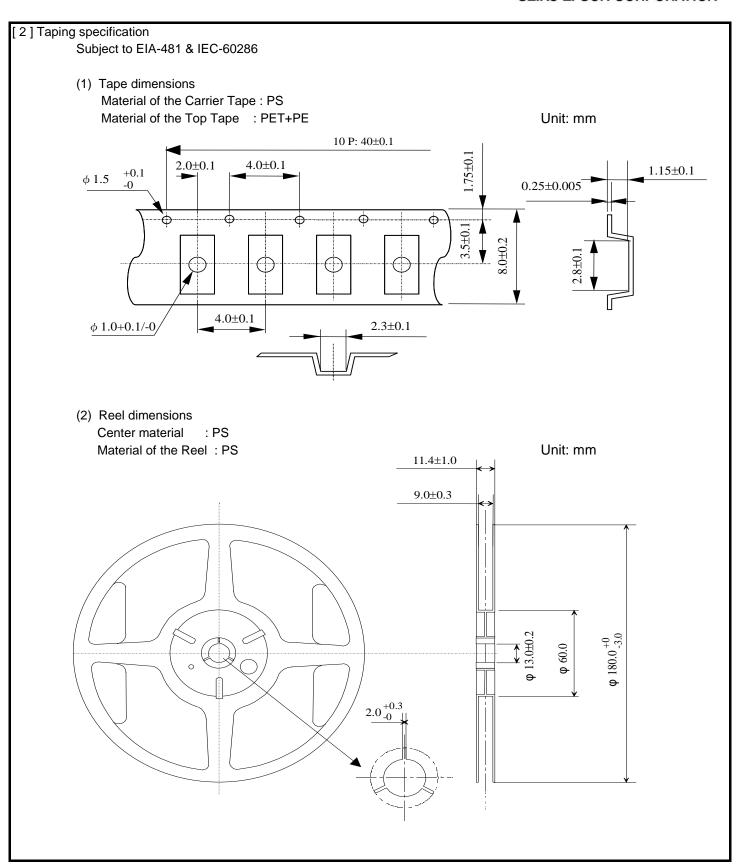
- 3) Condition
- (1) Oscilloscope
- · Band width should be minimum 5 times higher (wider) than measurement frequency.
- · Probe earth should be placed closely from test point and lead length should be as short as possible
- * Recommendable to use miniature socket. (Don't use earth lead.)
- (2) L_CMOS also includes probe capacitance.
- (3) By-pass capacitor (0.01 μ F to 0.1 μ F) is placed closely between VCC and GND.
- (4) Use the current meter whose internal impedance value is small.
- (5) Power supply
- · Start up time (0 %VCC to 90 %VCC) of power source should be more than 150 µs.
- · Impedance of power supply should be as lowest as possible.







1 lProduc	t number l	ast 2 digits code(xx) description		The recommended code is "00"
		591A052xx		
	Code	Condition	Code	Condition
	01	Any Q'ty vinyl bag(Tape cut)	14	1000pcs / Reel
	11	Any Q'ty / Reel	15	2000pcs / Reel
	12	250pcs / Reel	00	3000pcs / Reel



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