

### SEIKO EPSON CORPORATION

## LOW-JITTER SAW OSCILLATOR (SPSO) **OUTPUT : LV-PECL, LVDS**



#### Product Number XG-2121CA P: X1M000311xxxx00 XG-2121CA L: X1M000351xxxx00 XG-2102CA P: X1M000301xxxx00 XG-2102CA L: X1M000341xxxx00

# XG-2121CA XG-2102CA

<ul> <li>Frequency range</li> </ul>	:	100 MHz to 700 MHz
<ul> <li>Supply voltage</li> </ul>	1	2.5 V XG-2121CA
		3.3 V XG-2102CA
<ul> <li>Output</li> </ul>	:	LV-PECL or LVDS
<ul> <li>Function</li> </ul>	:	Output enable (OE)
<ul> <li>External dimensions</li> </ul>	:	$7.0 \times 5.0 \times 1.2 \text{ mm}$

f age

④Output (P:LV-PECL, L:LVDS)

5 Frequency tolerance 6 Operating temperature

	5. 1.0	S 5.0 × 1.2 mm				
<ul> <li>Low jitter and low place</li> </ul>	hase nois	e by SAW unit.				
Specifications	(charac	teristics)				
Item	Symbol	LV-PECL	LVDS	Conditions / Remarks		
nem	Symbol	XG-2121CA P XG-2102CA P	XG-2121CAL XG-2102CAL	Conditions / Remains		
Output frequency range	fo		to 700 MHz	Please contact us about available frequencies.		
Supply voltage	Vcc	2.5 V ± 0.125 V 3.3 V ± 0.33 V				
Storage temperature	T_stg	-55 C t	o +125 C	Storage as single product.		
Operating temperature	T_use		to +85 C, S: -20 C to +70 C			
Frequency tolerance	f_tol	G: ± 50 × 10 <sup>-6</sup>	<sup>3</sup> , H: ±100 × 10 <sup>-8</sup>			
Current consumption	Icc	60 mA Max.	30 mA Max.	OE=Vcc, L ECL=50 Ω or L LVDS=100 Ω		
Disable current	l_dis	2 mA Max.	15 mA Max.	OE=GND		
Symmetry	SYM		to 55 %	At outputs crossing point		
	VOH	1.55 V Typ. 2.35 V Typ.	-	DC characteristics		
Output voltage (LV-PECL)	• ••	Vcc-1.025 V to Vcc-0.88 V	-			
output totage (21 · 222)	Vol	0.80 V Typ. 1.60 V Typ.				
		Vcc-1.81 V to Vcc-1.62 V	-			
	Vod	-	350 mV Typ, 247 mV to 454 mV	Vodi, Vodi		
Output voltage (LVDS)	dVop	-	50 mV Max.	dV <sub>oD</sub> =   V <sub>OD1</sub> -V <sub>OD2</sub>   Voca Voca		
output totage (2122)	Vos	-	1.25 V Typ, 1.125 V to 1.375 V	VOS1, VOS2		
	dVos	-	150 mV Max.	dVos =   Vos1-Vos2		
Output load condition	L_ECL	50 Ω	-	Terminated to Vcc -2.0 V		
(ECL) / (LVDS)	L LVDS	-	100 Ω	Connected between OUT to OUT		
Input voltage	VIH		Vcc Min.	OE terminal		
	VIL	30 % \	Vcc Max.			
Disc time / Coll time		100 -	a Mari	Between 20 % and 80 % of (V <sub>OH</sub> -V <sub>OL</sub> ).		
Rise time / Fall time	tr / tf	400 p	os Max.	Between 20 % and 80 % of Differential Output Peak		
Start-up time	t str	10 m	is Max.	Peak voltage Time at minimum supply voltage to be 0 s		
Start-up time	Lou	0.23 ps Max.	0.27 ps Max.	100 MHz $\leq$ fo < 150 MHz		
	}	0.22 ps Max.	0.24 ps Max.	$150 \text{ MHz} \le 10 < 100 \text{ MHz}$		
Phase Jitter	teu	0.21 ps Max.	0.23 ps Max.	200 MHz < fo < 200 MHz		
		0.18 ps Max.	0.19 ps Max.	300 MHz < fo < 400 MHz		
		0.16 ps Max.	0.16 ps Max.	$400 \text{ MHz} \le 10 < 400 \text{ MHz}$ 12 kHz to 20 MHz $400 \text{ MHz} \le 10 < 500 \text{ MHz}$		

0.14 ps Max.

0.10 ps Max.

Product Name (Standard form)

Frequency aging

> XG-2121 CA 212.500000MHz P H P A (567): GRA, GSA are not available) 2 3 4567②Package type ③Frequency ①Model

0.14 ps Max.

0.10 ps Max

⑥Operating temp ⑤Frequency tolerance Ρ 0 °C to +70 °C ±50 × 10<sup>-6</sup> G °C to +85 °C R -5 н ±100 × 10<sup>-6</sup> S -20 °C to +70 °C

500 MHz  $\leq$  fo < 600 MHz

600 MHz  $\leq$  fo  $\leq$  700 MHz

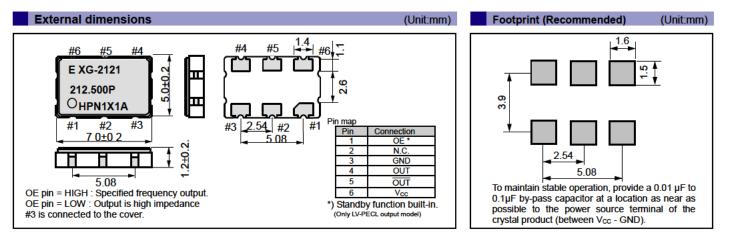
+25 C, First year, Vcc=2.5 V, 3.3 V

⑦Frequency aging (A\*1: Frequency tolerance include aging, N\*2: Frequency tolerance exclude aging)

± 10 × 10<sup>-6</sup>/ year Max

This includes initial frequency tolerance, temperature variation, supply voltage change, reflow drift, and aging(+25 C,10 years). \*1

\*2 This includes initial frequency tolerance, temperature variation, supply voltage change, and reflow drift (except aging).



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