COEVER 18V, 2A, 1MHz Synchronous Buck Converter EA8212H

General Description

The EA8212H is a 2A buck regulator, designed to operate from 4.5V to 18V input voltage range. Built-in low R_{DS(ON)} high/low side Power-MOSFETS not only reduce external components and has up to 96% efficiency, ideal for 2A output current applications. The EA8212H is designed to take into account the light load mode operation. The EA8212H has complete protection functions, including cycle-by-cycle current limit, short circuit protection, OVP, and OTP protection. The internal compensation design not only allows users to more simplified application, and can reduce the cost of external components. The EA8212H is available in the SOT-23-6 package and easy to use. FIC AN

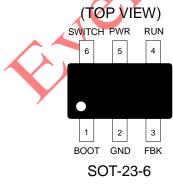
Features

- Built-in Low R_{DS(ON)} Power-MOSFETS
- Efficiency Up to 96% ►
- 4.5V to 18V Input Voltage Range
- Output Adjustable Down to 0.6V
- 2A Continuous Load Current
- Fixed 1MHz Switching Frequency
- Internal Compensation
- Cycle-by-Cycle Current Limit
- Internal Soft-Start
- Output Over Voltage Protection
- Auto Recovery Hiccup Mode Short Circuit Protection
- Auto Recovery OTP Protection
- Available in SOT-23-6 Package

Applications

- CCTV
- Security Camera
- **IP** Camera
- Distributed Power Systems

Pin Configurations



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Pin Description

Pin Name	Function Description	Pin No.
BOOT	The power input of the internal high side N-MOSFET gate driver. Connect a 33nF ceramic capacitor from BOOT pin to SWITCH pin.	1
GND	Ground pin.	2
FBK	Feedback input. Connect FBK pin and GND pin with voltage dividing resistors to set the output voltage.	3
RUN	The device turns on/turns off control input. The EA8212H on/off state can be controlled by RUN pin voltage level. Connect RUN pin to PWR pin with a 100K Ω pull up resistor for automatic startup.	4
PWR	The EA8212H power input pin. Recommended to use two 10uF MLCC capacitors between PWR pin and GND pin.	5
SWITCH	Internal MOSFET switching output. Connect SWITCH pin with a low pass filter circuit to obtain a stable DC output voltage.	6

Function Block Diagram

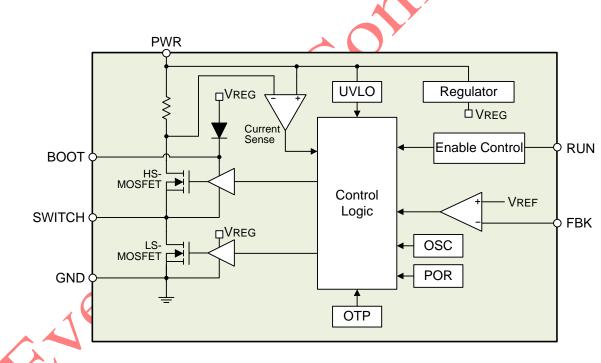


Figure 1. EA8212H internal function block diagram

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Absolute Maximum Ratings

Parameter	Value
Input Voltage (V _{PWR})	-0.3V to +20V
RUN Pin Input Voltage (V _{RUN})	-0.3V to +20V
BOOT Pin Voltage (V _{BOOT})	$V_{\text{SWITCH}}\text{-}0.3V$ to $V_{\text{SWITCH}}\text{+}5.0V$
SWITCH Pin Voltage (V _{SWITCH})	-1V,to +20V
FBK Pin Voltage (V _{FBK})	-0.3V to +6,3V
Ambient Temperature operating Range (T _A)	-40°C to +85°C
Maximum Junction Temperature (T _{Jmax})	+150°C
Lead Temperature (Soldering, 10 sec)	+260°C
Storage Temperature Range (T _s)	-65°C to +150°C

Note (1):Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. Exposure to "Absolute Maximum Ratings" conditions for extended periods may affect device reliability and lifetime.

Package Thermal Characteristics

Parameter	Value
SOT-23-6 Thermal Resistance (θ _{JC})	125°C/W
SOT-23-6 Thermal Resistance (θ _{JA})	250°C/W
SOT-23-6 Power Dissipation at T _A =25°C (P _{bmax})	0.5W
Note (1): P_{2} is calculated according to the formula: P_{2} , P_{2}	

Note (1): P_{Dmax} is calculated according to the formula: $P_{DMAX}=(T_{JMAX}-T_A)/\theta_{JA}$.

Recommended Operating Conditions

Parameter	Value		
Input Voltage (N _{PWR})	+4.5V to +18V		
RUN Pin Input Voltage (V _{RUN})	-0.3V to +18V		
Output Voltage (Vout)	+0.6V to +12V		
Junction Temperature Range (T _J)	-40°C to +125°C		

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Electrical Characteristics

 V_{PWR} =12V, T_A =25°C, unless otherwise noted

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Input Voltage	V_{PWR}		4.5		18	V
Shutdown Supply Current	I _{SD}	$V_{RUN} = 0V$		1		uA
Quiescent Current	Ι _Q	$V_{RUN} = 2V, V_{FBK} =$ 105% $V_{REF}, I_{LOAD} =$ 0A		400		uA
Output Load Current	I _{LOAD}				2	A
Reference Voltage	V_{REF}	4. 5V \leq V _{PWR} \leq 18V	0.588	0.6	0.612	V
Switching Frequency	F _{sw}		0.75	1	1.25	MHz
High Side MOSFET On-Resistance	R _{DS(ON)-HM}		. >	90		mΩ
Low Side MOSFET On-Resistance	R _{DS(ON)-LM}	\$		70		mΩ
High Side MOSFET Current Limit	I _{LIM-HM}		3	4		А
High Side MOSFET Leakage Current	I _{LEAK-HM}	V _{RUN} = 0V, V _{SWITCH} = 0V		1	10	uA
RUN Pin Input Low Voltage	V _{RUN-L}				0.4	V
RUN Pin Input High Voltage	V _{RUN-H}	6	1.8			V
Maximum Duty Cycle	D _{MAX}	$V_{FBK} = 0.5V$		92		%
High Side MOSFET Minimum On Time				60		ns
Thermal Shutdown Threshold	T _{OTP}			160		°C
Thermal Shutdown Hysteresis	T _{HYST}			30		°C

Note (1): MOSFET on-resistance specifications are guaranteed by correlation to wafer level measurements.

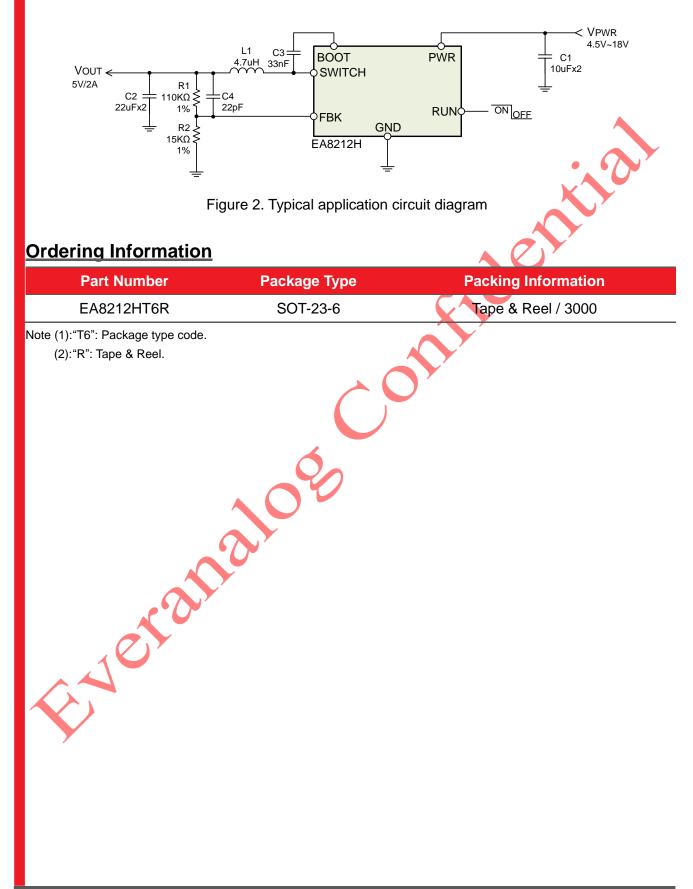
(2): Thermal shutdown specifications are guaranteed by correlation to the design and characteristics analysis.

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Application Circuit Diagram

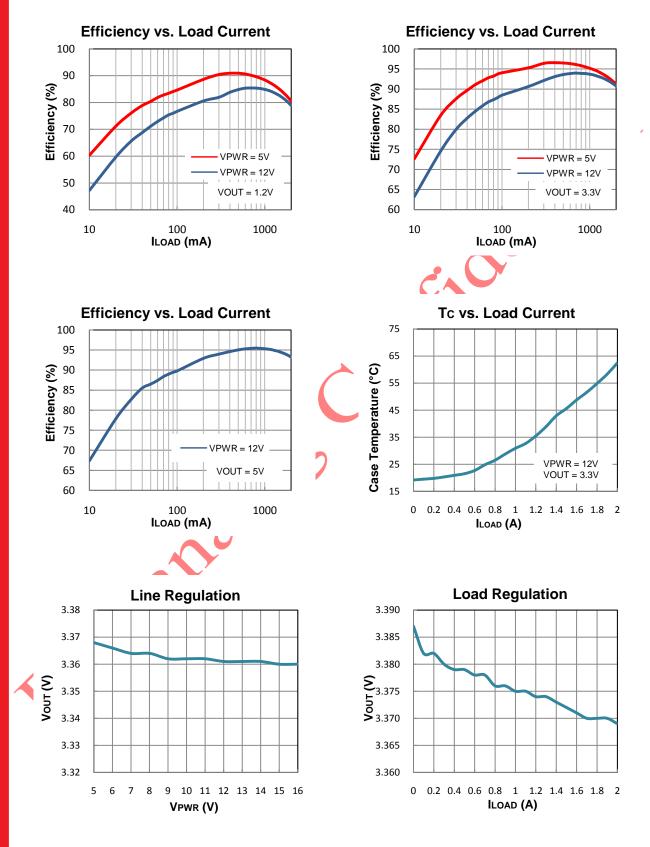


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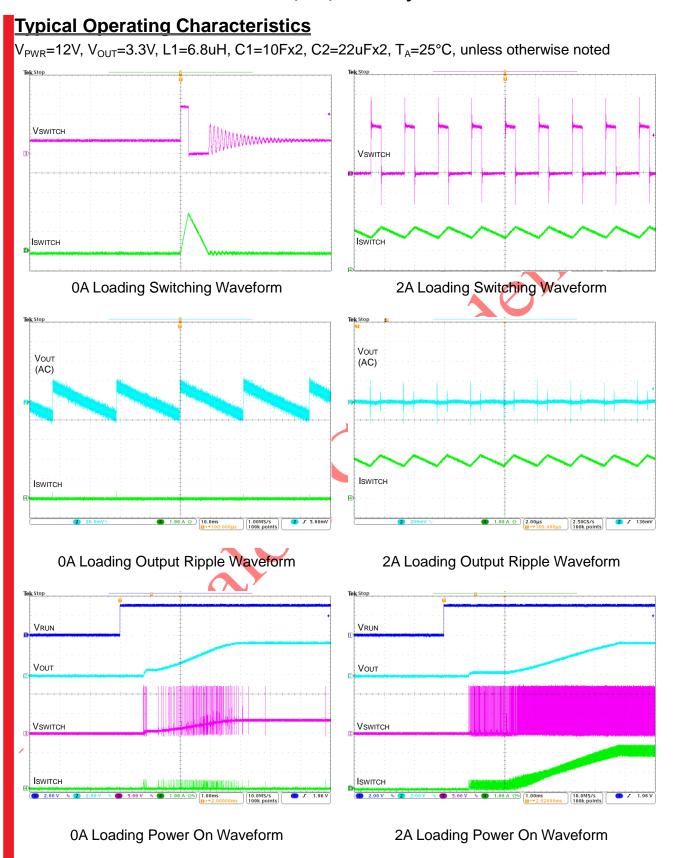
Typical Operating Characteristics

V_{PWR}=12V, V_{OUT}=3.3V, L1=6.8uH, C1=10Fx2, C2=22uFx2, T_A=25°C, unless otherwise noted



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VPWR

10u

4.5V~18V

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Application Information

Enable Control

The EA8212H use RUN pin to control the regulator turns on / turns off. When the RUN pin input voltage is higher than 1.8V, the EA8212H enters the operating mode. Drive the RUN pin input voltage lower than 0.4V to ensure the EA8212H into shutdown mode, as shown in Figure3. When the device works in the shutdown mode, the shutdown supply current is about 1uA. The EA8212H also provides automatic startup function as shown in Figure 4. Connect RUN pin and PWR pin with a 100K Ω resistor, when the PWR supply input voltage increasing and higher than RUN pin threshold voltage, the EA8212H will enter operating mode automatically.

PWR

GND

EA8212H

RUN

R3 100KO

Figure 4. Automatic startup application circuit

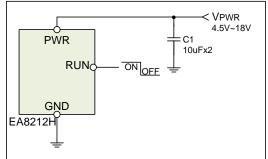


Figure 3. Enable control by RUN pin voltage

Output Voltage Setting

The EA8212H output voltage can be set via a resistor divider (R1, R2). The output voltage is calculated by following equation:

$$V_{OUT} = 0.6 \times \frac{R1}{R2} + 0.6 V$$

Taking into account the loop stability, R1 resistance value must be greater than $100K\Omega$. The following table lists common output voltage and the corresponding R1, R2 resistance value for reference.

Output Voltage	R1 Resistance	R2 Resistance	Tolerance
5V	110ΚΩ	15ΚΩ	1%
3.3V	510ΚΩ	110ΚΩ	1%
1.8V	200ΚΩ	100ΚΩ	1%
1.2V	100ΚΩ	100ΚΩ	1%

Input / Output Capacitors Selection

The input capacitors are used to suppress the noise amplitude of the input voltage and provide a stable and clean DC input to the device. Because the ceramic capacitor has low ESR characteristic, so it is suitable for input capacitor use. It is recommended to use X5R or X7R MLCC capacitors in order to have better temperature performance and smaller capacitance tolerance. In order to suppress the output voltage ripple, the MLCC capacitor is also the best choice. The suggested part numbers of input / output capacitors are as follows:

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Vendor	Part Number	Capacitance	Edc	Parameter	Size
TDK	C2012X5R1C106K	10uF	16V	X5R	0805
TDK	C3216X5R1E106K	10uF	25V	X5R	1206
TDK	C2012X5R0J226K	22uF	6.3V	X5R	0805
TDK	C3216X5R1A226M	22uF	10V	X5R	1206

Output Inductor Selection

The output inductor selection mainly depends on the amount of ripple current through the inductor ΔI_{L} . Large ΔI_{L} will cause larger output voltage ripple and loss, but the user can use a smaller inductor to save cost and space. On the contrary, the larger inductance can get smaller ΔI_{L} and thus the smaller output voltage ripple and loss. But it will increase the space and the cost. The inductor value can be calculated as:

$$L = \frac{V_{PWR} - V_{OUT}}{\Delta I_L \times F_{SW}} \times \frac{V_{OUT}}{V_{PWR}}$$

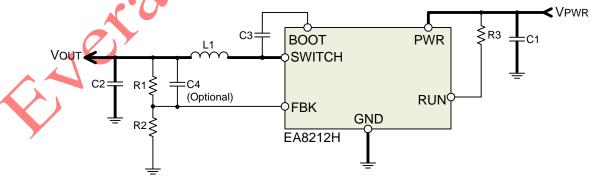
For most applications, 3.3uH to 6.8uH inductors are suitable for EA8212H. The suggested part numbers of output inductors are as follows:

Vendor	Part Number	Inductance	DCR (Max.)	Saturation Current	Dimensions (mm) (WxLxH)
SUMIDA	CDRH8D38-4R7	4.7uH	29mΩ	4A	8x8x3.8
SUMIDA	CDRH8D43R-6R8	6.8uH	29.8mΩ	4.2A	8.3x8.5x4.5

PCB Layout Recommendations

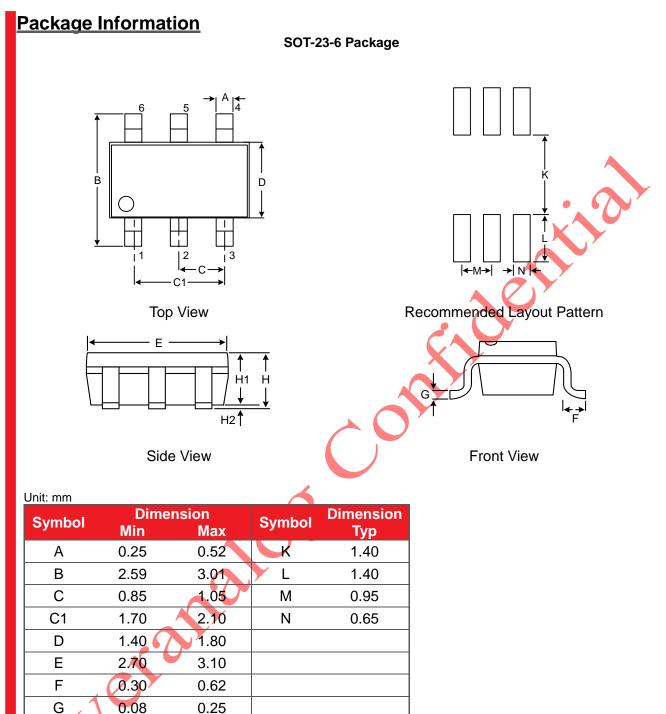
For EA8212H PCB layout considerations, please refer to the following suggestions in order to get good performance.

- High current path traces (shown as bellow) need to be widened.
- Place the input capacitors as close as possible to the PWR pin to reduce noise interference.
- ► Keep the feedback path (from V_{OUT} to FBK) away from the noise node (ex. SWITCH).
- SWITCH is a high current noise node. Complete the layout by using short and wide traces.



* Bold lines indicate high current paths

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H

H1

H2

0.89

0.89

0.00

1.35

1.20

0.15



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