DATASHEET

8 PIN DIP PHOTOTRANSISTOR PHOTOCOUPLER EL827 Series



Features:

- Current transfer ratio (CTR: 50~600% at I_F =5mA, V_{CE} =5V)
- High isolation voltage between input
- and output (Viso=5000 V rms)
- Compact small outline package
- •The product itself will remain within RoHS compliant version
- •Compliance with EU REACH
- UL and cUL approved(No. E214129)
- VDE approved (No. 132249)
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved
- CQC approved

Description

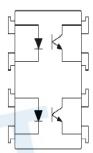
The EL827series devices each of consist of an infrared emitting diodes, optically coupled to a phototransistor detector.

They are packaged in a 8-pin DIP package and available in wide-lead spacing and SMD option.

Applications

- Programmable controllers
- System appliances, measuring instruments
- Telecommunication equipments
- Home appliances, such as fan heaters, etc.
- Signal transmission between circuits of different potentials and impedances

Schematic



Pin Configuration

- 1, 3. Anode 2, 4. Cathode
- 5, 7. Emitter
- 6, 8. Collector



Absolute Maximum Ratings (Ta=25℃)

	Parameter	Symbol	Rating	Unit
Input	Forward current	l _F	60	mA
	Peak forward current (1us, pulse)	I _{FP}	1	А
	Reverse voltage	V _R	6	V
	Power dissipation	P _D	P _D 100	
Output	Power dissipation	P _C	150	mW
	Collector current	Ι _C	50	mA
	Collector-Emitter voltage	V _{CEO}	80	V
	Emitter-Collector voltage	V _{ECO}	7	V
Total power dissipation		P _{TOT}	200	mW
Isolation voltage ^{*1}		V _{ISO}	5000	V rms
Operating temperature		T _{OPR}	-55 to 110	°C
Storage temperature		T _{STG}	-55 to 125	°C
Soldering temperature *2		T _{SOL}	260	°C

Notes:

*1 AC for 1 minute, R.H.= 40 ~ 60% R.H. In this test, pins 1, 2 & 3, 4are shorted together, and pins 5, 6 & 7, 8 are shorted together.

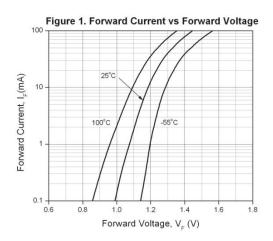
*2 For 10 seconds

Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

Input						
Parameter	Symbol	Min.	Тур.*	Max.	Unit	Condition
Forward Voltage	V _F	-	1.2	1.4	V	$I_F = 20 \text{mA}$
Reverse Current	I _R	-	-	10	μA	$V_R = 4V$
Input capacitance	C _{in}	-	30	250	pF	V = 0, f = 1kHz
Output						
Parameter	Symbol	Min.	Typ.*	Max.	Unit	Condition
Collector-Emitter dark current	I _{CEO}	-	-	100	nA	$V_{CE} = 20V, I_F = 0mA$
Collector-Emitter breakdown voltage	BV_{CEO}	80	-	-	V	$I_C = 0.1 \text{mA}$
Emitter-Collector breakdown voltage	BV_{ECO}	7	-	-	V	$I_E = 0.1 mA$
Transfer Characteristic						
Parameter	Symbol	Min	Тур.	Max.	Unit	Condition
Current Transfer ratio	CTR	50	-	600	%	$I_F = 5mA$, $V_{CE} = 5V$
Collector-Emitter saturation voltage	V _{CE(sat)}	-	0.1	0.2	V	$I_{F} = 20mA$, $I_{C} = 1mA$
Isolation resistance	R _{IO}	5×10 ¹⁰	-	-	Ω	V _{IO} = 500Vdc, 40~60% R.H.
Floating capacitance	C _{IO}	-	0.6	1.0	pF	$V_{IO} = 0$, f = 1MHz
Cut-off frequency	fc	-	80	-	kHz	$V_{CE} = 5V$, $I_C = 2mA$ $R_L = 100\Omega$, -3dB
Rise time	t _r	-	3	18	μs	$V_{CE} = 2V, I_C = 2mA,$
Fall time	t _f	-	4	18	μs	$R_L = 100\Omega$

* Typical values at $T_a = 25^{\circ}C$

Typical Electro-Optical Characteristics Curves



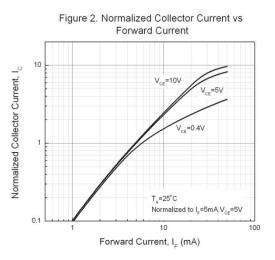
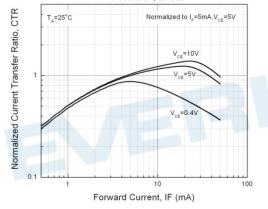


Figure 3. Normalized Current Transfer Ratio vs Forward Current



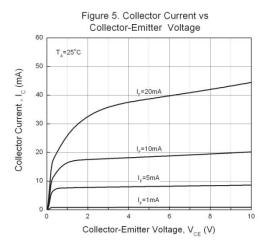
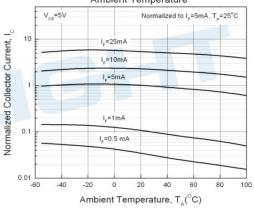
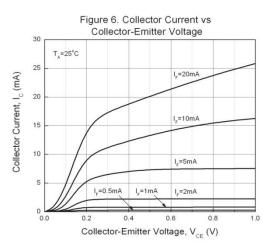


Figure 4. Normalized Collector Current vs Ambient Temperature







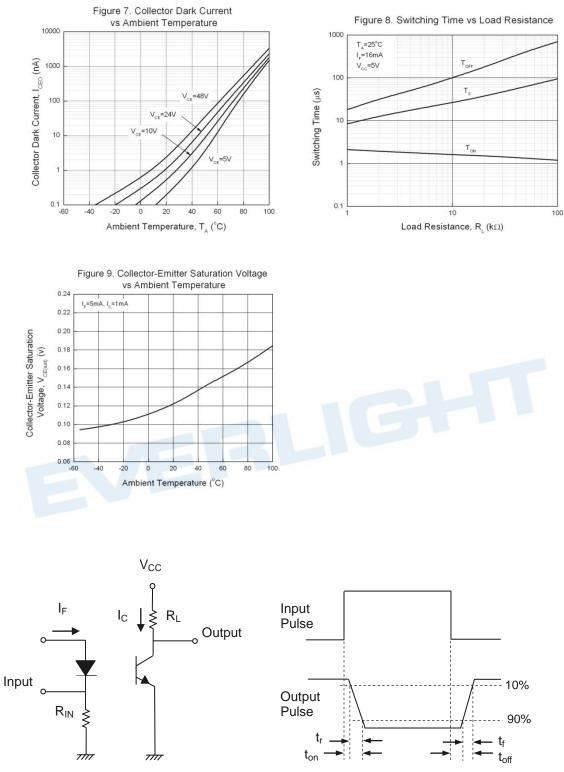


Figure 10. Switching Time Test Circuit & Waveforms

Order Information

Part Number



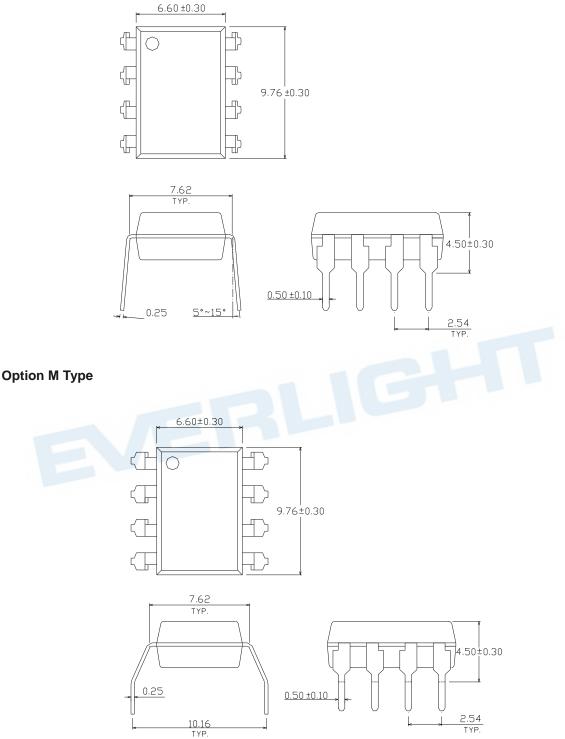
Note

- X = Lead form option (S, S1, M or none)
- Z = Tape and reel option (TA, TB or none)
- V = VDE safety (optional)

Option	Description	Packing quantity	
None	Standard DIP-8	45 units per tube	
М	Wide lead bend (0.4 inch spacing)	45 units per tube	
S (TA)	Surface mount lead form + TA tape & reel option	1000 units per reel	
S (TB)	Surface mount lead form + TB tape & reel option	1000 units per reel	
S1 (TA)	Surface mount lead form (low profile) + TA tape & reel option	1000 units per reel	
S1 (TB)	Surface mount lead form (low profile) + TB tape & reel option	1000 units per reel	

Package Dimension (Dimensions in mm)

Standard DIP Type



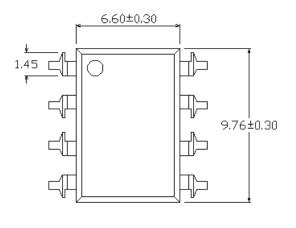


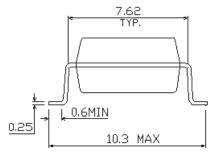
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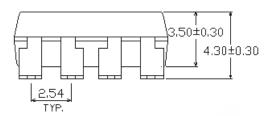
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EVERLIGHT

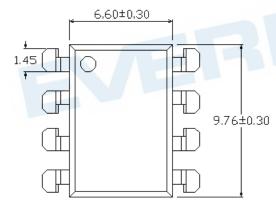
Option S Type

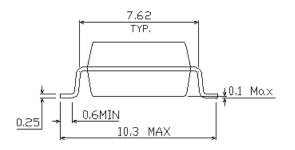


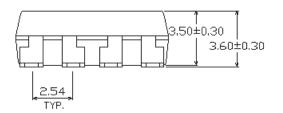




Option S1 Type



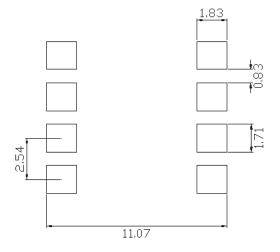




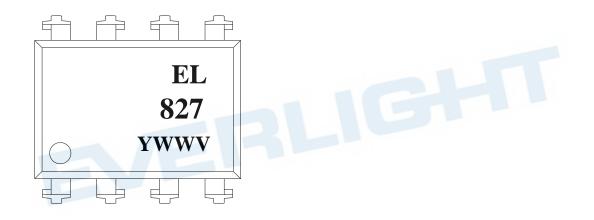
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Recommended pad layout for surface mount leadform



Device Marking



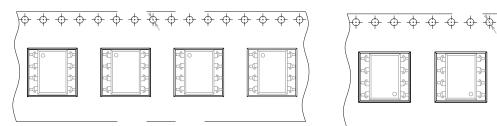
Notes

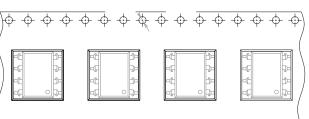
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EL827	denotes Device Number
Y	denotes 1 digit Year code
WW	denotes 2 digit Week code
V	denotes VDE (optional)

Tape & Reel Packing Specifications







Direction of feed from reel

Γ

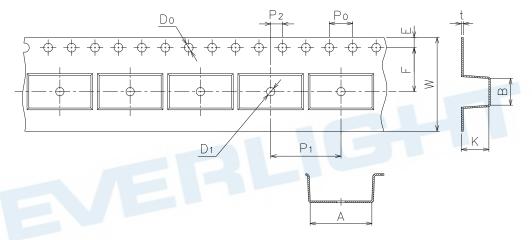


Direction of feed from reel

Option TB



Tape dimensions



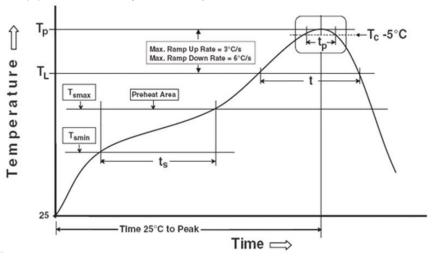
Dimension No.	А	В	Do	D1	E	F
Dimension(mm)	10.4±0.1	10.0±0.1	1.5±0.1	1.5+0.25 -0.1	1.75±0.1	7.5±0.1
Dimension No.	Ро	P1	P2	t	w	к
Dimension(mm)	4.0±0.1	12.0±0.1	2.0±0.1	0.4±0.1	16.0±0.3	4.5±0.1



Precautions for Use

1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

Preheat

Temperature min (T_{smin})

Temperature max (T_{smax})

Time (T_{smin} to T_{smax}) (t_s) Average ramp-up rate (T_{smax} to T_p)

Other

Liquidus Temperature (T_L) Time above Liquidus Temperature (t_L) Peak Temperature (T_P) Time within 5 °C of Actual Peak Temperature: T_P - 5°C Ramp- Down Rate from Peak Temperature Time 25°C to peak temperature Reflow times Reference: IPC/JEDEC J-STD-020D

150 °C 200°C 60-120 seconds 3 °C/second max

217 °C 60-100 sec 260°C 30 s 6°C /second max. 8 minutes max. 3 times

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