

DATASHEET

Technical Data sheet 5mm Phototransistor T-1 PT333-3B(BY)



Features

- .Fast response time
- .High photo sensitivity
- .Pb free
- .The product itself will remain within RoHS compliant version.
- .Compliance with EU REACH
- .Compliance Halogen Free .(Br <900 ppm ,Cl <900 ppm , Br+Cl < 1500 ppm).

Description

.PT333-3B(BY) is a high speed and high sensitive NPN silicon NPN epitaxial planar phototransistor molded in a standard 5 mm package. Due to its Black epoxy the device is sensitive to infrared radiation.

Applications

- . Infrared applied system
- . Camera
- . Cockroach catcher



Device Selection Guide

LED Part No.	Chip Materials	Lens Color	
PT333-3B(WW)	Silicon	Black	

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Collector-Emitter Voltage	VCEO	30	V
Emitter-Collector-Voltage	V _{ECO}	5	V
Collector Current	Ic	20	mA
Operating Temperature	Topr	-25~+85	°C
Storage Temperature	T _{stg}	-40~ +100	°C
Lead Soldering Temperature	Tsol	260 ℃	°C
Power Dissipation at (or below)	Pc	75	mW
25°CFree Air Temperature			

Notes: *1:Soldering time ≤ 5 seconds.

2.Tsol is the temperature of the tin stove



Electro-Optical Characteristics (Ta=25°ℂ)

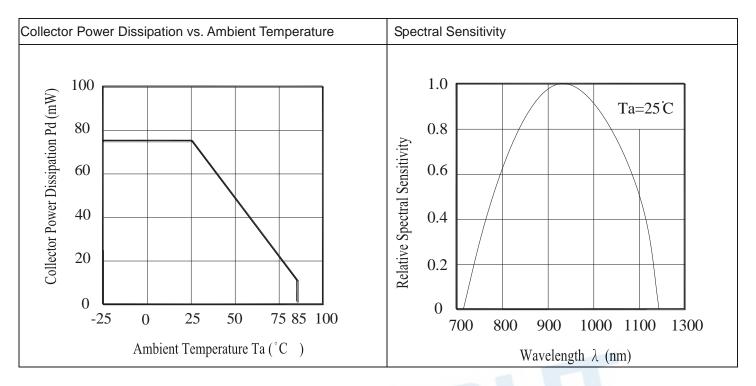
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Collector – Emitter Breakdown Voltage	BV _{CEO}	30			V	Ic=100μA Ee=0mW/cm ²
Emitter-Collector Breakdown Voltage	BV _{ECO}	5			V	I _E =100μA Ee=0mW/cm ²
Collector-Emitter Saturation Voltage	V _{CE(sat)}			0.4	V	I _C =2mA Ee=1mW/cm ²
Rise Time	t _r		15			V _{CE} =5V
Fall Time	t _f		15		μS	I _C =1mA RL=1000Ω
Collector Dark Current	ICEO			100	nA	Ee=0mW/cm ² V _{CE} =20V
On State Collector Current	I _{C(on)}	2.67		8.48	mA	Ee=1mW/cm ² V _{CE} =5V λp=940nm
Rang Of Spectral Bandwidth	λ _{0.5}	840		1100	nm	
Wavelength of Peak Sensitivity	λР	3	940		nm	

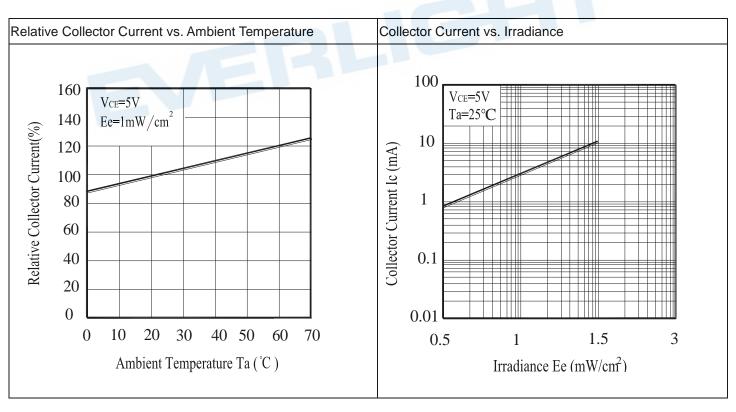
Note: Ic(on) parameter tolerance is $\pm 10\%$

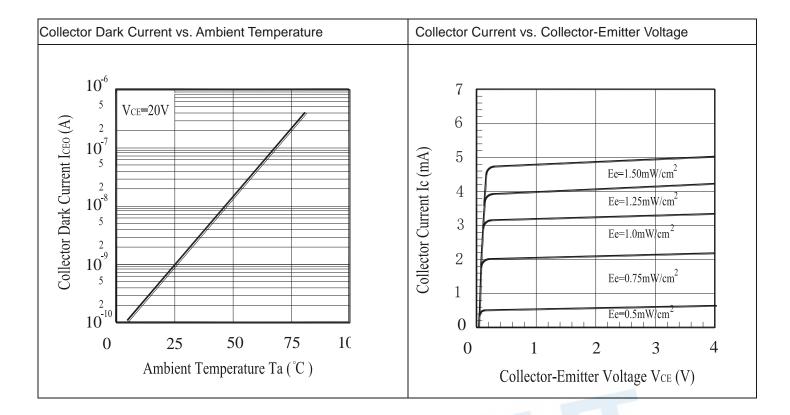
Parameter	Symbol	Min	Max	Unit	Test Condition
K	Ic(ON)	2.67	5.07	mA	
L	Ic(ON)	4.18	7.07	mA	VcE=5V Ee=1mW/cm ²
M	Ic(on)	5.68	8.48	mA	LC—IIII W/CIII



Typical Electro-Optical Characteristics Curves

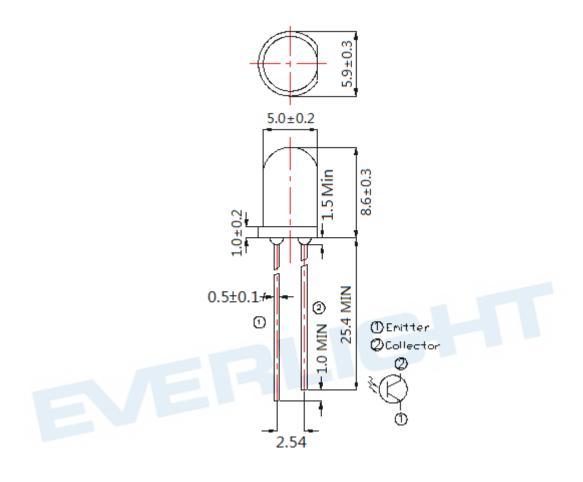








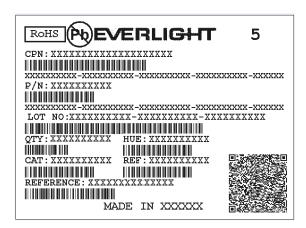
Package Dimension



Note: Tolerances unless dimensions ±0.25mm

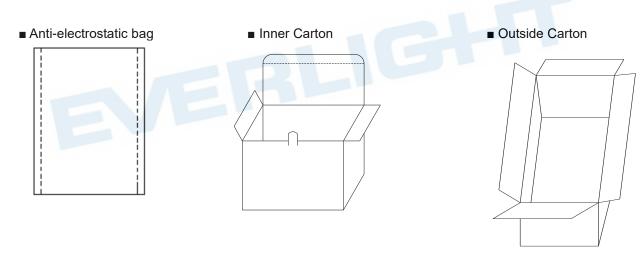


Label Explanation



- · CPN: Customer's Product Number
- · P/N: Product Number
- · QTY: Packing Quantity
- · CAT: Luminous Intensity Rank
- · HUE: Dom. Wavelength Rank
- · REF: Forward Voltage Rank
- · LOT No: Lot Number
- · X: Month
- · Reference: Identify Label Number

Packing Specification



- Packing Quantity
- 1. 200~500 PCS/1 Bag, 5 Bags/1 Inner Carton
- 2. 10 Inner Cartons/1 Outside Carton



Notes

1. Lead Forming

- During lead formation, the leads should be bent at a point at least 3mm from the base of the epoxy bulb.
- Lead forming should be done before soldering.
- Avoid stressing the PHOTOTRANSISTOR package during leads forming. The stress to the base may damage the PHOTOTRANSISTOR's characteristics or it may break the PHOTOTRANSISTORs.
- Cut the PHOTOTRANSISTOR lead frames at room temperature. Cutting the lead frames at high temperatures may cause failure of the PHOTOTRANSISTORs.
- When mounting the PHOTOTRANSISTORs onto a PCB, the PCB holes must be aligned exactly with the lead position of the PHOTOTRANSISTOR. If the PHOTOTRANSISTORs are mounted with stress at the leads, it causes deterioration of the epoxy resin and this will degrade the PHOTOTRANSISTORs.

2. Storage

- The PHOTOTRANSISTORs should be stored at 30°C or less and 70%RH or less after being shipped from Everlight and the storage life limits are 3 months. If the PHOTOTRANSISTORs are stored for 3 months or more, they can be stored for a year in a seaPhototransistor container with a nitrogen atmosphere and moisture absorbent material. After opening the PHOTOTRANSISTORs should be used up within 24 hours.
- Please avoid rapid transitions in ambient temperature, especially, in high humidity environments where condensation can occur.

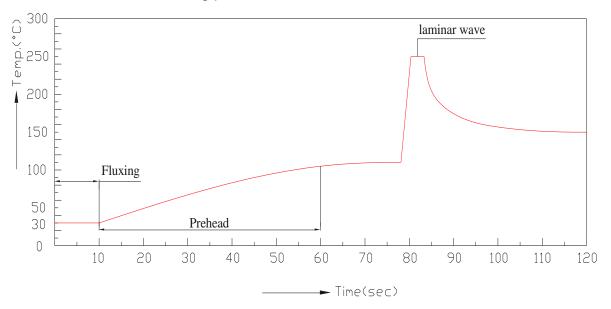
3. Soldering

- Careful attention should be paid during soldering. When soldering, leave more then 3mm from solder joint to epoxy bulb, and soldering beyond the base of the tie bar is recommended.
- Recommended soldering conditions:

Hand Soldering		DIP Soldering		
Temp. at tip of iron	300°C Max. (30W	Preheat temp.	100°C Max. (60 sec	
Temp. at tip of from	Max.)	i rondat tomp.	Max.)	
Soldering time	3 sec Max.	Bath temp. & time	260 Max., 5 sec	
Soldering time		Dain temp. & time	Max	
Distance	3mm Min.(From	Distance	3mm Min. (From	
solder joint to			solder joint to epoxy	
	epoxy bulb)		bulb)	



Recommended soldering profile



- Avoiding applying any stress to the lead frame while the PHOTOTRANSISTORs are at high temperature particularly when soldering.
- Dip and hand soldering should not be done more than one time
- After soldering the PHOTOTRANSISTORs, the epoxy bulb should be protected from mechanical shock or vibration until the PHOTOTRANSISTORs return to room temperature.
- A rapid-rate process is not recommended for cooling the PHOTOTRANSISTORs down from the peak temperature.

Although the recommended soldering conditions are specified in the above table, dip or hand soldering at the lowest

possible temperature is desirable for the PHOTOTRANSISTORs.

Wave soldering parameter must be set and maintain according to recommended temperature and dwell time in the solder wave.

4. Cleaning

- When necessary, cleaning should occur only with isopropyl alcohol at room temperature for a duration of no more than one minute. Dry at room temperature before use.
- Do not clean the PHOTOTRANSISTORs by the ultrasonic. When it is absolutely necessary, the influence of ultrasonic cleaning on the PHOTOTRANSISTORs depends on factors such as ultrasonic power and the assembPhototransistor condition. Ultrasonic cleaning shall be pre-qualified to ensure this will not cause damage to the PHOTOTRANSISTOR



5. Heat Management

- Heat management of PHOTOTRANSISTORs must be taken into consideration during the design stage of PHOTOTRANSISTOR application. The current should be de-rated appropriately by referring to the de-rating curve found in each product specification.
- The temperature surrounding the PHOTOTRANSISTOR in the application should be controlPhototransistor. Please refer to the data sheet de-rating curve.

6. ESD (Electrostatic Discharge)

- Electrostatic discharge (ESD) or surge current (EOS) can damage PHOTOTRANSISTORs.
- An ESD wrist strap, ESD shoe strap or antistatic gloves must be worn whenever handling PHOTOTRANSISTORs.
- All devices, equipment and machinery must be properly grounded.
- Use ion blower to neutralize the static charge which might have built up on surface of the PHOTOTRANSISTORs plastic lens as a result of friction between PHOTOTRANSISTORs during storage and handing.

DISCLAIMER

- 1. EVERLIGHT reserves the right(s) on the adjustment of product material mix for the specification.
- 2. The product meets EVERLIGHT published specification for a period of twelve (12) months from date of shipment.
- 3. The graphs shown in this datasheet are representing typical data only and do not show guaranteed values.
- 4. When using this product, please observe the absolute maximum ratings and the instructions for using outlined in these specification sheets. EVERLIGHT assumes no responsibility for any damage resulting from the use of the product which does not comply with the absolute maximum ratings and the instructions included in these specification sheets.
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- 6. This product is not intended to be used for military, aircraft, automotive, medical, life sustaining or life saving applications or any other application which can result in human injury or death. Please contact authorized Everlight sales agent for special application request.

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