FD2H003BY

## Low-Power Hall Switch

## General Description

Halogen

FD2H003B*-G1 is a low-power integrated Hall switch designed to sense the applied magnetic flux density and give a digital output, which indicates the present condition of the magnitude sensed. One example of the applications is the on/off switch in cellular flip-phones.

The micro power design is especially suitable for battery-operated systems such as cellular phones or laptop computers, in which power consumption is one major concern. The typical power consumption of FD2H003B*-G1 is below $10 \mu \mathrm{~W}$ at 2.7 V .

The magnetic switching points are precise and insensitive to process and temperature variations.
For FD2H003B*-G1, the output will be at the "high" level when no magnetic field is applied. When the applied magnetic flux density is stronger than the switching threshold, the output would be at the "low" level.

## Features

> Micro power consumption
> 1.8 V to 5.5 V battery operation
> Chopper Amplifier based design: Insensitive to noise and offset caused by process variations, operating temperatures and mechanical stress Digital output
> CMOS process
> CMOS output stage : no external pull-up resistor needed

## Pin Descriptions

FD2H003BYR-G1


| Name | I/O | Description |
| :---: | :---: | :--- |
| VDD | P | Positive supply |
| Q | O | Open Drain output |
| VSS | G | Ground |

Legend: I=input, O=output, I/O=input/output, P=power supply, G=ground

FD2H003BaR-G1

VSS
3


## FD2H003BH-G1



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## Marking Information



Halogen Free: Halogen free product indicator
Lot Number: Wafer lot number's last two digits


Internal ID: Internal Identification Code
Per-Half Month: Production period indicator in half month time unit
For Example : A $\rightarrow$ First Half Month of January
B $\rightarrow$ Second Half Month of January
C $\rightarrow$ First Half Month of February
D $\rightarrow$ Second Half Month of February
Year: Production year's last digit
Part Number Code: Part number identification code for this product.

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## Block Diagram



Figure. 1

## Functional Descriptions

Refer to the block diagram (Figure.1), FD2H003-LF is composed of the following building blocks:

## - Bias Generator

The bias generator provides precise, temperature and process insensitive current sources for both the Hall plate and the chopper amplifier. These current sources in turn guarantee proper operation of the chip and precise switching thresholds under all kinds of environments specified in the specification.

## - Oscillator + Timing logic

The built-in oscillator provides the clock signal, which is taken by the timing logic to determine the periods of the operating phase and the stand-by phase. Typically the operating time is about 60us and the stand-by time is 150 ms . Using such a clocking scheme, the average power consumption is almost equal to that in the stand-by phase, which is under $10 \mu \mathrm{~W}$ at 2.7 V .

## - Chopper Amplifier

To achieve a higher resolution the chopper amplifier structure is adopted in this design. Use of this structure dynamically removes both the offset and flicker noise at the same time.

## - Comparator with Hysteresis Control

This block determines the switching threshold of the Hall switch in different situations.

## Ordering information

| Part Number | Operating <br> Temperature | Part Number <br> Code | Package | Description | MOQ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| FD2H003BYR-G1 | $-20^{\circ} \mathrm{C}$ to $+105^{\circ} \mathrm{C}$ | CE | SOT23 | $\pm 25 \mathrm{G}(\mathrm{B})$ | $3000 \mathrm{ea} /$ Reel |
| FD2H003BaR-G1 | $-20^{\circ} \mathrm{C}$ to $+105^{\circ} \mathrm{C}$ | CE | TSOT23 | $\pm 25 \mathrm{G}(\mathrm{B})$ | $3000 \mathrm{ea} /$ Reel |
| FD2H003BH-G1 | $-20^{\circ} \mathrm{C}$ to $+105^{\circ} \mathrm{C}$ | - | SIP3 | $\pm 25 \mathrm{G}(\mathrm{B})$ | $1000 \mathrm{ea} / \mathrm{Bag}$ |

Absolute Maximum Ratings

| Parameter | Conditions | Min. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Ambient Operating Temperature | - | -40 | 85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | - | -40 | 150 | ${ }^{\circ} \mathrm{C}$ |
| DC Supply Voltage | - | 1.8 | 5.5 | V |
| Supply Current | - | -1 | 2.5 | mA |
| Programming Pin Voltage <br> (only available for FD2H003B*-G1) | With respect to <br> VSS | -0.3 | 5.5 | V |
| Magnetic Flux Density | - |  | unlimited | G |
| Lead Temperature | 10 sec | - | 260 | ${ }^{\circ} \mathrm{C}$ |

## IR Re-flow Soldering Curve



Figure. 2

## Operating Conditions

| Parameter | Conditions | Min. | Typ. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Supply Voltage | - | 1.8 | 2.7 | 5.5 | V |
| Output Voltage | - | -0.3 | 2.7 | 5.5 | V |
| Ambient Temperature | - | -40 | 25 | 85 | ${ }^{\circ} \mathrm{C}$ |

## Electrical Characteristics

| Parameter | Conditions | Min. | Typ. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Average Supply Current | - | - | $3^{1}$ | 20 | $\mu \mathrm{~A}$ |
| Average Supply Current <br> (operating phase) | - | - | $1.1^{1}$ | - | mA |
| Average Supply Current <br> (stand-by phase) | - | - | $2.5^{1}$ | - | $\mu \mathrm{A}$ |
| Output High Voltage(VOH) | $\mathrm{Io}=-0.5 \mathrm{~mA}$ | $\mathrm{VDD}-0.4 \mathrm{~V}$ | - |  | V |
| Output Low Voltage(VOL) | $\mathrm{Io}=+0.5 \mathrm{~mA}$ | - | - | 0.4 V | V |
| Output Leakage Current | - | - | 0.01 | - | $\mu \mathrm{A}$ |
| Operating time | - | - | 60 | - | $\mu \mathrm{s}$ |
| Standby time | - | - | 150 | - | ms |
| Duty cycle | - | - | 0.04 | - | $\%$ |

1. operating voltage 2.7 V

## Magnetic Characteristics

| Parameter | Conditions | Min. | Typ. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Operate Points (\|Bop $\mid$ ) | - | 15 | 25 | 35 | G |
| Hysteresis | - | 5 | 10 | 15 | G |

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## Magnetic Flux



Figure 3. FD2H003B*-G1

## Typical Characteristics



Figure. 4 Magnetic Switch Points Versus Ambient Temperature (VDD=2.7V)


Figure. 5 Magnetic Switch Points Versus Supply Voltage (Ta=25C degree)

[^2]

Figure. 6 Average Current Versus Supply Voltage ( $\mathrm{Ta}=25 \mathrm{C}$ degree)


Figure. 7 Average Current Versus Ambient Temperature (VDD=2.7V )

## Application Circuit Reference



Figure. 7
NOTE:

1. R1 is for power supply filtering function, and must be placed as close to IC1 as possible.

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## Package Outline

## SOT23( FD2H003BYR-G1)



Unit: mm

| Symbols | Min. (mm) | Nom. (mm) | Max. (mm) |
| :---: | :---: | :---: | :---: |
| A | - | - | 1.45 |
| A1 | - | - | 0.15 |
| A2 | 0.90 | 1.15 | 1.30 |
| b | 0.30 | - | 0.50 |
| c | 0.08 | - | 0.22 |
| D |  | 2.90 BSC. |  |
| E |  | 2.80 BSC |  |
| E1 |  | 1.60 BSC |  |
| e |  | 0.95 BSC |  |
| e1 |  | 1.90 BSC |  |
| L | 0.30 | 0.45 | 0.60 |
| L1 |  | 0.60 REF. |  |
| L2 |  | 0.25 BSC |  |
| R | 0.10 | - | - |
| R1 | 0.10 | - | 0.25 |
| $\theta^{\circ}$ | $0^{\circ}$ | $4^{\circ}$ | $8^{\circ}$ |
| $\theta 1^{\circ}$ | $5^{\circ}$ | $10^{\circ}$ | $15^{\circ}$ |

## TSOT-23L( FD2H003BaR-G1)



Unit: mm

| Symbols | Dimension In Millimeters |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Min | Nom | Max |  |
| A | 0.750 | - | 0.800 |  |
| A1 | 0.025 | - | 0.050 |  |
| A2 | 0.700 | 0.750 | 0.775 |  |
| b | 0.350 | - | 0.500 |  |
| c | 0.100 | - | 0.200 |  |
| D | 2.800 | 2.900 | 3.000 |  |
| E | 2.600 | 2.800 | 3.000 |  |
| E1 | 1.500 | 1.600 | 1.700 |  |
| e | 0.950 BSC |  |  |  |
| e1 | 1.900 BSC |  |  |  |
| L | 0.370 | 0.450 | 0.600 |  |
| L1 | 0.600 REF |  |  |  |
| L2 | 0.250 BSC |  |  |  |
| R | 0.100 | - | - |  |
| R1 | $0 . .100$ | - | 0.250 |  |
| $\Theta$ | $0^{\circ}$ | $4^{\circ}$ | $8^{\circ}$ |  |
| O1 | $4^{\circ}$ | $10^{\circ}$ | $12^{\circ}$ |  |

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## SIP-3L (FD2H003BH-G1)



Unit: mm

| Symbols | Min. (mm) | Nom. (mm) | Max. (mm) |  |
| :---: | :---: | :---: | :---: | :---: |
| A | 1.245 | - | 1.753 |  |
| A1 |  |  |  |  |
| b | 0.33 | $0.75 R E F$ | - |  |
| b1 |  | 0.54 REF |  |  |
| D | 3.85 | - | 4.2 |  |
| E | 2.87 | - | 3.124 |  |
| L | 13.5 | - | 15.6 |  |
| e |  |  |  |  |

## Note:

1. Dimension " D " does not include molding flash, protrusions or gate burrs.
2. Dimension " $E$ " does not include interlead flash, protrusions.

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