1A LED Driver with Internal Switch

Halogen Free

General Description

The FP7152 is a continuous current mode inductive step down converter. It can drive single or multiple series connected LEDs. The FP7152 includes the output switch and a high-side output current sensing circuit, which use an external resistor to set the average output current. Output current can also be adjusted by applying an external signal to the 'ADJ' pin. The ADJ pin accept either a DC voltage or a PWM dimming waveform. The PWM dimming filter components are contained within the chip. Applying a voltage under 0.2V to the ADJ pin will turn off the output.

The FP7152 is available in the SOT89-5L package, Its space-saving footprint occupys small PCB area for miscellaneous application fields.

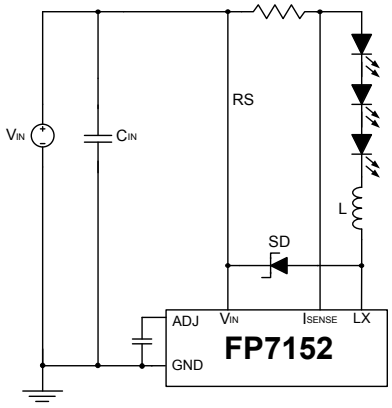
Features

- 1A Output Current
- Internal 0.5Ω 26V Power MOSFET Switch
- Wide 7 to 26V Operating Input Range
- 20μA Shutdown Mode Current
- Typical 4% Output Current Accuracy
- Signal pin ON / OFF and Brightness Control
- Adjustable Soft-Start
- Up to 95% Efficiency
- Up to 1MHz Switching Frequency
- Internal Dimming Filter
- Package: SOT89-5L

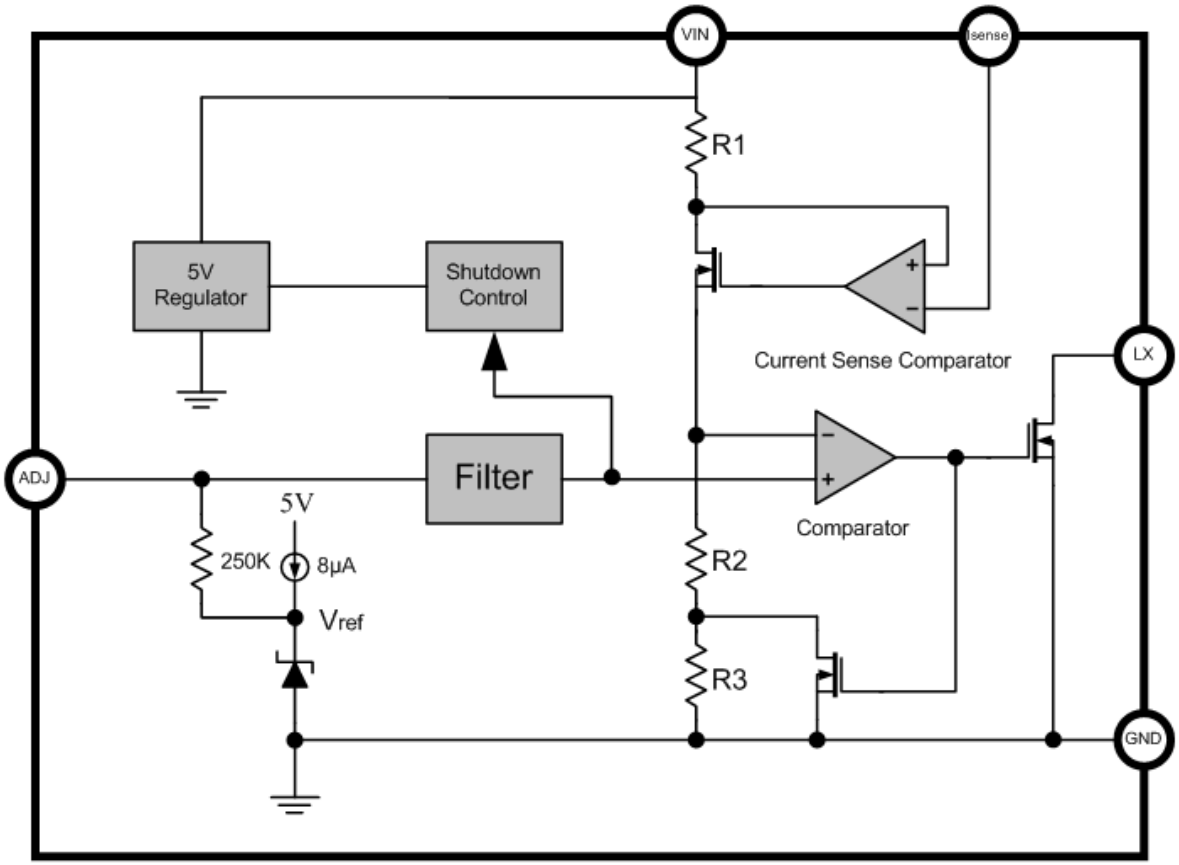
Applications

- Low Voltage Halogen replacement LEDs
- LED back-up lighting

Typical Application Circuit

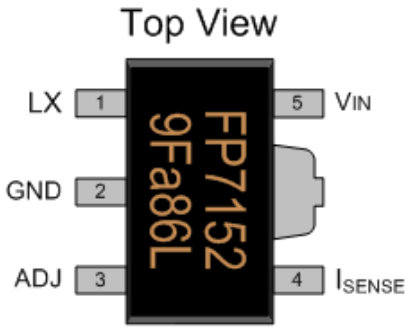


Function Block Diagram



Pin Descriptions

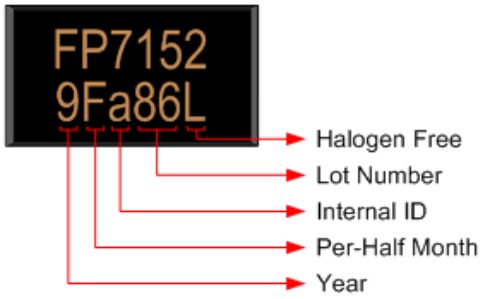
SOT89-5L



Name	No.	I / O	Description
LX	1	O	Power Switch Output
GND	2	P	IC Ground
ADJ	3	I	Multi Function ON / OFF& Brightness Control
I_{SENSE}	4	I	Current Sense Resistor Connected
V_{IN}	5	P	IC Power Supply

Marking Information

SOT89-5L



Halogen Free: Halogen free product indicator

Lot Number: Wafer lot number's last two digits

For Example: 132386TB → 86

Internal ID: Internal Identification Code

Per-Half Month: Production period indicated in half month time unit

For Example: A → First Half Month of January

B → Last Half Month of January

C → First Half Month of February

D → Last Half Month of February

Year: Production year's last digit

Ordering Information

Part Number	Operating Temperature	Package	MOQ	Description
FP7152AR-G1	-40℃ ~ 85℃	SOT89-5L	1000EA	Tape & Reel

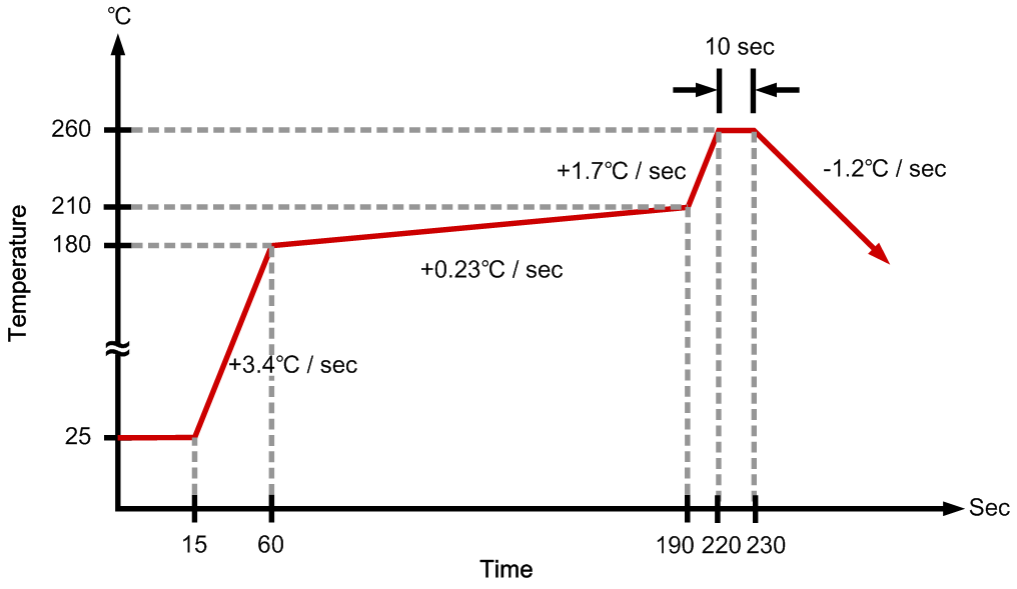
Absolute Maximum Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply Voltage	V_{IN}		-0.3		26	V
I_{SENSE} Voltage	V_{SENSE}	Measured with Respect to V_{IN}	+0.3		-5	V
LX Input Voltage	V_{LX}		-0.3		26	V
Adjust Pin Input Voltage	V_{ADJ}		-0.3		6	V
Power Dissipation	P_D	SOT89-5L @T_A=25℃			800	mW
Thermal Resistance (Note1)	θ_{JA}	SOT89-5L			+156	℃ / W
Junction Temperature	T_J				+150	℃
Operating Temperature	T_{OP}		-40		+85	℃
Storage Temperature	T_{ST}		-65		+150	℃
Lead Temperature		(soldering, 10 sec)			+260	℃

Note1:

θ_{JA} is measured in the natural convection at T_A=25°C on a low effective thermal conductivity test board of JEDEC 51-3 thermal measurement standard.

IR Re-flow Soldering Curve



DC Electrical Characteristics V_{IN}=12V, T_A=25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input voltage	V_{IN}		7		26	V
Internal Regulator Start-up Threshold	V_{SU}	V_{IN} Rising		4.8		V
Quiescent Supply Current with Output off	I_{INQoff}	ADJ pin Grounded		15	20	μA
Quiescent Supply Current with Output Switching	I_{INQon}	ADJ pin Floating f=250kHz		400	800	μA
Mean Current Sense Threshold Voltage (Defines LED Current Setting Accuracy)	V_{SENSE}	Measured on I_{SENSE} pin with Respect to V_{IN} V_{ADJ} =1.25V	95	100	105	mV
Sense Threshold Hysteresis	$V_{SENSEHYS}$			±15		%
I_{SENSE} Pin Input Current	I_{SENSE}	V_{SENSE} =V_{IN} -0.1		3	10	μA
Internal Reference Voltage	V_{REF}	Measured on ADJ pin with pin Floating	1.21	1.25	1.29	V
Temperature Coefficient of V_{REF}	$\triangle V_{REF} / \triangle T$			50		ppm/°C
External Control Voltage Range on ADJ Pin for DC Brightness Control (1)	V_{ADJ}		0.3		2.5	V
ADJ Pin Enable High Voltage	V_{ADJ_ON}	V_{ADJ} Rising	0.2	0.25	0.3	V
ADJ Pin Enable Hysteresis	V_{ADJ_HYS}	V_{ADJ} Falling	20	50	80	mV
Resistance Between ADJ Pin and V_{REF}	R_{ADJ}		135		250	kΩ
Continuous LX Switch Current	I_{LXmean}			1.0	1.2	A
LX Switch 'On' Resistance	R_{LX}			0.5	0.7	Ω
LX Switch Leakage Current	$I_{LX (leak)}$				1	μA

DC Electrical Characteristics (Cont.) V_{IN}=12V, T_A=25℃ (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Duty Cycle Range of PWM Signal Applied to ADJ Pin During Low Frequency PWM Dimming Mode	$D_{PWM\ (LF)}$	PWM Frequency <500Hz PWM Amplitude= V_{REF} Measured on ADJ pin	0.01		1	
Brightness Control Range				100:1		
Duty Cycle Range of PWM Signal Applied to ADJ Pin During High Frequency PWM Dimming Mode	$D_{PWM\ (HF)}$	PWM Frequency >10kHz PWM Amplitude= V_{REF} Measured on ADJ Pin	0.16		1	
Brightness Control Range				5 :1		
Soft Start Time	T_{SS}	Time Taken for Output Current to Reach 90% of Final Value after Voltage on ADJ pin Has Risen Above 0.3V		500		μs
Operating Frequency	f_{LX}	ADJ Pin Floating L=100μH (0.82Ω) I_{OUT}=700mA @ V_{LED}=3.4V Driving 1 LED		250		KHz
Minimum Switch 'ON' Time	T_{ONmin}	LX switch 'ON'	200			ns
Minimum Switch 'OFF' Time	T_{OFFmin}	LX switch 'OFF'	200			ns
Recommended Maximum Operating Frequency	f_{LXmax}				1	MHz
Recommended Duty Cycle Range of Output Switch at $f_{Lx\ max}$	D_{LX}		0.3		0.7	
Internal Comparator Propagation Delay	T_{PD}			50		ns

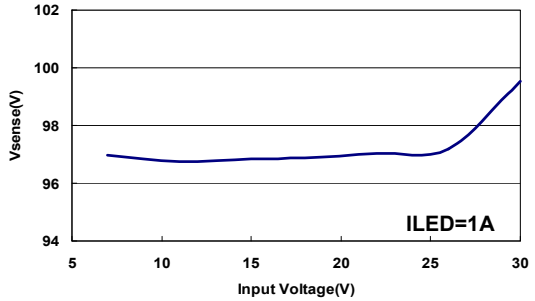
Notes：

1. 100% brightness corresponds to V_{ADJ} = V_{ADJ} (nom) = V_{REF}. Driving the ADJ pin above V_{REF} will increase the V_{SENSE} threshold and output current proportionally.

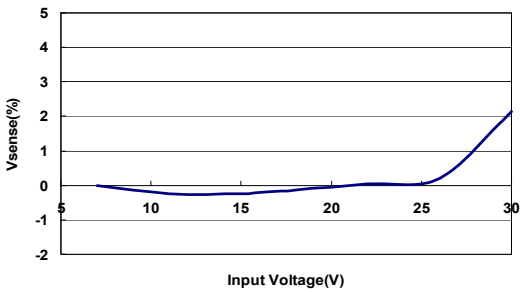
Typical Operating Characteristics

(V_{CC}=12V,Single LED,T_A= 25°C,L=100uH, unless otherwise noted)

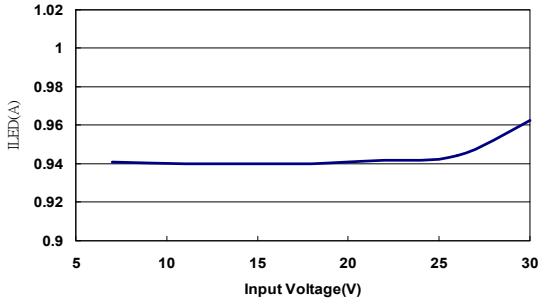
Input Voltage vs. Vsense Voltage



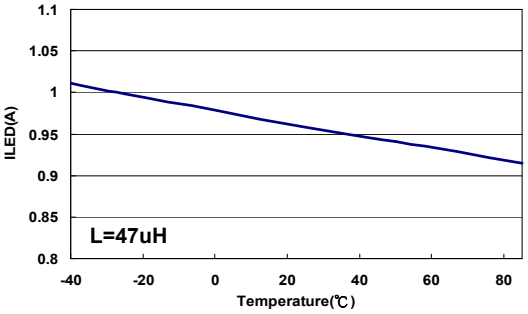
Input Voltage vs. Vsense Voltage



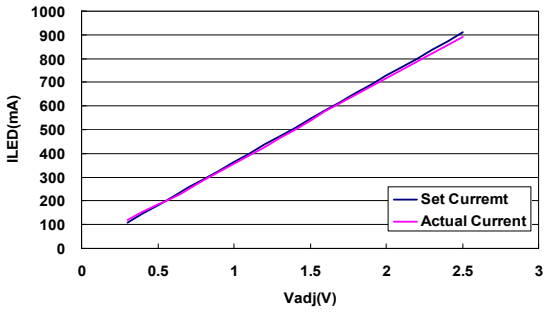
Input Voltage vs. ILED



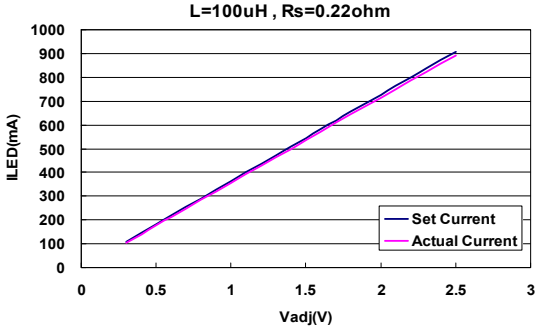
Temperature vs. ILED



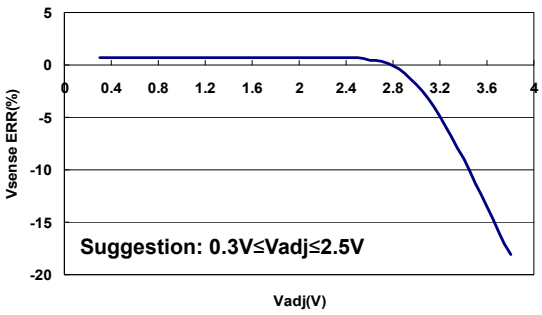
Vadj vs. ILED
L=47uH , Rs=0.22ohm



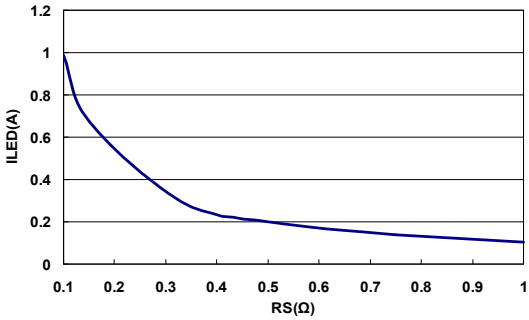
Vadj vs. ILED
L=100uH , Rs=0.22ohm



Vadj vs. Vsense ERR



Rs vs. ILED



Function Description

Operation

The FP7152 is a continuous mode inductive step-down converter that is easy to be configured in varies applications ranging from 7V~26V input. The converter employs a high side current sensing resistor RS to detect and regulate the LED current. The voltage across the current sensing resistor is kept measured and regulated in 100mV±15mV range.

The internal 1.25V reference voltage is utilized to provide a 0.25V reference for enabling the part and a 1.25V pulling-up voltage as current reference voltage. When V_{ADJ}>0.25V the output of the comparator becomes high and the other blocks are enabled.

When input voltage VIN is first applied, the initial current in L1 and RS is zero and there is no output from the current sense circuit. Under this condition, the output of comparator is high. This turns on an internal switch and switches the SW pin low, causing current to flow from VIN to ground, via RS, L1 and the LED(s). The current rises at a rate determined by VIN and L1 to produce a voltage ramp across RS. When (V_{IN} - V_{SENSE}) > 115mV, the output of the comparator switches low and turns the LX pin output to high impedance state. Then the current flowing on the RS decreases at another rate. When (V_{IN}- V_{SENSE}) < 85mV, the LX switch turns on again and the mean current on the LED is 100mV/R_S.

Adjusting output current

The device contains a low pass filter between the ADJ pin and the threshold comparator. An internal current limiting resistor (250k nom.) is placed between ADJ and the internal reference voltage. This allows the ADJ pin to be overdriven with either DC or pulse signals to change the V_{SENSE} switching threshold and adjust the output current.

Output Shutdown

The output of the low pass filter drives the shutdown circuit. When the input voltage to this circuit falls below the threshold (0.2V nom.), the internal regulator and the output switch are turned off. The voltage reference remains powered during shutdown to provide the bias current for the shutdown circuit. Quiescent supply current during shutdown is nominally 15uA and switch leakage is below 1uA.

Dimming Control

The FP7152 provides two dimming methods: PWM dimming and DC analog dimming. To use PWM dimming, apply a square wave to the EN/DIM pin. To used analog dimming, apply a 0.3V~2.5V DC voltage to this pin.

Application Information

Setting the LED Current

The LED current is identical and set by the current setting resistor between the V_{IN} pin and I_{SENSE} pin. To set the LED current according to the following equation:

$$ILED = \frac{0.1}{RS}$$

For RS=0.33Ω, the LED current is set to 300mA

Shutdown Mode

Taking the ADJ pin to a voltage below 0.2V will turn off the output and the supply current will fall to a low standby level of 15μA nominal.

Soft Start

The voltage on the ADJ pin is the inductor current reference. An external capacitor connected between the ADJ pin and ground provides a soft-start delay. When V_{IN} starts, internal voltage source charges the capacitor from 0V to 1.25V to fulfill soft-start function.

Capacitor selection

A low ESR capacitor should be used for input decoupling. The ESR of this capacitor appears in series with the supply source impedance and lowers overall efficiency. This capacitor has to supply the relatively high peak current to the coil and smooth the current ripple on the input supply. A minimum value of 4.7uF is acceptable if the input source is close to the device, but higher values will improve performance at lower input voltages, especially when the source impedance is high. The input capacitor should be placed as close to the IC as possible.

For maximum stability over temperature and voltage, capacitors with X7R, X5R, or better dielectric are recommended. Capacitors with Y5V dielectric are not suitable for decoupling in this application and should not be used.

Reducing output ripple

Peak to peak ripple current in the LED(s) can be reduced, if required, by shunting a capacitor C_{LED} across the LED(s)

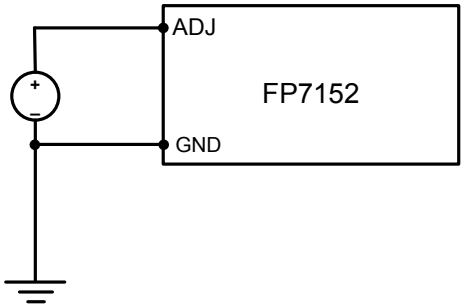
Inductor selection

Recommended inductor values for the FP7152 are in the range of 47uH to 100uH. Higher

inductance value is recommended at higher supply voltages in order to minimize errors due to switching delays, which result in increased ripple and lower efficiency. Higher values of inductance also result in a smaller change in output current over the supply voltage range. The inductor should be mounted as close to the device as possible with low resistance connections to the LX and VIN pins. The chosen coil should have a saturation current higher than the peak output current and a continuous current rating above the required mean output current.

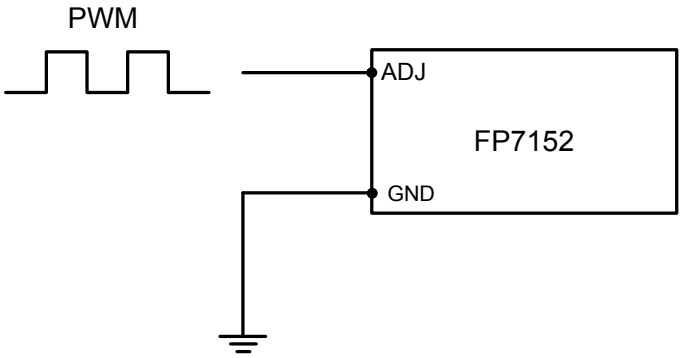
DC Dimming

I_{OUTDC}= 0.08*V_{ADJ}/RS (for 0.3< V_{ADJ} <2.5V). The ADJ pin can be driven by an external DC voltage (V_{ADJ}) to adjust the output current to a value below the nominal average value defined by RS. The LED current decreases linearly with the V_{ADJ} when 0.3V ≤ V_{ADJ} ≤ 2.5V, RS must be increased in proportion to prevent I_{OUTDC} exceeding 700mA maximum. When the V_{ADJ} falls below the threshold, 0.2V, the output switch is turned off which allows PWM dimming.



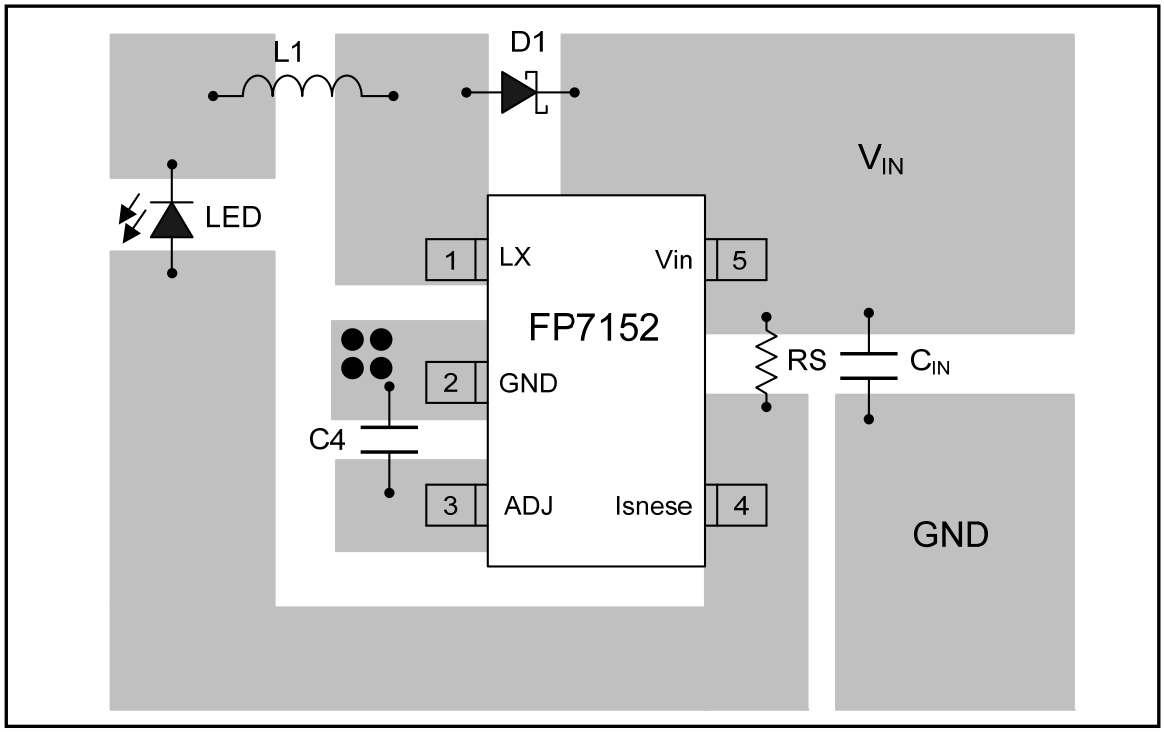
PWM Dimming

A Pulse Width Modulated (PWM) signal with duty cycle D_{PWM} can be applied to the ADJ pin, The PWM signal is recommended above 50kHz.As shown below, to adjust the output current to a value above or below the nominal average value set by resistor RS:



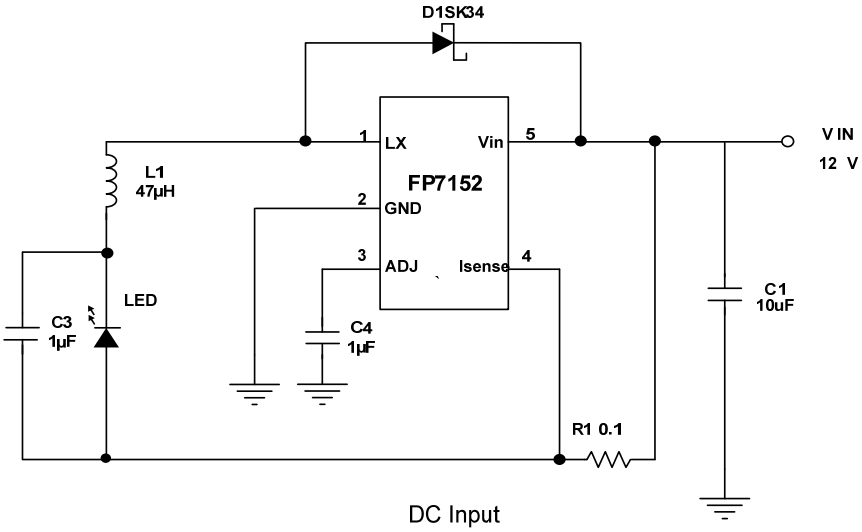
PC Board Layout Checklist

1. the capacitor C1 has to be placed to V_{IN} as close as possible
2. The sense resistor RS has to be placed as close as possible to V_{IN} and I_{SENSE}
3. The D1 anode, the LX pin and the inductor L1 have to be placed as close as possible to avoid ringing

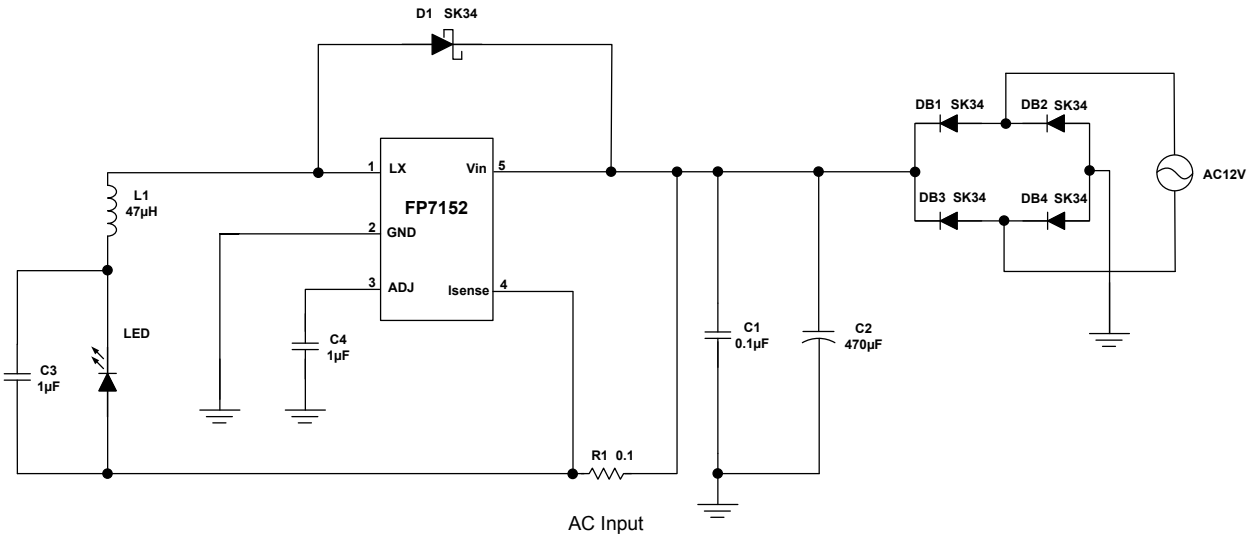


Suggested Layout

Typical Application



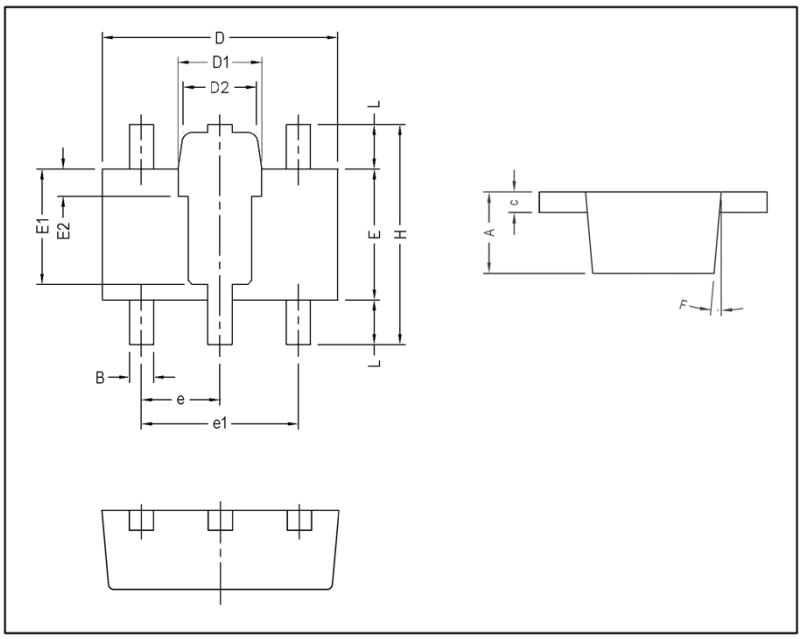
DC Input
LED Application Circuit



AC Input
LED Application Circuit

Package Outline

SOT89-5L



UNIT: mm

Symbols	Min. (mm)	Max. (mm)
A	1.400	1.600
B	0.400	0.520
c	0.350	0.410
D	4.400	4.600
D1	1.500	1.700
D2	1.300	1.500
E	2.400	2.600
E1	2.200 REF	
E2	0.520 REF	
e	1.500 REF	
e1	3.000 REF	
F	5° REF	
H	4.050	4.250
L	0.800	

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