



N 沟道增强型场效应晶体管

N-CHANNEL MOSFET

FHA9N90D

主要参数 MAIN CHARACTERISTICS

ID	9A
VDSS	900 V
Rdson-typ (@Vgs=10V)	1.1Ω
Qg-typ	45nC

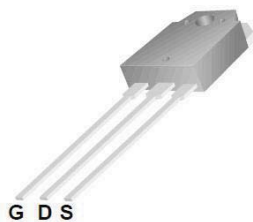
用途 APPLICATIONS

不间断电源	UPS
高频开关电源	High efficiency switch mode power supplies

产品特性 FEATURES

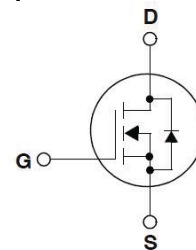
低栅极电荷	Low gate charge
低 Crss (典型值 14pF)	Low Crss (typical 14pF)
开关速度快	Fast switching
100%经过雪崩测试	100% avalanche tested
高抗 dv/dt 能力	Improved dv/dt capability
RoHS 产品	RoHS product

封装形式 Package



TO-3PN
FHA series

等效电路 Equivalent Circuit



绝对最大额定值 ABSOLUTE RATINGS (Tc=25°C)

项目 Parameter	符号 Symbol	数值 Value	单位 Unit
		FHA9N90D	
最高漏极-源极直流电压 Drain-Source Voltage	VDS	900	V
连续漏极电流* Drain Current -continuous *	ID (Tc=25°C)	9	A
	ID (Tc=100°C)	5.7	A
最大脉冲漏极电流 (注 1) Drain Current – pulse (note 1)	IDM	36	A
最高栅源电压 Gate-Source Voltage	VGS	±30	V
单脉冲雪崩能量 (注 2) Single Pulsed Avalanche Energy (note 2)	EAS	900	mJ
雪崩电流 (注 1) Avalanche Current (note 1)	IAR	9	A
重复雪崩能量 (注 1) Repetitive Avalanche Current (note 1)	EAR	28	mJ
二极管反向恢复最大电压变化速率 (注 3) Peak Diode Recovery dv/dt (note 3)	dv/dt	5.0	V/ns
耗散功率 Power Dissipation	Pd (TC=25°C)	280	W
	-Derate above 25°C	2.22	W/°C
最高结温及存储温度 Operating and Storage Temperature Range	TJ, TSTG	-55~+150	°C
引线最高焊接温度 Maximum Lead Temperature for Soldering Purposes	TL	300	°C

*漏极电流由最高结温限制

*Drain current limited by maximum junction temperature

电特性 ELECTRICAL CHARACTERISTICS

项目 Parameter	符号 Symbol	测试条件 Tests conditions	最小 Min	典型 Typ	最大 Max	单位 Units
关态特性 Off –Characteristics						
漏—源击穿电压 Drain-Source Voltage	BVDSS	$I_D=250\mu A, V_{GS}=0V$	900	-	-	V
击穿电压温度特性 Breakdown Voltage Temperature Coefficient	$\Delta BVDSS/\Delta T_J$	$I_D=250\mu A$, referenced to 25°C	-	0.9	-	V/°C
零栅压下漏极漏电流 Zero Gate Voltage Drain Current	IDSS	$V_{DS}=900V, V_{GS}=0V, T_C=25^\circ C$	-	-	1	μA
		$V_{DS}=720V, T_C=125^\circ C$	-	-	10	μA
栅极体漏电流 Gate-body leakage current	IGSS (F/R)	$V_{DS}=0V, V_{GS}=\pm 30V$	-	-	± 100	nA
通态特性 On-Characteristics						
阈值电压 Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D=250\mu A$	3.0	-	5.0	V
静态导通电阻 Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D=4.5A$	-	1.1	1.4	Ω
正向跨导 Forward Transconductance	gfs	$V_{DS} = 50V, I_D=12A$ (note 4)	-	24	-	S
动态特性 Dynamic Characteristics						
输入电容 Input capacitance	Ciss	$V_{DS}=25V, V_{GS}=0V, f=1.0MHz$	-	2100	-	pF
输出电容 Output capacitance	Coss		-	175	-	
反向传输电容 Reverse transfer capacitance	Crss		-	14	-	
开关特性 Switching Characteristics						
延迟时间 Turn-On delay time	td(on)	$V_{DS}=450V, I_D=9A, R_G=25\Omega, V_{GS}=10V$ (note 4, 5)	-	50	-	ns
上升时间 Turn-On rise time	tr		-	120	-	ns
延迟时间 Turn-Off delay time	td(off)		-	100	-	ns
下降时间 Turn-Off Fall time	tf		-	75	-	ns
栅极电荷总量 Total Gate Charge	Qg	$V_{DS} = 720V, I_D=9A, V_{GS}=10V$ (note 4, 5)	-	45	-	nC
栅—源电荷 Gate-Source charge	Qgs		-	13	-	nC
栅—漏电荷 Gate-Drain charge	Qgd		-	18	-	nC
漏—源二极管特性及最大额定值 Drain-Source Diode Characteristics and Maximum Ratings						
正向最大连续电流 Maximum Continuous Drain-Source Diode Forward Current		I_S	-	-	9	A
正向最大脉冲电流 Maximum Pulsed Drain-Source Diode Forward Current		I_{SM}	-	-	36	A
正向压降 Drain-Source Diode Forward Voltage	VSD	$V_{GS}=0V, I_S=9A$	-	0.9	1.4	V
反向恢复时间 Reverse recovery time	trr	$V_{GS}=0V, I_S=9A, dI_F/dt=100A/\mu s$ (note 4)	-	550	-	ns
反向恢复电荷 Reverse recovery charge	Qrr		-	6.5	-	μC

热特性 THERMAL CHARACTERISTIC

项目 Parameter	符号 Symbol	最大值 Max	单位 Unit
结到管壳的热阻 Thermal Resistance, Junction to Case	Rth(j-c)	0.45	°C/W
结到环境的热阻 Thermal Resistance, Junction to Ambient	Rth(j-A)	40	°C/W

注释:

- 1: 脉冲宽度由最高结温限制
- 2: L=21mH, IAS=9A, VDD=50V, RG=25 Ω,起始结温 TJ=25°C
- 3: ISD ≤9A, di/dt ≤200A/μs, VDD≤BVDS,起始结温 TJ=25°C
- 4: 脉冲测试: 脉冲宽度 ≤300μs, 占空比≤2%
- 5: 基本与工作温度无关

Notes:

- 1: Pulse width limited by maximum junction temperature
- 2: L=21mH, IAS=9A, VDD=50V, RG=25 Ω, Starting TJ=25°C
- 3: ISD ≤9A, di/dt ≤200A/μs, VDD≤BVDS, Starting TJ=25°C
- 4: Pulse Test: Pulse Width ≤300μs, Duty Cycle≤2%
- 5: Essentially independent of operating temperatur

Typical Characteristics

典型特性曲线

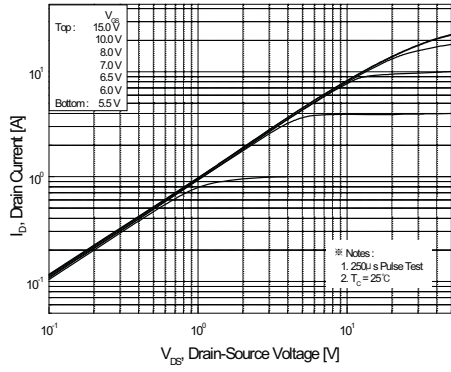


Figure 1. On-Region Characteristics

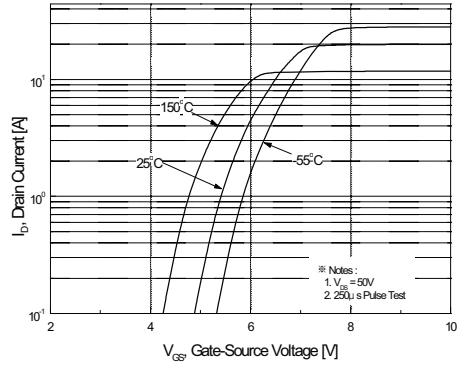


Figure 2. Transfer Characteristics

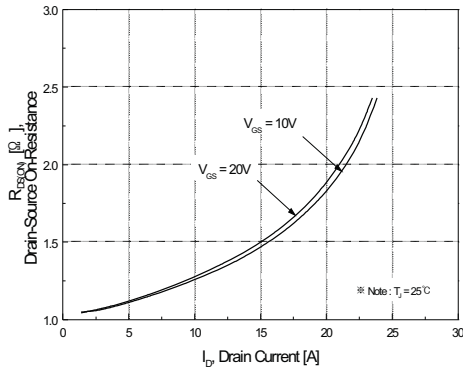


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

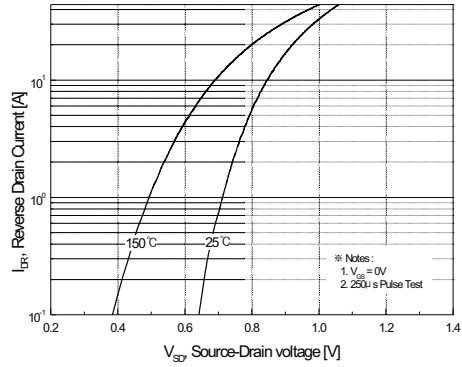


Figure 4. Body Diode Forward Voltage Variation with Source Current

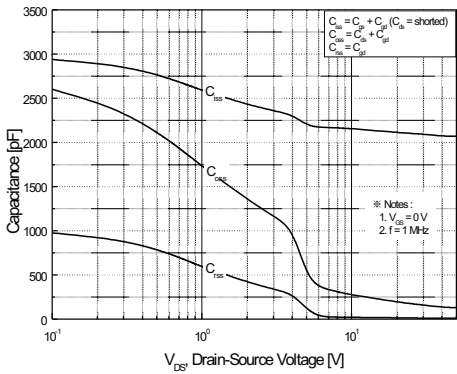


Figure 5. Capacitance Characteristics

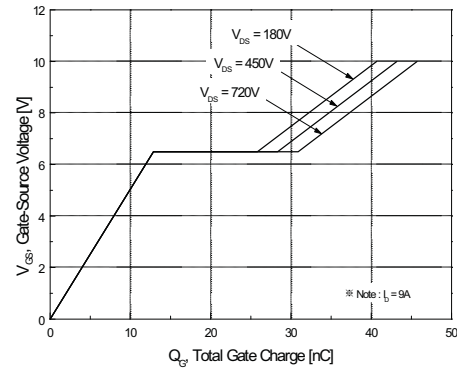


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

典型特性曲线 (续)

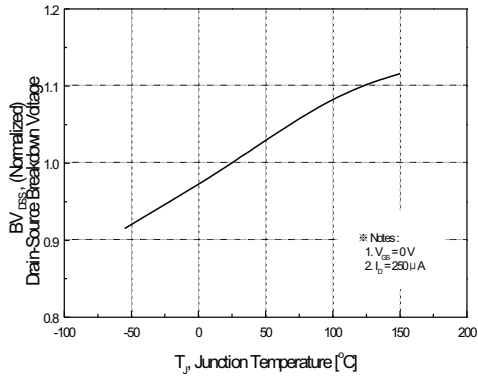


Figure 7. Breakdown Voltage Variation

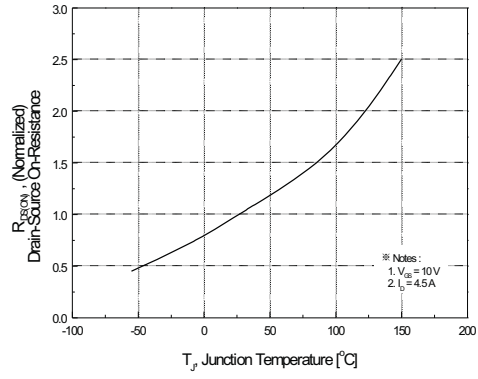


Figure 8. On-Resistance Variation

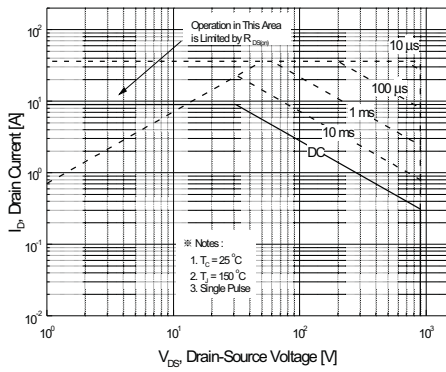


Figure 9. Maximum Safe Operating Area

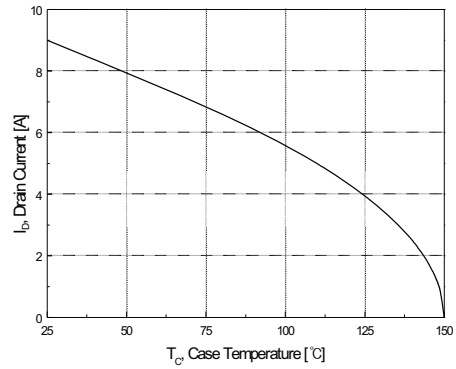


Figure 10. Maximum Drain Current vs Case Temperature

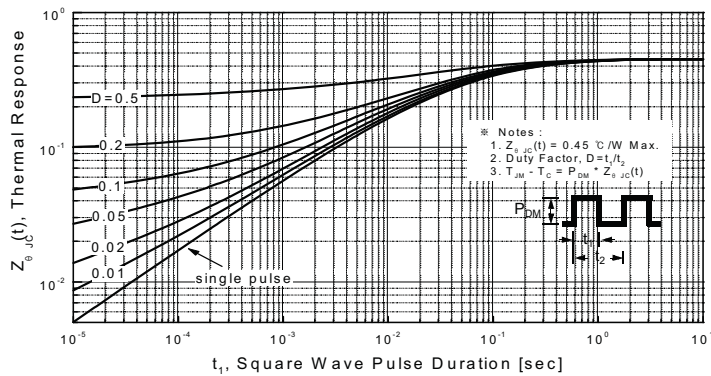


Figure 11. Transient Thermal Response Curve

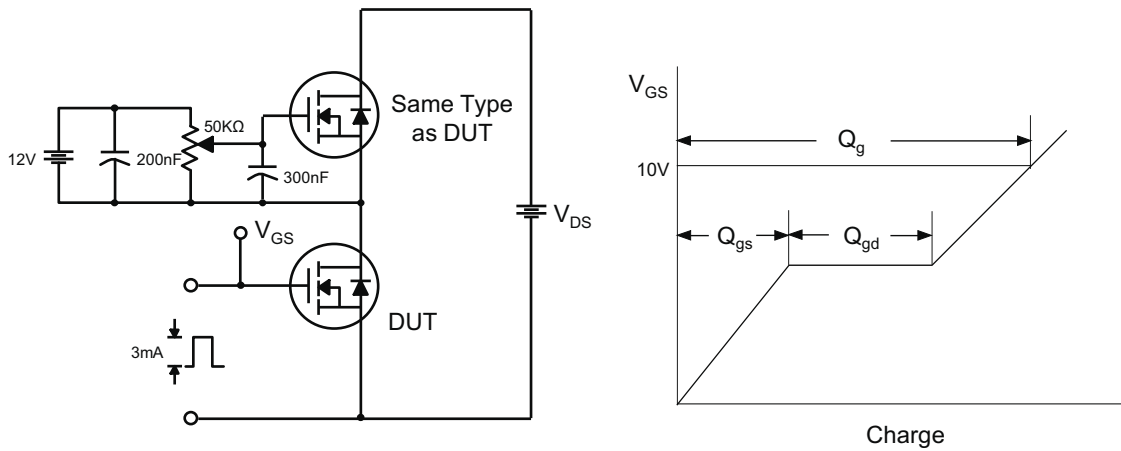


Fig 12. Gate Charge Test Circuit & Waveform

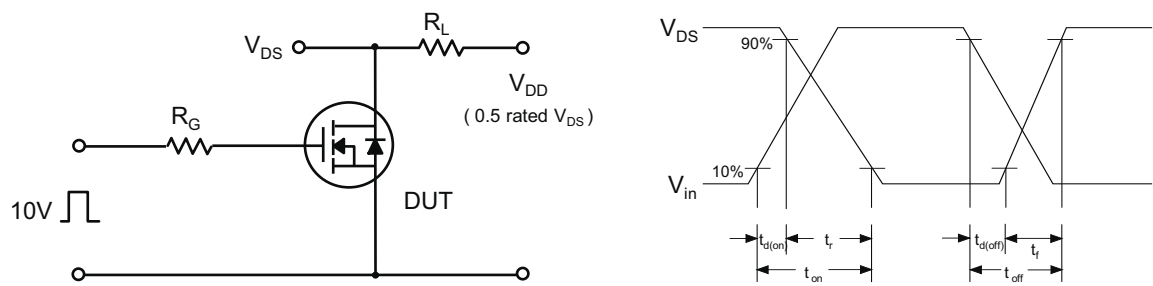


Fig 13. Resistive Switching Test Circuit & Waveforms

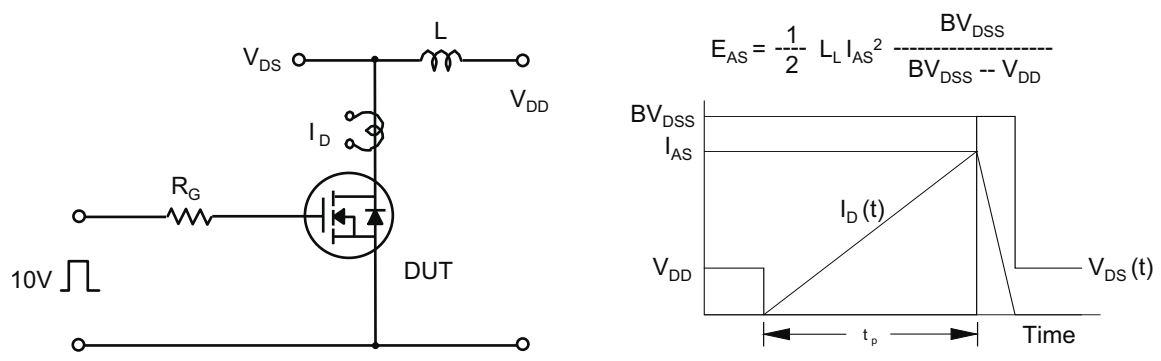


Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms

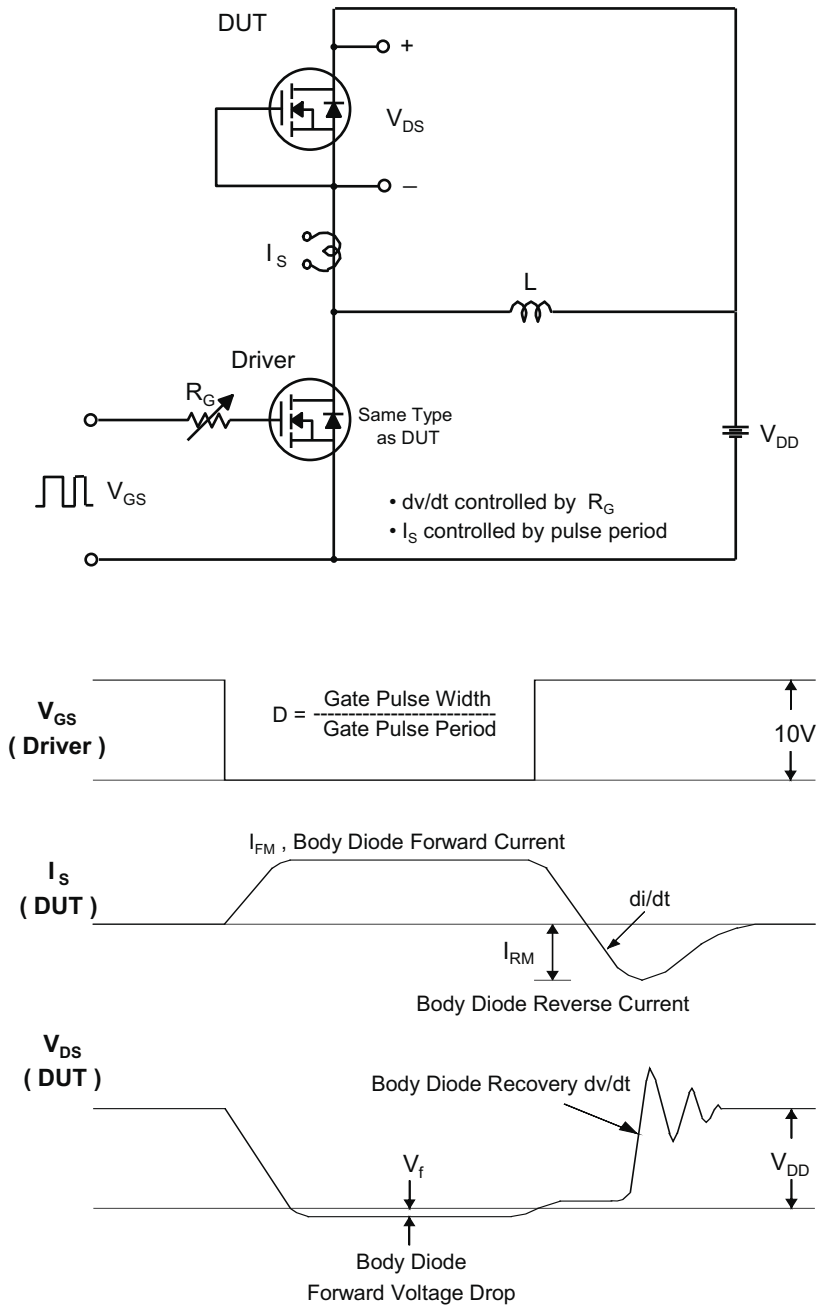
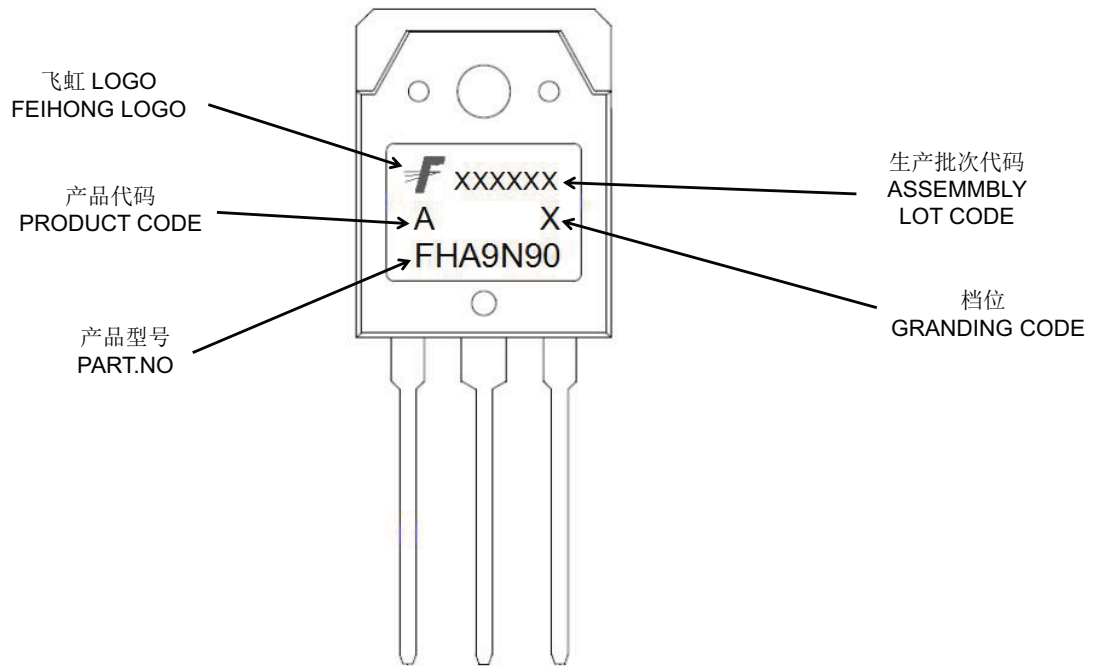


Fig 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

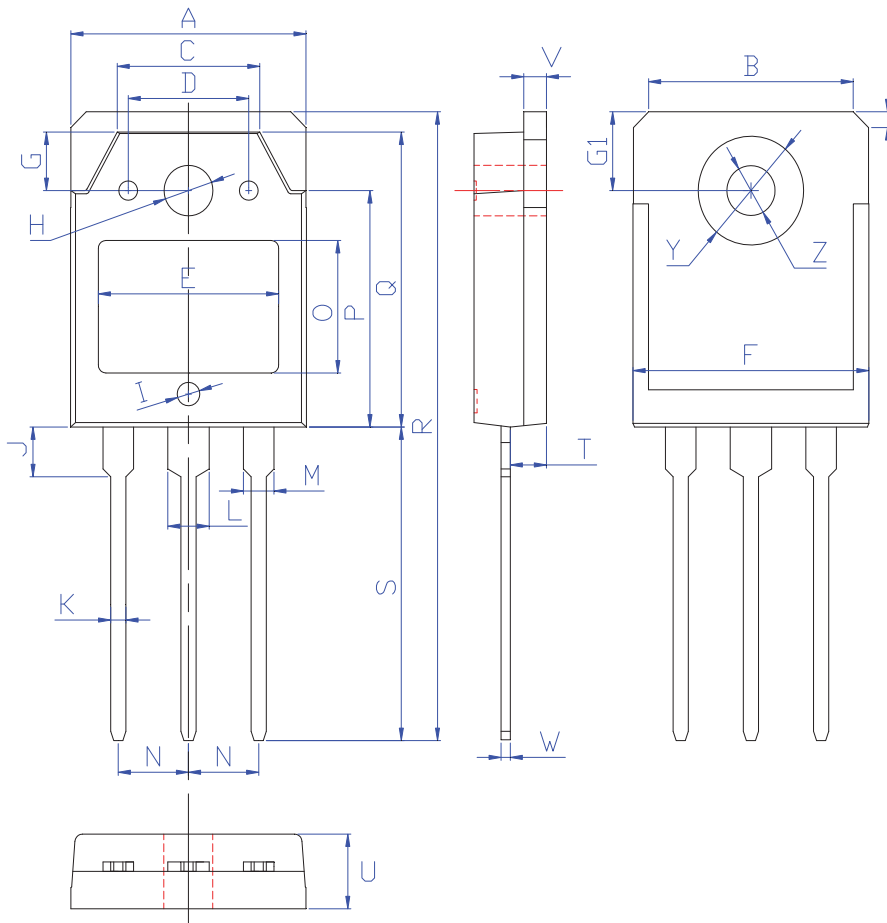
印记 Marking:



外形尺寸:

Package Dimension:

TO-3PN



DIM	MILLIMETERS
A	15.60±0.30
B	13.60±0.30
C	9.50±0.30
D	8.00±0.30
E	11.85±0.30
F	15.65±0.30
G	3.80±0.30
G1	5.00±0.30
H	φ 3.50±0.30
I	φ 1.50±0.30 深 0.15±0.15
J	3.20±0.30
K	1.00±0.15
L	3.10±0.15
M	2.10±0.15
N	5.45±0.30
O	8.40±0.30
P	13.90±0.30
Q	18.70±0.30
R	40.00±0.60
S	20.00±0.40
T	2.40±0.30
U	4.80±0.30
V	1.50±0.15
W	0.60±0.15
X	1.80±0.40
Y	7.00±0.30
Z	3.20±0.30

(Unit: mm)

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