PIN 5 CASE/ GND · PIN 1 V_{out+} · PIN 2 V_{CC} · PIN 4 V_{out-} · PIN 3 $+V_{BIAS}$ · Ø9.2 · Ø5.08 PIN CIRCLE

BACKSIDE VIEW

Ø6.60 · Ø8.3 · 2.2 · Ø 0.46 5 PL · 113° VIEWING ANGLE · 4.2 · 7.6 MIN 5 PL · ±1

1.00 SQ · ACTIVE AREA: 0.196 mm² (500 μm DIAMETER)

CHIP DIMENSIONS

FEATURES

- ∅ 0.500 mm active area
- Low noise
- High gain
- Long term stability

DESCRIPTION

The AD500-9-400M-TO5 is an Avalanche Photodiode Amplifier Hybrid containing a 0.196 mm² active area APD chip integrated with an internal transimpedance amplifier. Hermetically packaged in a TO-5 with a borosilicate glass window cap.
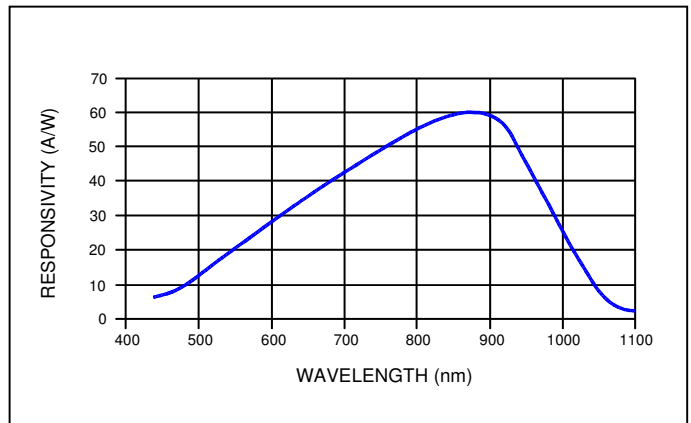
APPLICATIONS

- Precision photometry
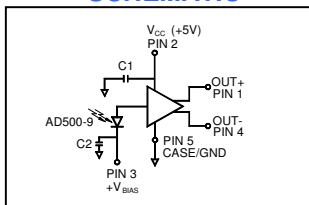- Analytical instruments
- Medical equipment
- Low light sensor

RoHS COMPLIANT

ABSOLUTE MAXIMUM RATING

SYMBOL	PARAMETER	MIN	MAX	UNITS
T_{STG}	Storage Temp	-55	+125	°C
T_{OP}	Operating Temp	0	+60	°C
$T_{SOLDERING}$	Soldering Temp	-	+240	°C
P	Power Dissipation	-	360	mW
V_{CC}	Single Supply Voltage	+3.0	+5.5	V
I_{CC}	Supply Current	-	63	mA

SCHEMATIC

V_{CC} (+5V) PIN 2 · C1 · OUT+ PIN 1 · AD500-9 · OUT- PIN 4 · C2 · PIN 5 CASE/GND · PIN 3 $+V_{BIAS}$

SPECTRAL RESPONSE at M = 100

RESPONSIVITY (A/W) vs WAVELENGTH (nm)

ELECTRO-OPTICAL CHARACTERISTICS @ 22° C (V_CC = single supply +3.3V, R_L = 100w unless otherwise specified)

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	MIN	TYP	MAX	UNITS
f_{-3dB}	Frequency Response	-3dB @ 905 nm	---	400	---	MHz
S	Sensitivity*	λ = 905 nm; M = 100	---	100	---	KV/W
I_{CC}	Supply Current	Dark state	---	34	63	mA

* Sensitivity = APD responsivity (0.45 A/W X 100 gain) x TIA gain (2.5K)

These devices are sensitive to electrostatic discharge. Please use ESD precautions when handling.

Disclaimer: Due to our policy of continued development, specifications are subject to change without notice.

AVALANCHE PHOTODIODE DATA @ 22 °C

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I_D	Dark Current	M = 100 (see note 2)	---	0.5	5.0	nA
C	Capacitance	M = 100 (see note 2)	---	1.2	---	pF
V_{BR}	Breakdown Voltage (see note 1)	I_D = 2 µA	160	240	---	V
	Temperature Coefficient of V_{BR}		---	1.55	---	V/K
	Responsivity	M = 100; = 0 V; λ = 905 nm	55	60	---	A/W
Δf_{3dB}	Bandwidth	-3dB	---	0.5	---	GHz
t_r	Rise Time	M = 100	---	550	---	ps
	Optimum Gain		50	60	---	
	"Excess Noise" factor	M = 100	---	2.5	---	
	"Excess Noise" index	M = 100	---	0.2	---	
	Noise Current	M = 100	---	1.0	---	pA/Hz$^{1/2}$
	Max Gain		200	---	---	
NEP	Noise Equivalent Power	M = 100; λ = 905 nm	---	2.0 X 10^{-14}	---	W/Hz$^{1/2}$

Note 1: The following different breakdown voltage ranges are available: (160 – 200 V), (200 – 240 V).

Note 2: Measurement conditions: Setup of photo current 1 nA at M = 1 and irradiated by a 880 nm, 80 nm bandwidth LED. Increase the photo current up to 100 nA, (M = 100) by internal multiplication due to an increasing bias voltage.

TRANSIMPEDANCE AMPLIFIER DATA @ 25 °C
(Vcc = +3.0 V to 5.5 V, TA = 0°C to 70°C, 100Ω load between OUT+ and OUT-. Typical values are at TA = 25°C, Vcc = +3.3 V)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage		3	5	5.5	V
Supply Current		---	34	63	mA
Transimpedance	Differential, measured with 40 µA p-p signal	2.10	2.75	3.40	kΩ
Output impedance	Single ended per side	48	50	52	Ω
Maximum Differential Output Voltage	Input = 2 mA p-p with 100 Ω differential termination	220	380	575	mV p-p
AC Input Overload		2	---	---	mA p-p
DC Input Overload		1	---	---	mA
Input Referred RMS Noise	TO-5 package, see note 4	---	490	668	nA
Input Referred Noise Density	See note 4	---	11	---	pA/Hz$^{1/2}$
Small signal bandwidth	Source capacitance = 0.85 pF, see note 3	1.525	2.00	---	GHz
Low Frequency Cutoff	-3 dB, input < 20 µA DC	---	30	---	kHz
Transimpedance Linear Range	Peak to peak 0.95 < linearity < 1.05	40	---	---	µA p-p
Power Supply Rejection Ratio (PSRR)	Output referred, f < 2 MHz, PSSR = -20 Log (ΔVout / ΔVcc)	---	50	---	dB

Note 3: Source capacitance for AD500-9-400M-TO5 is the capacitance of APD.

Note 4: Input referred noise is calculated as RMS output noise/ (gain at f = 10 Mhz). Noise density is (input referred noise)/√bandwidth.

TRANSFER CHARACTERISTICS

The circuit used is an avalanche photodiode directly coupled to a high speed data handling transimpedance amplifier. The output of the APD (light generated current) is applied to the input of the amplifier. The amplifier output is in the form of a differential voltage pulsed signal.

The APD responsivity curve is provided in Fig. 2. The term Amps/Watt involves the area of the APD and can be expressed as Amps/mm^2/Watts/mm^2, where the numerator applies to the current generated divided by the area of the detector, the denominator refers to the power of the radiant energy present per unit area. As an example assume a radiant input of 1 microwatt at 850 nm. The APD's corresponding responsivity is 0.4 A/W.

If energy in = 1 µW, then the current from the APD = (0.4 A/W) x (1 x 10^{-6}W) = 0.4 µA. We can then factor in the typical gain of the APD of 100, making the input current to the amplifier 40 µA. From Fig. 5 we can see the amplifier output will be approximately 75 mV p-p.

APPLICATION NOTES

The AD500-9-400M-TO5 is a high speed optical data receiver. It incorporates an internal transimpedance amplifier with an avalanche photodiode.

This detector requires +3.5 V to +5.0 V voltage supply for the amplifier and a high voltage supply (100-240 V) for the APD. The internal APD follows the gain curve published for the AD500-9-TO52-S1 avalanche photodiode. The transimpedance amplifier provides differential output signals in the range of 200 millivolts differential.

In order to achieve highest gain, the avalanche photodiode needs a positive bias voltage (Fig. 1). However, a current limiting resistor must be placed in series with the photodiode bias voltage to limit the current into the transimpedance amplifier. **Failure to limit this current may result in permanent failure of the device.** The suggested initial value for this limiting resistor is 390 KOhm.

When using this receiver, good high frequency placement and routing techniques should be followed in order to achieve maximum frequency response. This includes the use of bypass capacitors, short leads and careful attention to impedance matching. The large gain bandwidth values of this device also demand that good shielding practices be used to avoid parasitic oscillations and reduce output noise.

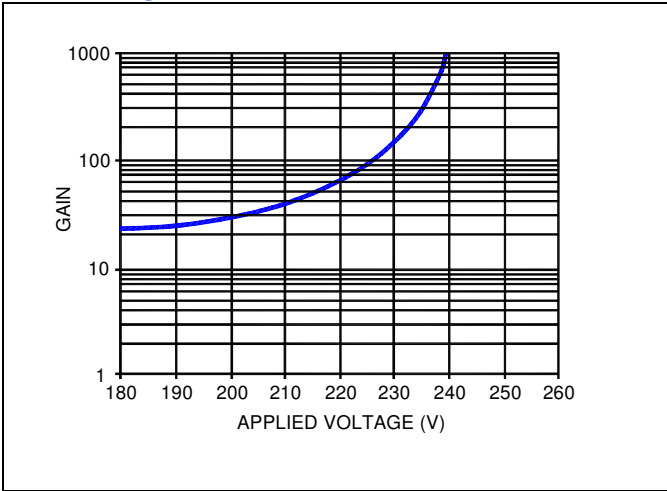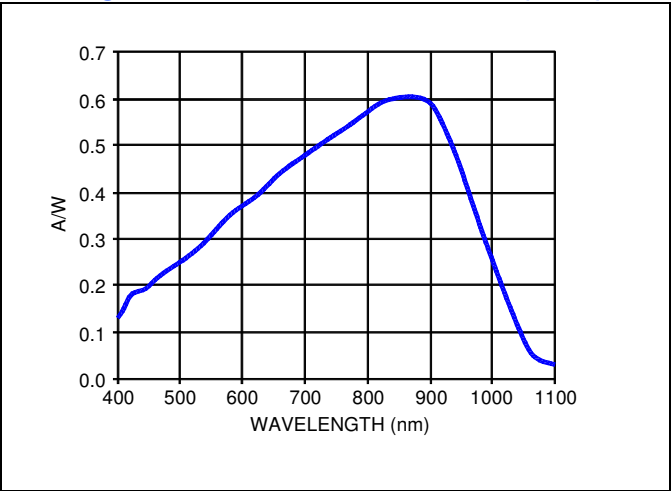## Fig. 1: APD GAIN vs BIAS VOLTAGE

Chart: X-axis "APPLIED VOLTAGE (V)" from 180 to 260; Y-axis "GAIN" (logarithmic) from 1 to 1000.

Fig. 2: APD SPECTRAL RESPONSE (M = 1)

Chart: X-axis "WAVELENGTH (nm)" from 400 to 1100; Y-axis "A/W" from 0.0 to 0.7.

Fig. 3 : AMPLIFIER OUTPUT vs TEMPERATURE

Chart: X-axis "AMBIENT TEMPERATURE (°C)" from 0 to 80; Y-axis "AMPLITUDE (mV)" from 200 to 400.
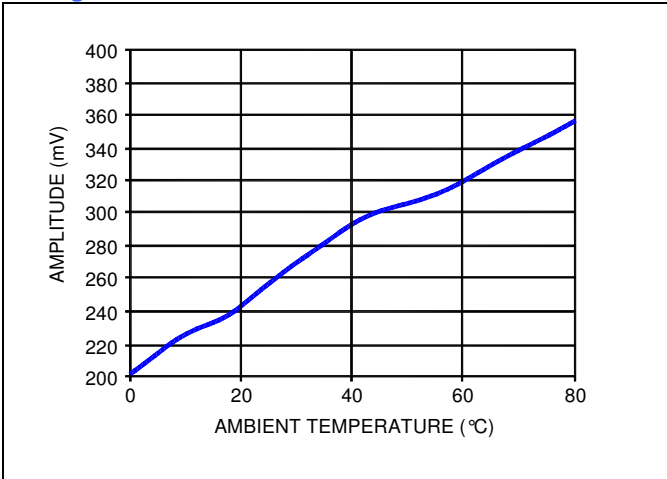
Fig.4 : APD CAPACITANCE vs VOLTAGE

Chart: X-axis "BREAKDOWN VOLTAGE (Vbr)" from 0 to 100; Y-axis "JUNCTION CAPACITANCE (pF)" from 0 to 40.
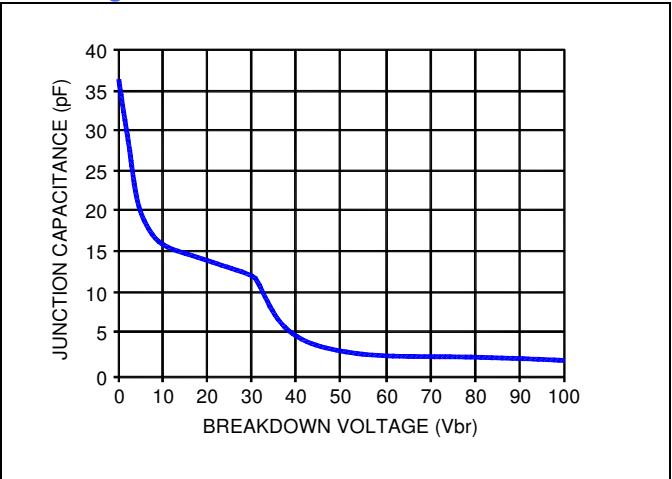
Fig. 5: AMPLIFIER TRANSFER FUNCTION

Chart: X-axis "INPUT CURRENT (µA)" from -100 to 100; Y-axis "DIFFERENTIAL OUTPUT VOLTAGE (mV p-p)" from -200 to 200.
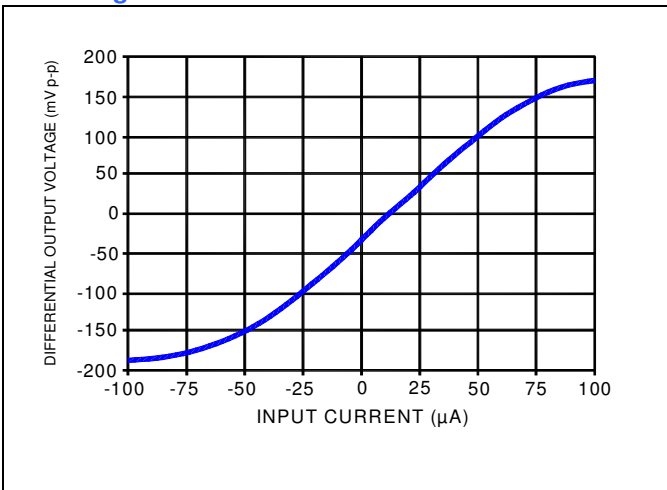
Fig. 6: TOTAL FREQUENCY RESPONSE
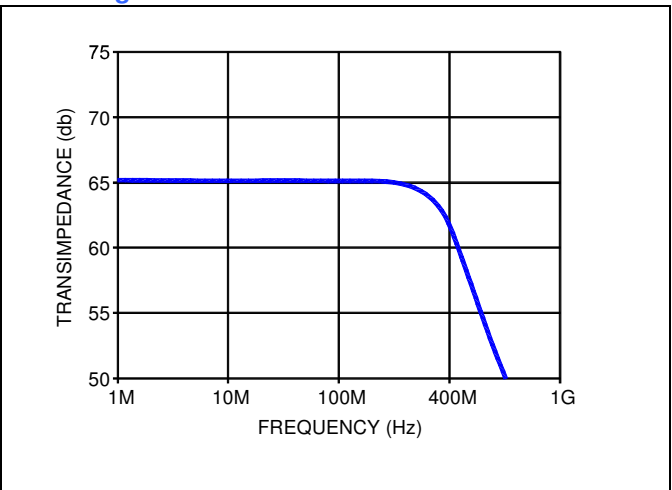
Chart: X-axis "FREQUENCY (Hz)" from 1M to 1G; Y-axis "TRANSIMPEDANCE (db)" from 50 to 75.

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