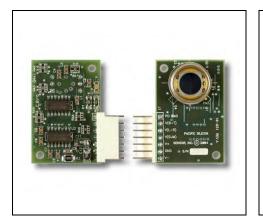
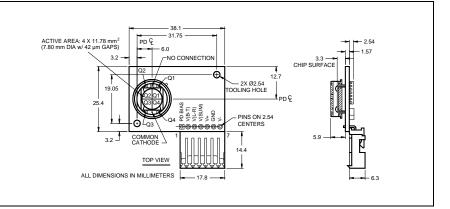
First Sensor 6

First Sensor Evaluation Boards Data Sheet Quad Sum and Difference Amplifier Part Description QP50-6-42u SD2 US Order # 10-025 International Order # 5000010





Description

The QP50-6-42u SD2 is a quad photodiode array with current-to-voltage amplifiers that provide bottom minus top and left minus right difference signals. Additionally the QP50-6-42u SD2 provides a signal that is the sum of all four quadrant diode signals. The difference signals are voltage analogs of the light intensity difference sensed by the pairs of photodiode elements in the array. The board has a 7 pin connector attached for easy hook up.

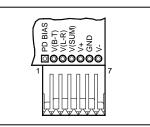
Options

- Can be purchased without connector, use order # 10-031.
- For alternate gap size, see data sheet QP50-6-18u SD2 for 18 μm gap version.
- Also available with quad detector active area sizes ranging from 1mm² to 20 mm².

Absolute maximum ratings

Symbol	Parameter	Min	Max	Unit
T _{STG}	Storage Temp	-15	+100	°C
T _{OP}	Operating Temp	0	+70	°C
Vs	Power Supply Voltage Recommended ±15V	±4.5	±18	V
V _R	Applied Bias Voltage*	0	15	V

Connections

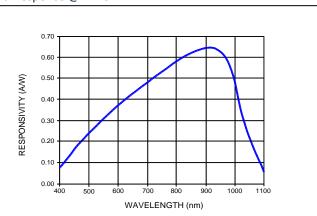


Applications

RoHS

- NIR & visible pulsed light positioning and tracking
- Laser beam centering
- 2011/65/EU

Spectral response @ 22°C

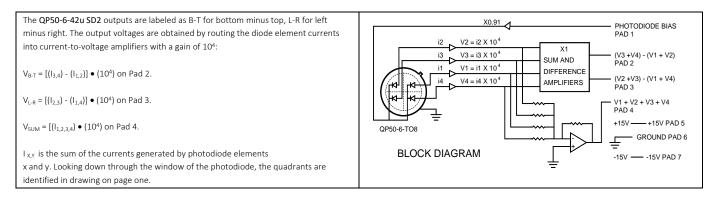


Electro-optical characteristics @ 22° C

Symbol	Characteristic	Test conditions	Min	Тур	Max	Units
Vo	Output Voltage (all outputs)			+V _s -3		V
				-V _s +3		
lo	Output Current Limit	V _s = ± 15 V; V _R = 0 V			25	mA
	Slew Rate	$V_s = \pm 15 V; V_R = 0 V$			10	V/µs
	Theoretical noise	$V_s = \pm 15 V; V_R = 0 V$		15		nV/VHz
$\Delta f_{ ext{-3dB}}$	Bandwidth**	V_{s} = \pm 15 V; V_{R} = 5 V; λ = 880 nm		250		kHz

* actual bias voltage to photodiode: pad 1 voltage times 0.91. Do not apply negative voltages to pad 1.

** dependent on bias voltage



APPLICATION NOTES

Beam Size

The light spot applied to the QP50-6-42u SD2 must be smaller than the diameter of the quadrant photodiode array. The detector active area has a diameter of 7.8 millimeters. If the light spot is too large, it may be reduced to fit the photodiode array by use of a lens. A decrease in output signal strength is observed as the light spot crosses the separation boundary of the quadrants, usually referred to as the "gap". This effect is more pronounced as the diameter of the light spot decreases, as a larger percentage of the light spot's power falls within the non-active gap. For this reason, the minimum light beam diameter should be a least one millimeter. A lens may be used to increase the beam diameter.

Photodiode Bias Operation

The QP50-6-42u SD2 array may be operated either in the zero bias or the reverse bias mode. Pin 1 is connected, via a resistor divider, to the non-inverting input of a voltage follower operational amplifier. This line may be left unterminated, grounded or connected to a voltage source of zero volts to operate in the zero bias mode. If the photodiode bias line is connected to a positive voltage source (but less than Vcc), then the photodiode elements in the array will be biased at 0.91 x VBias. Do not connect Pin 1 to negative voltages, as this will forward bias the photodiode array, making it inoperable and possibly damaging the circuit.

Use of Sum and Difference Signals for Alignment

The sum output signal may be used to help preliminary alignment of the QP50-6-42u SD2 to the source light beam. First, the beam or the QP50-6-42u SD2 is adjusted for maximum sum output signal. Second, the beam or QP50-6-42u SD2 is adjusted until the L-R and B-T signals are at minimum. This procedure results in the beam being centered on the quad photodiode array.

Signal Null Detector for Servo Applications

A common application for the QP50-6-42u SD2 is a signal null detector as part of a servo system that maintains the position of a light beam. Errors in beam position are reported by the QP50-6-42u SD2 and may be used to adjust a positioning device that restores the beam or the beam's source to a null position.

Speed of Response

Increasing the photodiode bias voltage will increase the speed of the QP50-6-42u SD2. Operating with zero reverse bias is sufficient for many applications (-3dB is around 150 kHz at 880 nm). As noted above, Pin 1 is provided for applying positive bias voltage to the quad for higher frequency response. Care should be taken not to exceed the circuit common mode values and the breakdown voltage of the quad photodiode. See Absolute Maximum Ratings on page one for maximum values.

Temperature Considerations

The operation temperature must be between 0 and 70 °C. For best resolution the temperature should be kept at or below 25 °C. Thermal gradients across the detector will cause position errors and should be avoided.

Offsets

Precision components are used in the circuitry but as much as 10 millivolts of dark offset may still be present in the outputs. If this causes a problem the offsets should be removed externally.

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