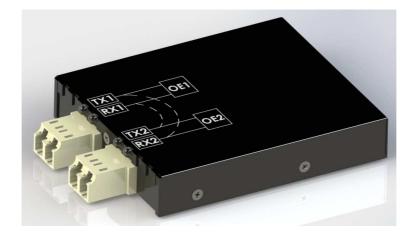


Specification

1.25G Ethernet Optical Bypass Module

Quad Ports 1.25G Base-SX



O B M – B 3 B H 2 – E 0 1



Features

- Reliable Passive Fiber Bypass (Latching)
- Low Return Loss
- > Available in 50/125µm Multimode Fiber
- **PCB Mountable Type**
- **Fast Ethernet Standard Compliant**
- Digital Diagnostic SFF-8472 Rev.10.2 Compliant
- SONET/SDH Standard Compliant
- > Two Dual LC Adapter
- > 1.25 G-BASE-SX are available
- **Compliant with CE & FCC Standard**
- > Compact Format and ROHS Compliant

Product Overview

Formerica's Dual Port 1.25 Gbit/s Optical Bypass Module(OBM) is a compact module that contains four 1.25 G Base-SX ports and can be integrated with I/O port controllers in a Network Interface Card (NIC). This module is targeted for maintaining network connectivity when power failure or system fails in an In-Line Network System.

Formerica's Dual Port 1.25 Gbit/s Optical Bypass Module supports Normal and Bypass modes, and can be configured to perform the Block mode. In Normal mode, two ports function independently. In Bypass mode, signal packets received from one port are loop-backed to the adjacent port. In Block mode, the module blocks the route.

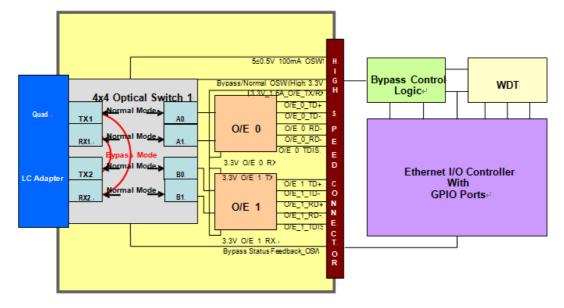
Formerica's Dual Port 1.25 Gbit/s Optical Bypass Module can Bypass or Block its I/O ports upon a host system failure, power off, or by software request. Formerica's Dual Port 1.25 Gbits Optical Bypass Module can be integrated with any brand's Controllers CPU. It is suitable for connecting with in-line equipment for power failure or system maintenance.

When the In-Line unit is not on or is in bypass mode, the relays within the OBM are set to bridge the optical signals directly through the optical switch, completely bypassing the In-Line equipment.

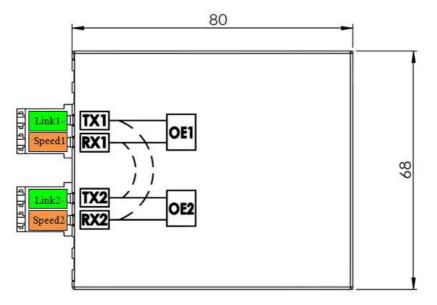
Formerica's OBM module provides excellent performance and reliable Optical Bypass and Block functions. All routing fibers, switches and electronics network are enclosed in a safe and compact housing which provides safe and space-saving to networking equipment.



Block Diagram and Optical Paths



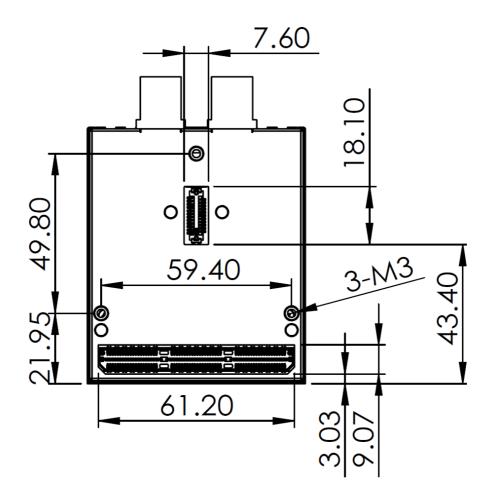
Dual Ports Optical Bypass Module



- Link1 and Link2 indications:
 Green: Link
- Speed1 and Speed2 indications: Amber: Data rate 1.25 G



Module Pin Out





84 PIN Assignments

Pin Number	Name	I/0	Function	Note
1				
2	GND			
3	On-Line	0	1K Ohm to Ground	
4	GND			
5	OE1 TX Disable	Ι	Transmitter Disable for OE-1	2
6	OE1 TX Fault	0	Transmitter Fault Indication for OE-1	1
7	OE1 MOD-DEF2	I/0	2 Wire Serial ID Interface (Data) for OE-1	3
8	OE1 MOD-DEF1	Ι	2 Wire Serial ID Interface (Clock) for OE-1	3
9	OE1 Vcc		3.3V for Tx1 Power Supply and Rx1 Power Supply 300mA	_
10	OE1-Link	Ι	OE1-Link LED Indicator (Voltage Low/ LED Green)	
11	OE1-Speed	Ι	OE1-Speed LED Indicator (Voltage Low / LED Orange)	
12	OE1 Rate Select	Ι	NA	
13	GND			
14				
15				
16				
17				
18				
19				
20				
21				
22	GND			
23	OE2 TX Disable	Ι	Transmitter Disable for OE-2	2
24	OE2 TX Fault	0	Transmitter Fault Indication for OE-2	1
25	OE2 MOD-DEF2	I/0	2 Wire Serial ID Interface (Data) for OE-2	3
26	OE2 MOD-DEF1	Ι	2 Wire Serial ID Interface (Clock) for OE-2	3
27	OE2 Vcc		3.3V for Tx2 Power Supply and Rx2 Power Supply 300mA	_
28	OE-2-Link	1	OE2-Link LED indicator (Voltage Low / LED Green)	
29	OE-2-Speed	Ι	OE2-Speed LED indicator (Voltage Low / LED Orange)	
30	OE2 Rate Select	Ι	NA	
31	GND			
32				



1.25G Ethernet Optical Bypass Module

PRODUCT NUMBER: OBM-B3BH2-E01

34					
35	33				
36	-				
37					
38	36				
39	37				
40 GND Image: constraint of the state o	38				
41 GND Image: constraint of the system of t	39				
42 Vcc5_1 5.0V Power Supply (OSW 1 Power) 8 43 OSW 1 N1 I Change to Normal mode for OSW1 7/5 44 OSW 1 B1 I Change to Bypass mode for OSW1 7/5 45 OSW 1 State Output O High=Normal Mode, Low=Bypass Mode for OSW1 7/5 46 I I Image: Comparison of Co	40	GND			
43 OSW 1 N1 I Change to Normal mode for OSW1 7/5 44 OSW 1 B1 I Change to Bypass mode for OSW1 7/5 45 OSW 1 State Output O High=Normal Mode, Low=Bypass Mode for OSW1 7/5 46 Image to Bypass mode for OSW1 Image to Bypass Mode for OSW1 7/5 46 Image to Bypass Mode for OSW1 Image to Bypass Mode for OSW1 1mage to Bypass Mode for OSW1 46 Image to Bypass Mode, Low=Bypass Mode for OSW1 Image to Bypass Mode for OSW1 1mage to Bypass Mode for OSW1 1mage to Bypass Mode for OSW1 46 Image to Bypass Mode, Low=Bypass Mode for OSW1 Image to Bypass Mode for OSW1 1mage to Bypass Mode for OSW1 46 Image to Bypass Mode for OSW1 Image to Bypass Mode for OSW1 Image to Bypass Mode for OSW1 1mage to Bypass Mode for OSW1 48 Image to Bypass Mode for OSW1 Image to Bypass Mode for OSW1 Image to Bypass Mode for OSW1 1mage to Bypass Mode for OSW1 1ma	41	GND			
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45 OSW 1 State Output O High=Normal Mode, Low=Bypass Mode for OSW1 46 47 48 49 50 51 52 53 54 55 OE2 LOS O Loss of Signal for OE-2 4 56 OE2 RD O E2 Signal Ground 57 OE2 RD O OE2 Inversed Data Output 5 58 OE2 RD+ O OE2 Signal Ground 60 OE2 GND OE2 Signal Ground 6 61 OE2 TD+ I OE2 Data Input 6 62 OE2 GND OE2 Signal Ground 6 6	43	OSW 1 N1	I	Change to Normal mode for OSW1	7/9
45 Output 0 High=Normal Mode, Low=Bypass Mode for OSW1 46	44	OSW 1 B1	Ι	Change to Bypass mode for OSW1	7/9
47	45		0	High=Normal Mode, Low=Bypass Mode for OSW1	
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58OE2 RD+OOE2 Data Output559OE2 GNDOE2 Signal Ground660OE2 GNDOE2 Signal Ground661OE2 TD+IOE2 Data Input662OE2 TD-IOE2 Inversed Data Input663OE2 GNDOE2 Signal Ground664IOE2 Signal Ground165IIOE2 Signal Ground666IIII67IIII67IIII68IIII69IIII60IIII61IIII63IIII64IIII66IIII66IIII67IIII67IIII67IIII67IIII67IIII10IIII10IIII10IIII10IIII10IIII10IIII10IIII10IIII <td>56</td> <td>OE2 GND</td> <td></td> <td>OE2 Signal Ground</td> <td></td>	56	OE2 GND		OE2 Signal Ground	
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63 OE2 GND OE2 Signal Ground 64 65 66 67	61	OE2 TD+	1	OE2 Data Input	6
64 65 66 67	62	OE2 TD-	1	OE2 Inversed Data Input	
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69				
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73	OE1 LOS	0	Loss of Signal for OE-1	4
74	OE1 GND		OE1 Signal Ground	
75	OE1 RD-	0	OE1 Inversed Data Output	5
76	OE1 RD+	0	OE1 Data Output	5
77	OE1 GND		OE1 Signal Ground	
78	OE1 GND		OE1 Signal Ground	
79	OE1 TD+		OE1 Data Input	6
80	OE1 TD-		OE1 Inversed Data Input	6
81	OE1 GND		OE1 Signal Ground	
82				
83				
84				



23 PIN Assignments

Pin	Name	1/0	Function	Note
Number	Name	1/0	Function	Note
1	Vcc5_1		5.0V Power Supper (OSW1 Power)	
2	Vcc5_1		5.0V Power Supper (OSW1 Power)	
3				
4	OE1-Link	I	OE1-Link LED Indicator (Voltage Low/ LED Green)	
5				
6	OE1-Speed	Ι	OE1-Speed LED Indicator (Voltage Low / LED Orange)	
7				
8	OE2-Link	I	OE2-Link LED indicator (Voltage Low / LED Green)	
9				
10	OE2-Speed	Ι	OE2-Speed LED indicator (Voltage Low / LED Orange)	
11				
12	OSW 1 State	ο	High=Normal Mode, Low=Bypass Mode for OSW1	
12	Output	Ŭ		
13	GND			
14	GND			
15				
16	OSW 1 B1	Ι	Change to Bypass mode for OSW1	
17				
18	OSW 1 N1	Ι	Change to Normal mode for OSW1	
19			No Connector	
20			No Connector	
21			No Connector	
22				
23				



Notes:

- TX Fault is an open collector/drain output, which should be pulled up with a 4.7K 10KΩ resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.5V.
- 2. TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7-10 K Ω resistor. Its states are:
 - Low (0 0.8V): Transmitter on
 - (>0.8, < 2.0V): Undefined
 - High (2.0 3.465V): Transmitter Disabled
 - Open: Transmitter Disabled
- 3. Mod-Def 1,2, These are the module definition pins. They should be pulled up with a 4.7K 10KΩ resistor on the host board. The pull-up voltage shall be VccT or VccR (see Section IV for further details). Mod-Def 0 is grounded by the module to indicate that the module is present Mod-Def 1 is the clock line of two wire serial interface for serial ID Mod-Def 2 is the data line of two wire serial interface for serial ID
- 4. LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K 10KΩ resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.5V.</p>
- 5. RD-/+: These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 350 and 850 mV differential (175 – 425 mV single ended) when properly terminated.
- 6. TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 180 700 mV (90 350 mV single-ended).
- 7. Latching Type The input is used to control the optical switch mode for OSW
- Normal mode: OSW N1/N2: > 3.75V and OSW B1/B2 < 0.5V and over 20ms. Bypass mode: OSW N1/N2: < 0.5V and OSW B1/B2 >3.75V and over 20ms Non-Latching Type - High =Normal Mode, Low=Bypass Mode for OSW.
- 9. Non-Latching Type Pin 43, 44, 82 and 83: Ground



Absolute Maximum Ratings

Parameter	Symbol	Min.	Тур.	Max.	Unit
Storage Temperature	Ts	-40		85	°C
Supply Voltage	Vcc	0		5	V

Recommended Operating Conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Case Operating Temperature	Тор	0		70	°C	1
	Vcc5	4.75		5.25	V	Vcc
+5.0V Supply Voltage	VLLS					5
+3.3V Supply Voltage	Vcc3	3.10		3.50	V	
Relative Humidity	рц	F		85	%	
(non-condensation)	RH	RH 5		65	70	
Data Rate		-100ppm	1.25	+100ppm	Gbps	

Note1 : Please see order information



Electrical Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note		
+5.0V Supply Current	lcc5			100	mA			
+3.3V Supply Current	Icc3			1200	mA			
	Transm	itter						
Transmitter Differential Input Voltage	VDT	180		700	mV	1		
Transmitter Disable Input-High	Vdish	2		Vcc+0.3	V			
Transmitter Disable Input-Low	VDISL	0		0.8	V			
Transmitter Fault Pull up Resistor	RTX FAULT	4.7		10	ΚΩ	2		
Transmitter Fault Output-High	Vtxfh	2.4		Vcc	V	2		
Transmitter Fault Output-Low	VTXFL	0		0.5	V	2		
	Receive	er						
Receiver Differential Output Voltage	VDR	350		850	mV	3		
Receiver LOS Load	RRXLO S	4.7		10	ΚΩ	2		
LOS Output Voltage-High	VLOSH	2.4		Vcc	V	2		
LOS Output Voltage-Low	VLOSL	0		0.5	V	2		
Optical Switch								
Latching Voltage-High	VLATH	4.75	5	5.25	V			
Latching Voltage-Low	VLATL	0		0.8	V			
Latching Resistance	RLAT		125		Ω			

Notes:

- 1. Internally AC coupled and terminated to 1000hm differential load
- 2. Pull up to Vcc on Host Board.
- 3. Internally AC coupled, but requies a 1000hm differential termination at or internal to Serializer/Deserializer.



Optical Characteristics (Top=0~70°C, Data Rate=1.25Gb/sec, PRBS=2³¹-1 NRZ)

Parameter	Multimode Fiber Type	Min. Modal Bandwidth (MHz*km)	Operating Range (m)	Maximum Channel Intertion Loss (dB)
Operating	62.5um MMF OM1	200	0.5 to 137	2.53
Operating Range	50um MMF OM2	500	0.5 to 550	3.42
	50um MMF OM3	2000	0.5 to 800	4.62

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note		
Transmitter								
Output Optical Power(Avg.)	Ро	-9.5		-4	dBm	1		
Optical Extinction Ratio	ER	9			dB			
Center Wavelength	λ	830	850	860	nm			
Spectral Width (RMS)	σλ			0.85	nm			
	Rece	eiver		L	I			
Sensitivity (OMA)	P _{IN}			-20	dBm	1,2		
Input Optical Wavelength	λ	830		860	DBm			
Return Loss				-12	dB			
LOS-Deasserted (Avg.)	PA			-20	dBm			
LOS-Asserted (Avg.)	PD	-34			dBm			
LOS-Hysteresis	$P_A - P_D$	0.5			dB			
Overload	Ро			-3	dBm			
	Optical	Switch						
Wavelength Range	λR	670		980	nm			
Insertion Loss	OIL	0.9		1.8	dB			
Return Loss	ORL	30			dB			
Switch Time				8	ms			
Lifetime			\geq 10 ⁷		times			
Latching Resistance	R _{LAT}		125		Ω			

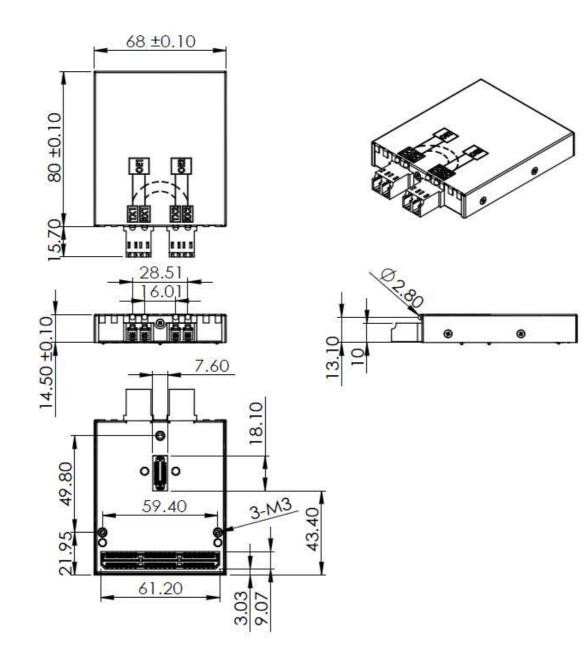
Notes:

1. Normal Mode (Bypass off).



 The sensitivity provided at a BER of 1×10-12 or better with an input signal consisting of 10.3125Gb/s, 2³¹-1 PRBS

Package Outline Drawing (mm)





ESD

Normal ESD precautions are required during the handling of this module. This transceiver is shipped in ESD protective packaging. It should be removed from the packaging and handled only in an ESD protected environment.

Contact Information

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