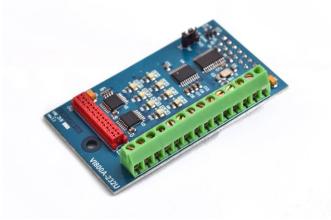


VI800A_232U Datasheet Version 1.2

Document Reference No.: BRT_000009 Clearance No.: BRT#013

FTDI Chip

VI800A-232U Datasheet Plug in accessory for VM800P Embedded Video Engine Plus module



1 Introduction

The VI800A-232U is a plug in accessory for the VM800P module, which expands the VM800P IO capabilities to include RS232.

This module behaves as an SPI to RS232 bridge on the VM800P Plus module.

1.1 Features

- Connects to the VM800P Plus module using an SPI slave interface
- SPI slave interface is converted to RS232 interface
- 4 GPIO inputs and 4 GPIO outputs
- 8 LEDs to indicate the input and output status
- 5 V tolerant buffers
- Screw connector to connect the RS232 signals, GPIO inputs and GPIO outputs
- IO connector to connect the RS232 signals, GPIO inputs and GPIO outputs
- Powered from the VM800P module

Neither the whole nor any part of the information contained in, or the product described in this manual, may be adapted or reproduced in any material or electronic form without the prior written consent of the copyright holder. This product and its documentation are supplied on an as-is basis and no warranty as to their suitability for any particular purpose is either made or implied. Future Technology Devices International Ltd will not accept any claim for damages howsoever arising as a result of use or failure of this product. Your statutory rights are not affected. This product or any variant of it is not intended for use in any medical appliance, device or system in which the failure of the product might reasonably be expected to result in personal injury. This document provides preliminary information that may be subject to change without notice. No freedom to use patents or other intellectual property rights is implied by the publication of this document. Future Technology Devices International Ltd, Unit 1, 2 Seaward Place, Centurion Business Park, Glasgow G41 1HH United Kingdom. Scotland Registered Company Number: SC136640



2 Ordering Information

| Part No. | Description |
|-------------|---|
| VI800A-232U | VI800A RS232 module, plug in accessory for the VM800P Plus module |

Table 2-1 – Ordering information



Table of Contents

| 1 1 | ntroduction | 1 |
|-----|--------------------------------------|------|
| 1.1 | Features | 1 |
| 2 (| rdering Information | 2 |
| 3 H | ardware Description | 4 |
| 3.1 | VI800A-232U module | 4 |
| 3.2 | Physical Descriptions | 5 |
| 3 | 2.1 PCB Dimensions | 5 |
| 3 | 2.2 VI800A-232U Connectors | 6 |
| 3 | 2.3 VI800A-232U Components | 8 |
| 4 E | oard Schematics | . 9 |
| 5 H | ardware Setup Guide | 10 |
| 5.1 | Power Configuration | . 10 |
| 5.2 | RS232 Interface connection | . 10 |
| 6 (| ontact Information | 11 |
| Арр | endix A – References | 12 |
| Do | ument References | . 12 |
| Арр | endix B - List of Figures and Tables | 13 |
| Lis | of Figures | . 13 |
| Lis | of Tables | . 13 |
| Арр | endix C – Revision History | 14 |



3 Hardware Description

Please refer to section **3.2.2** for connector settings. Some VI800A-232U jumpers must be set to work properly with your system.

3.1 VI800A-232U module



Figure 3-1 - VI800A-232U module

The VI800A-232U module is designed to connect directly with the VM800P Plus module. The main functions of the VI800A-232U are as follows:

- Plug in accessory board for the VM800P Plus module.
- Interface to the VM800P Plus board as a SPI slave device.
- Connects with an external RS232 interface.
- Supports 4 GPIO inputs.
- Supports 4 GPIO outputs.
- Contains 8 LEDs, 4 represent input signal level and 4 represent output level.
- Powered by the VM800P Plus board.



3.2 Physical Descriptions

3.2.1 PCB Dimensions

The VI800A-232U module PCB layout is illustrated in Figure 3-2, Figure 3-3 and Figure 3-4.

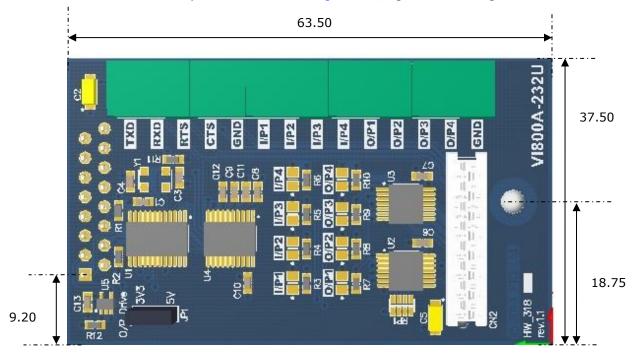


Figure 3-2 - VI800A-232U module PCB Top view

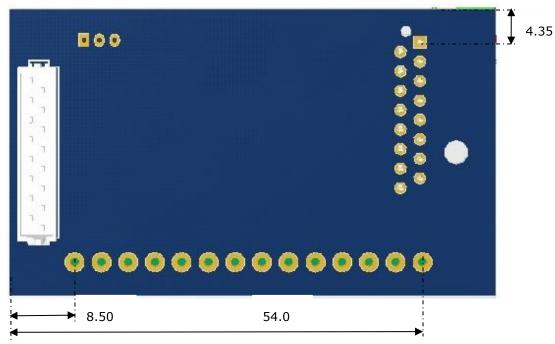


Figure 3-3 - VI800A-232U module PCB Bottom view



VI800A_232U Datasheet Version 1.2

Document Reference No.: BRT_000009 Clearance No.: BRT#013

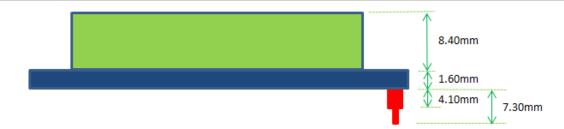


Figure 3-4 - VI800A-232U module PCB Side view

All dimensions are in mm

3.2.2 VI800A-232U Connectors

Connectors and jumpers are described in the following sections.

• CN1- SPI Interface

This is the interface where the SPI control and data signals are routed. There are also power and ground pins on this interface. This interface is used to connect the VI800A-232U board to the VM800P Plus board.

Note:

This connector should be connected to J6 of the VM800P plus board.

| Pin No. | Name | Туре | Description | |
|---------|------|------|---|--|
| 1 | SCLK | I | SPI Clock input, 3.3V (5V tolerant) | |
| 2 | MOSI | I | Master Out Slave in, 3.3V (5V tolerant) | |
| 3 | MISO | 0 | Master In Slave out, 5V | |
| 4 | SS# | Ι | SPI Chip select, active low, 3.3V (5V tolerant) | |
| 5 | INTO | 0 | Interrupt output active low, 3.3V | |
| 6 | IO6 | Ι | Daughter reset input, active low , 3.3V (5V tolerant) | |
| 7 | AD4 | IO | Address/Data Line 4 | |
| 8 | AD5 | IO | Address/Data Line 5 | |
| 9 | 3V3 | Р | 3.3V power supply | |
| 10 | 5V | Р | 5V power supply | |
| 11 | GND | Р | Ground | |
| 12 | RST# | I | Reset, active low | |
| 13 | AD1 | IO | Address/Data Line 1 | |
| 14 | NC | NA | Not Connected | |
| 15 | AD3 | IO | Address/Data Line 3 | |
| 16 | AD2 | IO | Address/Data Line 2 | |

Table 3-1 – CN1 Pinout

• CN2- IO Interface (alternative to CN3)

This is the interface where the RS232 connections, GPIO inputs and outputs are connected. There are also power and ground pins on this interface.

VI800A_232U Datasheet Version 1.2



Document Reference No.: BRT_000009 Clearance No.: BRT#013

| Pin No. | Name | Туре | Description |
|---------|---------|--------|-------------------|
| 1 | 3V3 | Р | 3.3V power supply |
| 2 | 5V | Р | 5V power supply |
| 3 | I/P1 | I | Input 1 |
| 4 | I/P2 | I | Input 2 |
| 5 | I/P3 | I | Input 3 |
| 6 | I/P4 | I | Input 4 |
| 7 | O/P1 | 0 | Output 1 |
| 8 | O/P2 | 0 | Output 2 |
| 9 | O/P3 | 0 | Output 3 |
| 10 | O/P4 | 0 | Output 4 |
| 11 | GND | Р | Ground |
| 12 | GND | Р | Ground |
| 13 | TXD_OUT | 0 | Transmit Data |
| 14 | RXD_IN | I | Receive Data |
| 15 | RTS_OUT | 0 | Request to send |
| 16 | CTS_IN | I I | Clear to send |

Table 3-2 – CN2 Pinout

• CN3- External Screw Connector (alternative to CN2)

This is the interface where the RS232 connections, GPIO input and outputs are connected. There are also power and ground pins on this interface.

| Pin No. | Name | Туре | Description |
|---------|---------|------|-----------------|
| 1 | TXD_OUT | 0 | Transmit data |
| 2 | RXD_IN | I | Receive data |
| 3 | RTS_OUT | О | Request to send |
| 4 | CTS_IN | Ι | Clear to send |
| 5 | GND | Р | Ground |
| 6 | I/P1 | Ι | Input 1 |
| 7 | I/P2 | I | Input 2 |
| 8 | I/P3 | Ι | Input 3 |
| 9 | I/P4 | Ι | Input 4 |
| 10 | O/P1 | 0 | Output 1 |
| 11 | O/P2 | 0 | Output 2 |
| 12 | O/P3 | 0 | Output 3 |
| 13 | O/P4 | 0 | Output 4 |
| 14 | GND | P | Ground |

Table 3-3 – CN3 Pinout



• JP1- Output Drive Select

This jumper provides the option to select the power supply voltage for the inputs and outputs.

| Jumper position | Description | |
|-----------------|-----------------------|--|
| Short pin 1-2 | 3.3V selected | |
| Short pin 2-3 | 5V selected (default) | |

Table 3-4 – JP1 Pin options

3.2.3 VI800A-232U Components

• U1 - SC16IS760

This converts the SPI signals from the VM800P Plus board to UART TTL signals.

• U4 – ZT3222F

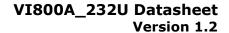
This converts the UART signals from the TTL level to RS232 level.

• LED1 – LED4

Indicates the status of GPIO inputs. Illuminate when the GPIO line is logic 0.

• LED5 -LED8

Indicates the status of the GPIO outputs. Illuminate when the GPIO line is logic 0.





4 Board Schematics

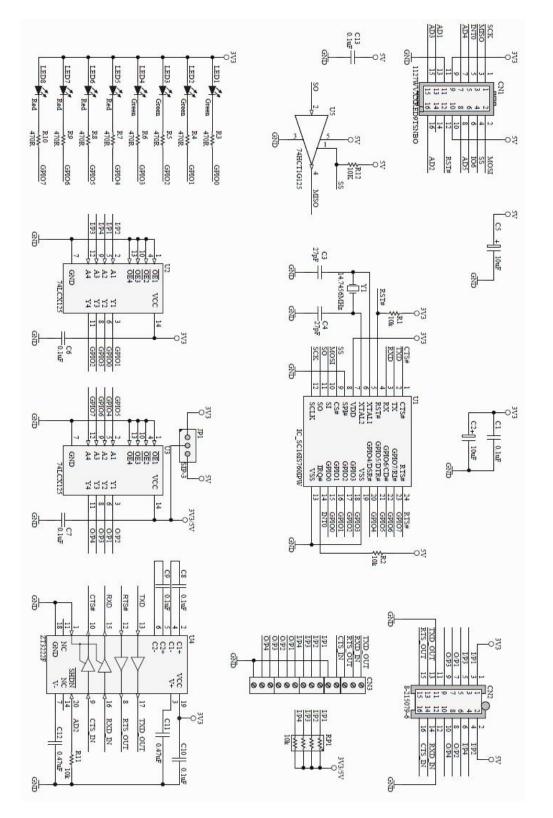


Figure 4-1 - VI800A-232U Schematics

5 Hardware Setup Guide

5.1 Power Configuration

The board is powered from the VM800P Plus board. The CN1 connector on the VI800A-232U board should be connected to the J6 connector of the VM800P plus board as shown in the Figure 5-1.



Figure 5-1 - VI800A-232U module connected to VM800P Plus module

5.2 RS232 Interface connection

The TXD_OUT signal on CN3 is connected to the RXD signal on the external RS232 device. The RXD_IN signal on CN3 is connected to the TXD signal on the external RS232 device. The RTS_OUT signal on CN3 is connected to the CTS signal on the external RS232 device. The CTS_IN signal on CN3 is connected to the RTS signal on the external RS232 device. The GND signal on CN3 is connected to the GND signal on the external RS232 device. The output from the external device is connected to the inputs I/P1, I/P2, I/P3 and I/P4 on CN3. The outputs O/P1, O/P2, O/P3 and O/P4 on CN3 are connected to the input of the external device. The LEDs LED1 to LED8 are used to display the status of the inputs and outputs. This interface is used to interface the VM800P Plus module to the devices having RS232 and GPIO interfaces.



6 Contact Information

Head Quarters – Singapore

Bridgetek Pte Ltd 178 Paya Lebar Road, #07-03 Singapore 409030 Tel: +65 6547 4827 Fax: +65 6841 6071 Branch Office – Taipei, Taiwan

Bridgetek Pte Ltd, Taiwan Branch 2 Floor, No. 516, Sec. 1, Nei Hu Road, Nei Hu District Taipei 114 Taiwan, R.O.C. Tel: +886 (2) 8797 5691 Fax: +886 (2) 8751 9737

E-mail (Sales) E-mail (Support) <u>sales.apac@brtchip.com</u> <u>support.apac@brtchip.com</u>

Branch Office – Vietnam

E-mail (Sales)

E-mail (Support)

Bridgetek Pte. Ltd. Unit 1, 2 Seaward Place, Centurion Business Park Glasgow G41 1HH United Kingdom Tel: +44 (0) 141 429 2777 Fax: +44 (0) 141 429 2758

Branch Office - Glasgow, United Kingdom

Bridgetek VietNam Company Limited Lutaco Tower Building, 5th Floor, 173A Nguyen Van Troi, Ward 11, Phu Nhuan District, Ho Chi Minh City, Vietnam Tel : 08 38453222 Fax : 08 38455222

E-mail (Sales) E-mail (Support)

<u>sales.emea@brtichip.com</u> support.emea@brtchip.com

E-mail (Sales) E-mail (Support) sales.apac@brtchip.com
support.apac@brtchip.com

sales.apac@brtchip.com

support.apac@brtchip.com

Web Site

http://brtchip.com/

Distributor and Sales Representatives

Please visit the Sales Network page of the <u>Bridgetek Web site</u> for the contact details of our distributor(s) and sales representative(s) in your country.

System and equipment manufacturers and designers are responsible to ensure that their systems, and any Future Technology Devices International Ltd (FTDI) devices incorporated in their systems, meet all applicable safety, regulatory and system-level performance requirements. All application-related information in this document (including application descriptions, suggested FTDI devices and other materials) is provided for reference only. While FTDI has taken care to assure it is accurate, this information is subject to customer confirmation, and FTDI disclaims all liability for system designs and for any applications assistance provided by FTDI. Use of FTDI devices in life support and/or safety applications is entirely at the user's risk, and the user agrees to defend, indemnify and hold harmless FTDI from any and all damages, claims, suits or expense resulting from such use. This document is subject to change without notice. No freedom to use patents or other intellectual property rights is implied by the publication of this document. Neither the whole nor any part of the information contained in, or the product described in this document, may be adapted or reproduced in any material or electronic form without the prior written consent of the copyright holder. Future Technology Devices International Ltd, Unit 1, 2 Seaward Place, Centurion Business Park, Glasgow G41 1HH, United Kingdom. Scotland Registered Company Number: SC136640



Appendix A – References

Document References

VM800P Datasheet: <u>VM800P Plus board</u> FT800 datasheet: <u>FT800 Embedded Video Engine</u> FT800 software programming guide: <u>FT800 Programmer Guide</u>

FT800 sample application notes: <u>AN 246 VM800CB SampleAPP Arduino Introduction</u> <u>AN 275 FT800 Example with Arduino</u> <u>AN 318 Arduino Library for FT800 Series</u> <u>AN 330 VI800A TTL 232U N485U ArduinoLibrary Sample</u>



Appendix B - List of Figures and Tables

List of Figures

| Figure 3-1 – VI800A-232U module | 4 |
|---|----|
| Figure 3-2 - VI800A-232U module PCB Top view | 5 |
| Figure 3-3 - VI800A-232U module PCB Bottom view | 5 |
| Figure 3-4 - VI800A-232U module PCB Side view | 6 |
| Figure 4-1 - VI800A-232U Schematics | 9 |
| Figure 5-1 - VI800A-232U module connected to VM800P Plus module | 10 |

List of Tables

| Table 2-1 – Ordering information | 2 |
|----------------------------------|---|
| Table 3-1 – CN1 Pinout | 6 |
| Table 3-2 – CN2 Pinout | 7 |
| Table 3-3 – CN3 Pinout | 7 |
| Table 3-4 – JP1 Pin options | 8 |



Appendix C – Revision History

| Document Title: | VI800A_232U Datasheet | |
|-------------------------|----------------------------|--|
| Document Reference No.: | BRT_000009 | |
| Clearance No.: | BRT#013 | |
| Product Page: | http://brtchip.com/product | |
| Document Feedback: | Send Feedback | |
| | | |

| Revision | Changes | Date |
|-------------|--|------------|
| Version 1.0 | Initial Release | 2014-10-14 |
| Version 1.1 | Added height dimensions | 2014-10-20 |
| Version 1.2 | Dual branding to reflect the migration of the product to the Bridgetek name – logo changed, copyright changed, contact information changed | 2016-09-13 |

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Daughter Cards & OEM Boards category:

Click to view products by FTDI manufacturer:

Other Similar products are found below :

ADZS-21262-1-EZEXT 27911 MPC5777C-416DS KITMPC5744DBEVM SPC56ELADPT144S TMDXRM46CNCD DM160216 EV-ADUCM350GPIOTHZ EV-ADUCM350-BIO3Z ATSTK521 EXP-IO EXP-PROTO EXP-RELAY EXP-SER 1130 MA160015 MA240013 MA240026 MA320014 MA330014 MA330017 TMDSCNCD28054MISO MIKROE-2152 MIKROE-2154 MIKROE-2381 TSSOP20EV MIKROE-1108 MIKROE-1516 SPS-READER-GEVK AC244049 AC244050 AC320004-3 2077 ATSMARTCARD-XPRO EIC - Q600 -230 ATZB-212B-XPRO SPC560PADPT100S SPC560BADPT64S MA180018 EIC - Q600 - 220 AC164134-1 BOB-12035 STM8/128-D/RAIS AC164127-6 AC164127-4 AC164134-3 AC164156 MA320021 MA320024 DFR0285