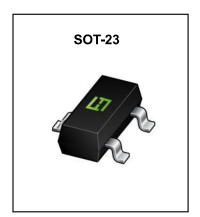


#### FEATURES

- Ldeally suited for automatic insertion
- Epitaxial planar die construction
- Complementary PNP type available(BC807)

### MAXIMUM RATINGS (T<sub>a</sub>=25°C unless otherwise noted)

Symbol	Parameter	Value	Unit
V <sub>сво</sub>	Collector-Base Voltage	50	V
V <sub>CEO</sub>	Collector-Emitter Voltage	45	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
lc	Collector Current	500	mA
Pc	Collector Power Dissipation	300	mW
R <sub>OJA</sub>	Thermal Resistance From Junction To Ambient	417	°C/W
T <sub>J</sub> ,T <sub>stg</sub>	Operation Junction and Storage Temperature Range	-55~+150	°C



#### ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25<sup>°</sup>C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Collector-base breakdown voltage	V <sub>CBO</sub>	I <sub>C</sub> = 10μΑ, I <sub>E</sub> =0	50			V
Collector-emitter breakdown voltage	V <sub>CEO</sub>	ν <sub>CEO</sub> I <sub>C</sub> = 10mA, I <sub>B</sub> =0				V
Emitter-base breakdown voltage	V <sub>EBO</sub>	Ι <sub>Ε</sub> = 1μΑ, Ι <sub>C</sub> =0	5			V
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = 45 V , I <sub>E</sub> =0			0.1	μA
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = 4V, I <sub>C</sub> =0			0.1	μA
DC aurrent ania	h <sub>FE(1)</sub>	V <sub>CE</sub> = 1V, I <sub>C</sub> = 100mA	100		600	
DC current gain	h <sub>FE(2)</sub>	V <sub>CE</sub> = 1V, I <sub>C</sub> = 500mA	40			
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = 500mA, I <sub>B</sub> = 50mA			0.7	V
Base-emitter saturation voltage	V <sub>BE</sub> (sat)	I <sub>C</sub> = 500mA, I <sub>B</sub> = 50mA			1.2	V
Base-emitter voltage	V <sub>BE</sub>	V <sub>CE</sub> = 1 V, I <sub>C</sub> = 500mA			1.2	V
Collecter capactiance	C <sub>ob</sub>	V <sub>CB</sub> =10V ,f=1MHz		10		pF
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 10mA f=100MHz	100			MHz

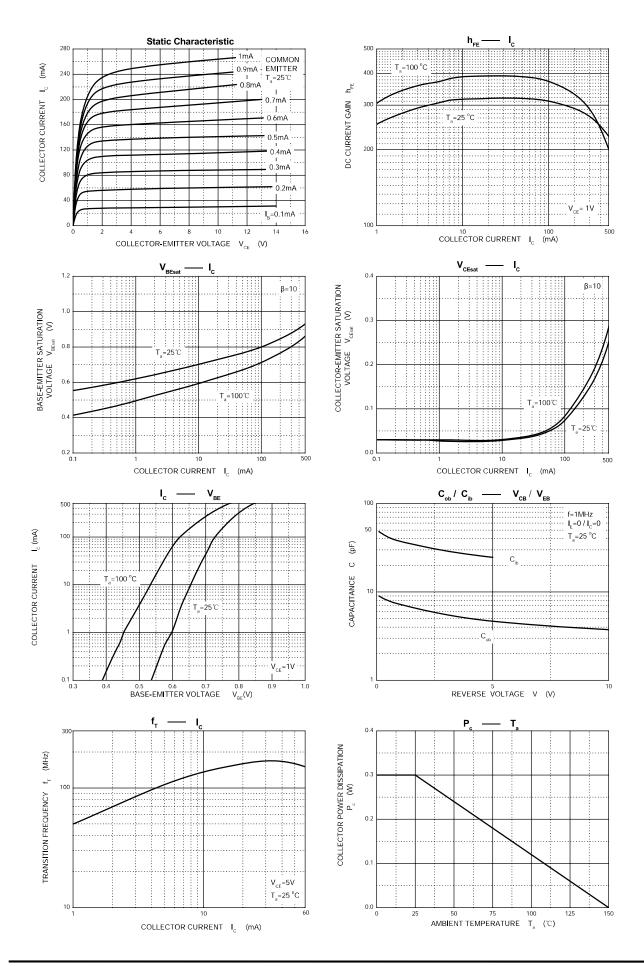
#### CLASSIFICATION OF hFE (1)

Rank	BC817-16	BC817-25	BC817-40
Range	100-250	160-400	250-600
Marking	6A	6B	6C



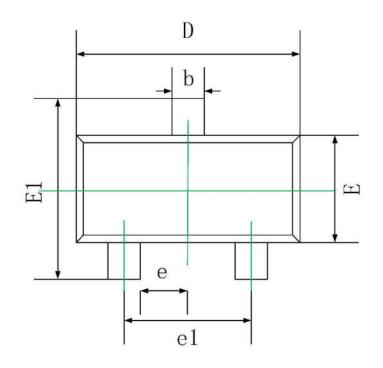
# BC817-16/25/40

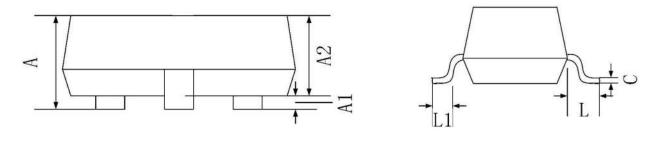
General Purpose Transistors NPN Silicon





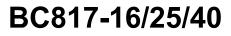
## **SOT-23 Package Information**





Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
A	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP.		0.037 TYP.		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF.		0.022 REF.		
L1	0.300	0.500	0.012	0.020	



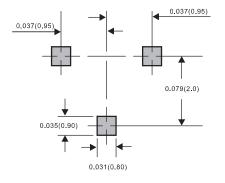


## **Pinning information**

Pin	Simplified outline	Symbol	
PinB Base PinC Collector PinE Emitter		воссов	

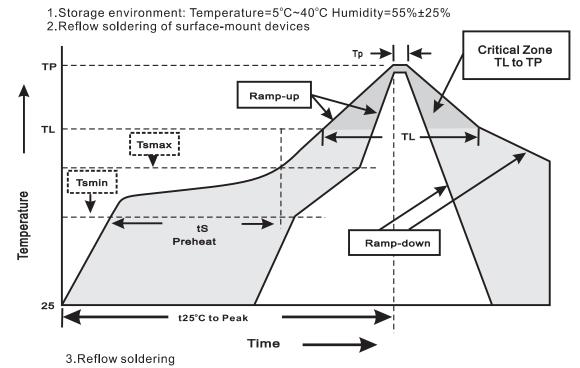
## Suggested solder pad layout

SOT-23



Dimensions in inches and (millimeters)





## Suggested thermal profiles for soldering processes

Profile Feature	Soldering Condition
Average ramp-up rate(T⊾ to TP)	<3°C/sec
Preheat -Temperature Min(Tsmin) -Temperature Max(Tsmax) -Time(min to max)(ts)	150°C 200°C 60~120sec
Tsmax to T∟ -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T∟) -Time(t∟)	217°C 60~260sec
Peak Temperature(T <sub>P</sub> )	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(tբ)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes

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