

## Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	$I_b$
100V	280mΩ@10V	2A
	310mΩ@4.5V	

## Feature

- Advanced trench process technology
- High density cell design for ultra low on-resistance

## Application

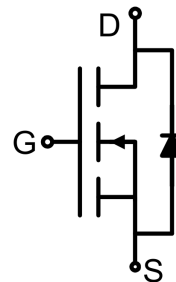
- Load Switch for Portable Devices
- DC/DC Converter

## Package

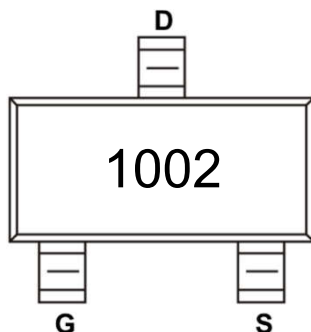


SOT-23

## Circuit diagram



## Marking



**Absolute maximum ratings (Ta=25°C unless otherwise noted)**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	±20	V
Continuous Drain Current	$I_D$	2	A
Pulsed Drain Current	$I_{DM}$	8	A
Power Dissipation	$P_D$	1.2	W
Junction Temperature	$T_J$	150	°C
Storage Temperature	$T_{STG}$	-55 ~ +150	°C

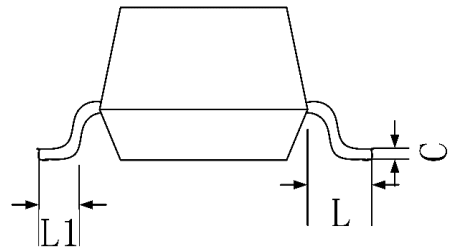
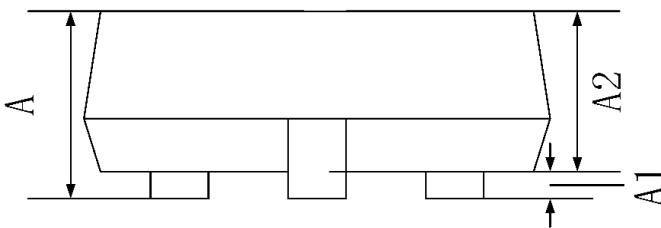
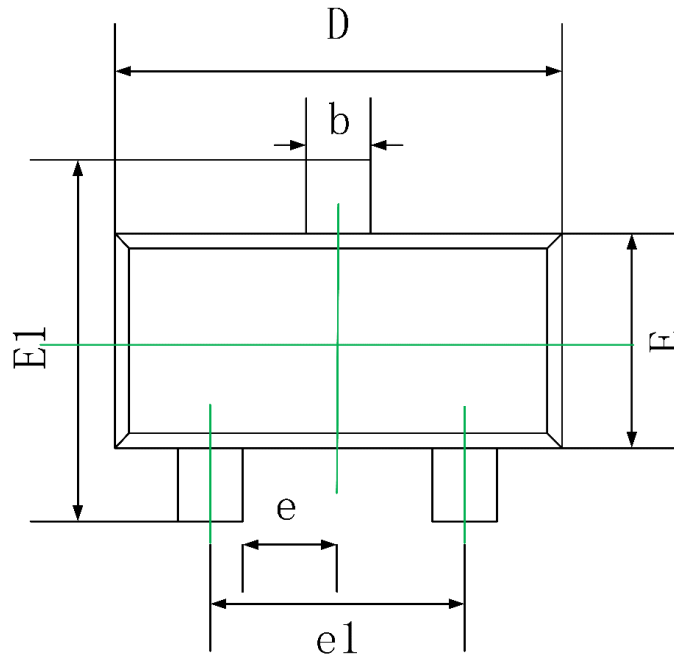
**Electrical characteristics (T<sub>A</sub>=25 °C, unless otherwise noted)**

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100			V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 100V, V_{GS} = 0V$			1	μA
Gate-body leakage current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.1		3.0	V
Drain-source on-resistance <sup>1)</sup>	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 2A$			280	mΩ
		$V_{GS} = 4.5V, I_D = 2A$			310	
<b>Dynamic characteristics<sup>2)</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 50V, V_{GS} = 0V, f = 1MHz$		330		pF
Output Capacitance	$C_{oss}$			88		
Reverse Transfer Capacitance	$C_{rss}$			17		
Total Gate Charge	$Q_g$	$V_{DS} = 50V, V_{GS} = 10V, I_D = 2A$		5.3		nC
Gate-Source Charge	$Q_{gs}$			1.4		
Gate-Drain Charge	$Q_{gd}$			1.8		
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 50V, V_{GS} = 10V, I_D = 1.3A, R_{GEN} = 1\Omega, R_L = 39\Omega$		14		nS
Turn-on rise time	$t_r$			54		
Turn-off delay time	$t_{d(off)}$			18		
Turn-off fall time	$t_f$			11		
<b>Source-Drain Diode characteristics</b>						
Diode Forward Current <sup>1)</sup>	$I_S$				2.0	A
Diode Forward voltage	$V_{DS}$	$V_{GS} = 0V, I_S = 2A$			1.2	V

Notes:

- 1) Pulse Test: Pulse Width < 300μs, Duty Cycle ≤2%.
- 2) Guaranteed by design, not subject to production testing.

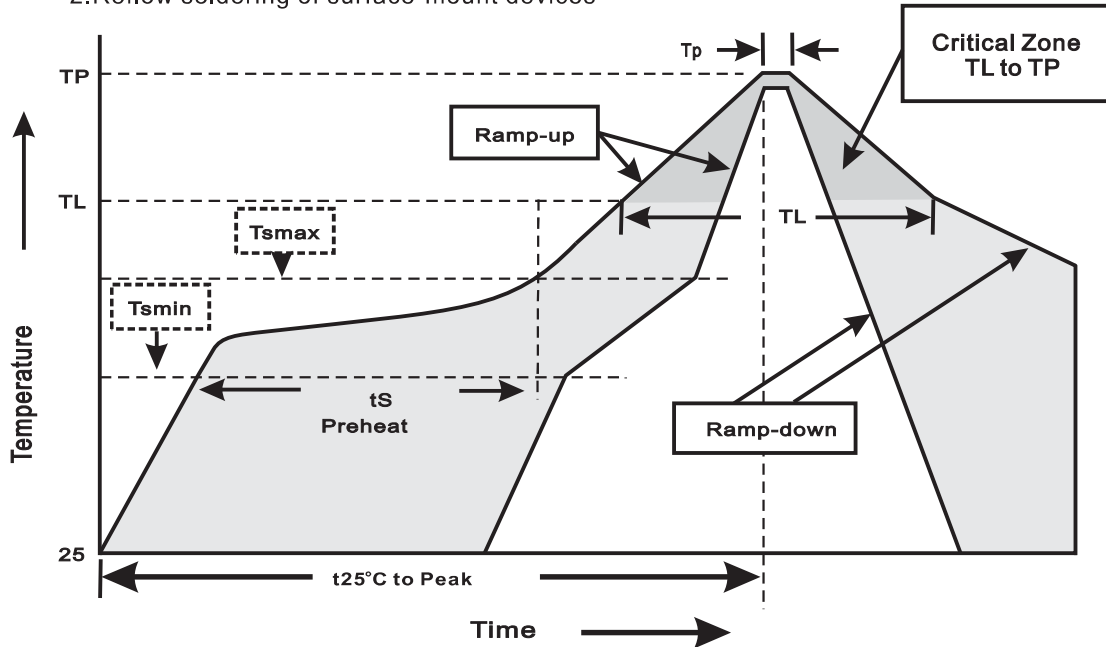
## SOT-23 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020

**Suggested thermal profiles for soldering processes**

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T <sub>L</sub> to T <sub>P</sub> )	<3°C/sec
Preheat -Temperature Min(T <sub>smin</sub> ) -Temperature Max(T <sub>smax</sub> ) -Time(min to max)(t <sub>s</sub> )	150°C 200°C 60~120sec
T <sub>smax</sub> to T <sub>L</sub> -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T <sub>L</sub> ) -Time(t <sub>L</sub> )	217°C 60~260sec
Peak Temperature(T <sub>P</sub> )	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t <sub>P</sub> )	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes

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