

Product Summary

Symbol	Value	Unit
$I_{T(RMS)}$	0.8	A
$V_{DRM} V_{RRM}$	600 / 800	V
I_{GT}	10~200	μA

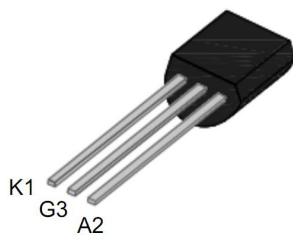
Feature

With high ability to withstand the shock loading of large current, Provide high dv/dt rate with strong resistance to electromagnetic interference.

Application

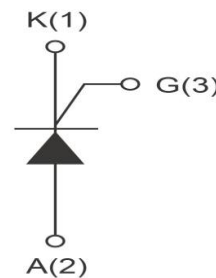
Power charger, T-tools, massager, solid state relay, AC Motor speed regulation and so on.

Package

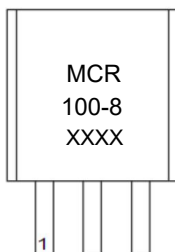


TO-92

Circuit diagram



Marking



Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Repetitive peak off-state voltage	V _{DRM}	600 / 800	V
Repetitive peak reverse voltage	V _{RPM}	600 / 800	V
RMS on-state current	I _{T(RMS)}	0.8	A
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I _{TSM}	8	A
I ² t value for fusing (tp=10ms)	I ² t	0.32	A ² s
Critical rate of rise of on-state current (I _G = 2 × I _{GT})	di _T /dt	50	A/μs
Peak gate current	I _{GM}	0.2	A
Average gate power dissipation	P _{G(AV)}	0.1	W
Junction Temperature	T _J	-40 ~ +110	°C
Storage Temperature	T _{STG}	-40 ~ +150	°C

Electrical characteristics (T_A=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Value		Unit
			Min	Max	
Gate trigger current	I _{GT}	V _D = 12V I _T =10mA T _J =25°C	10	200	μA
Gate trigger voltage	V _{GT}		-	0.8	V
Gate non-trigger voltage	V _{GD}	V _D = 1/2V _{DRM} T _J =110°C	0.2	-	V
latching current	I _L	V _D = 12V I _G =0.5mA R _{GK} =1kΩ T _J =25°C	-	3	mA
Holding current	I _H		-	4	mA
Critical-rate of rise of commutation voltage	dV _D /dt	V _D =2/3V _{DRM} Gate Open T _J =110°C	10	-	V/μs
STATIC CHARACTERISTICS					
Forward "on" voltage	V _{TM}	I _{TM} = 1.2A tp=380μs	-	1.5	V
Repetitive Peak Off-State Current	I _{DRM}	V _D = V _{DRM} V _R = V _{RPM}	T _J =25°C		μA
Repetitive Peak Reverse Current	I _{RPM}		T _J =110°C		mA
THERMAL RESISTANCES					
Thermal resistance	R _{th(j-c)}	Junction to case	TYP.	60	°C/W
	R _{th(j-a)}	Junction to ambient	TYP.	150	°C/W

Typical Characteristics

FIG.1: Maximum power dissipation versus RMS on-state current (full cycle)

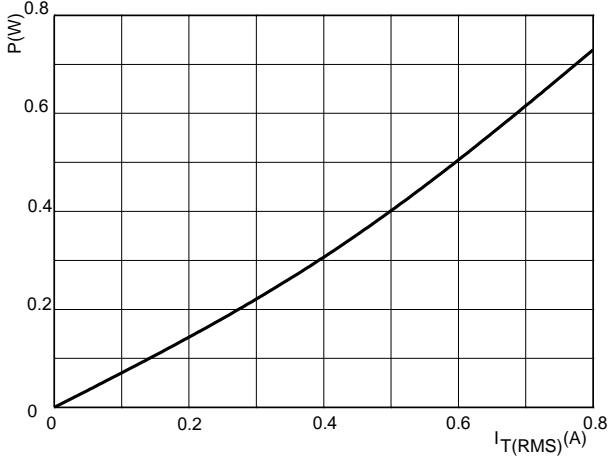


FIG.2: RMS on-state current versus case temperature (full cycle)

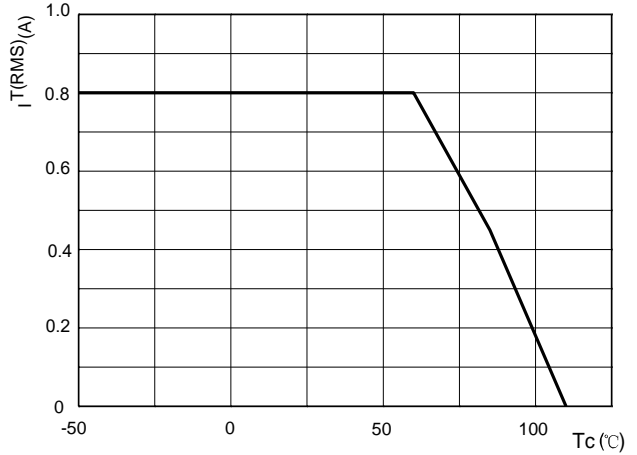


FIG.3: Surge peak on-state current versus number of cycles

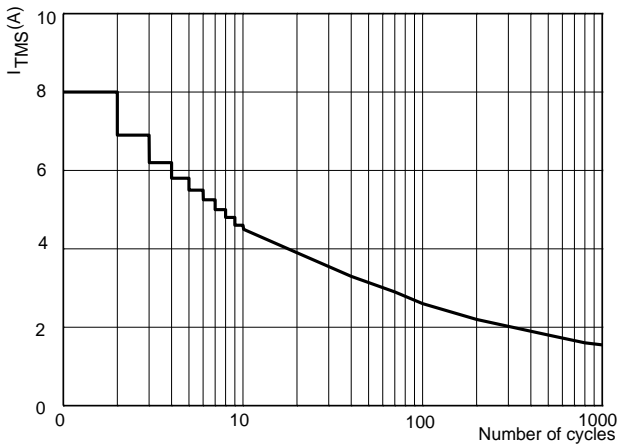


FIG.4: On-state characteristics (maximum values)

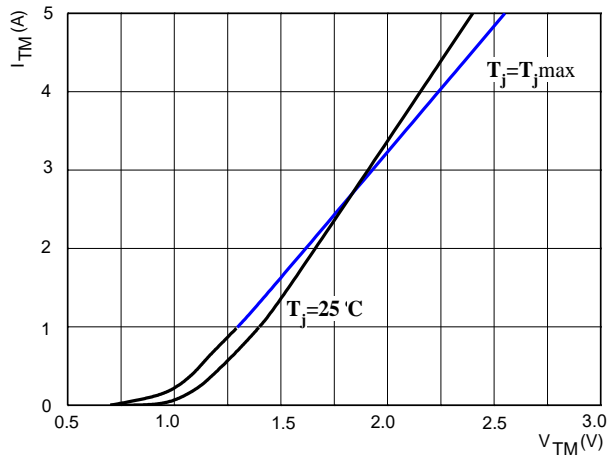


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10\text{ms}$

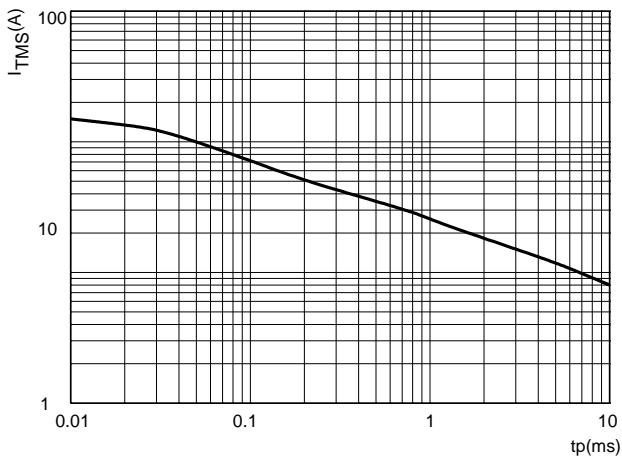
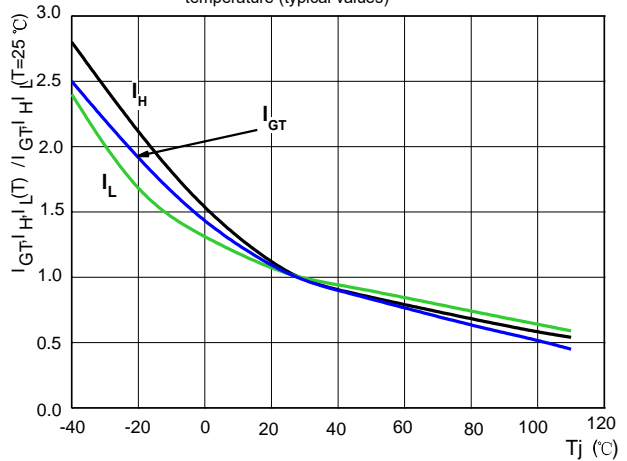
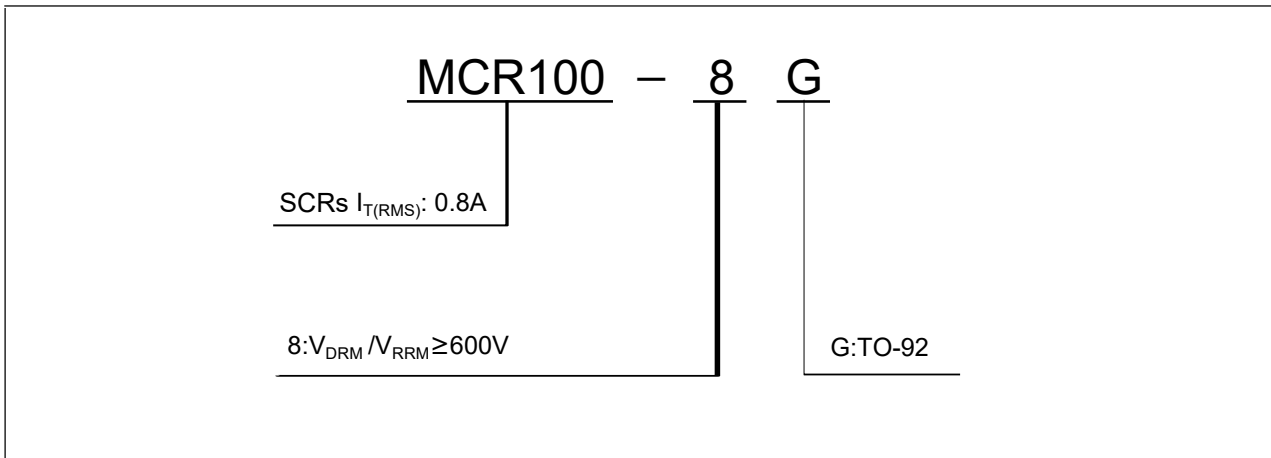


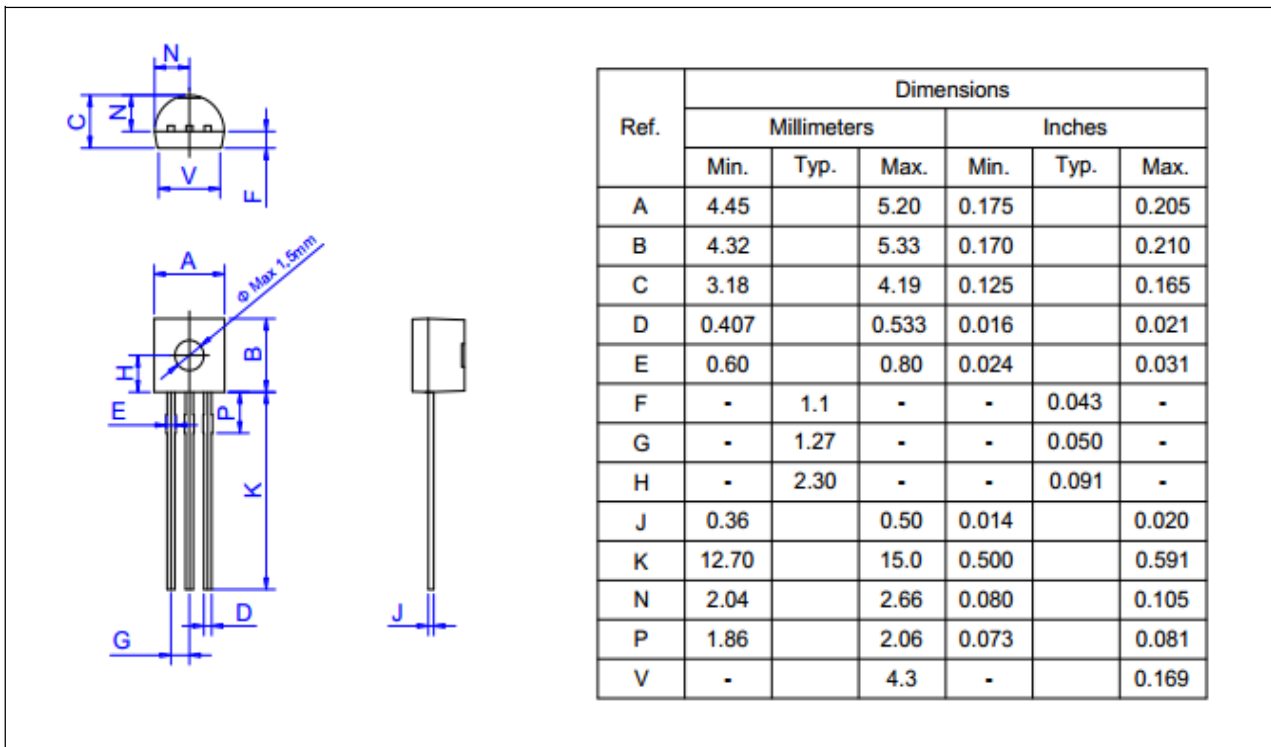
FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature (typical values)



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TO-92 Package Information



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