

## **MUR16xxFCT**

#### **16A Super Fast Recovery** Rectifiers - 200V-600V

## Features 💒 🥬





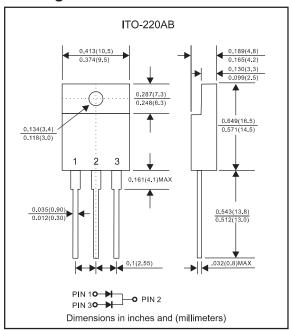


- Dual rectifier construction, positive centetap, offer 8.0A Half wave and 16.0A full wave rectification.
- High surge current capability.
- Super fast recovery time for switching mode application.
- Low power loss.
- Glass passivated chip junctions.
- · Lead-free parts meet environmental standards of MIL-STD-19500/228

#### Mechanical data

- Epoxy: UL94-V0 rated flame retardant
- Case: JEDEC ITO-220AB molded plastic body over passivated chip
- Lead: Axial leads, solderable per MIL-STD-202, Method 208 guranteed
- Polarity: As marked • Mounting Position : Any

#### Package outline



#### **Maximum ratings** (AT T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOLS	MUR1620FCT	MUR1640FCT	MUR1660FCT	UNIT
Maximum repetitive peak reverse voltage	VRRM	200	400	600	٧
Maximum RMS voltage	VRMS	140	280	420	V
Maximum DC blocking voltage	VDC	200	400	600	V
Maximum average forward rectified current	lo	16		А	
Peak forward surge current 8.3ms single half sine-wave(JEDEC method)	IFSM	100		А	
Operating junction temperature range	TJ	-55 to +150		°C	
Storage temperature range	Тѕтс	-55 to +150		°C	

## Electrical Characteristics (AT T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOLS	MUR1620FCT	MUR1640FCT	MUR1660FCT	UNIT
Maximum forward voltage per leg at IF=8 A	VF	0.99	1.30	1.80	V
Maximum reverse recovery time per leg (Note 1)	trr	50			ns
Maximum DC reverse current at TJ =25°C at rated DC blocking voltage per leg at TJ =125°C	lR	10 500		uA uA	

#### **Thermal Characteristics**

PARAMETER	SYMBOLS	MUR1620FCT	MUR1640FCT	MUR1660FCT	UNIT
Typical thermal resistance junction to case per leg	Rejc		2.5		°C/W

Note 1: Reverse recovery time test condition, I<sub>F</sub>=0.5A, I<sub>R</sub>=1.0A, I<sub>RR</sub>=0.25A





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#### Rating and characteristic curves

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

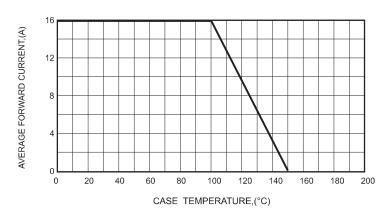
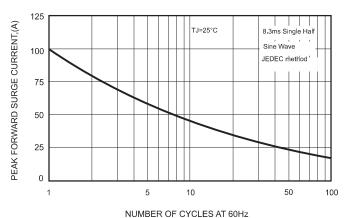
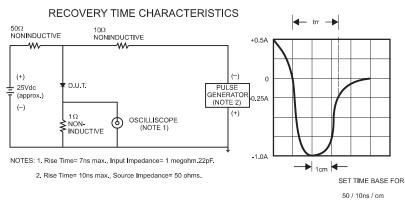


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT



#### FIG.5- TEST CIRCUIT DIAGRAM AND REVERSE



#### FIG.2-TYPICAL FORWARD

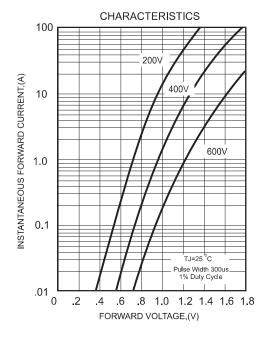
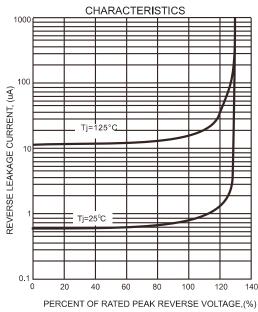


FIG.4 - TYPICAL REVERSE





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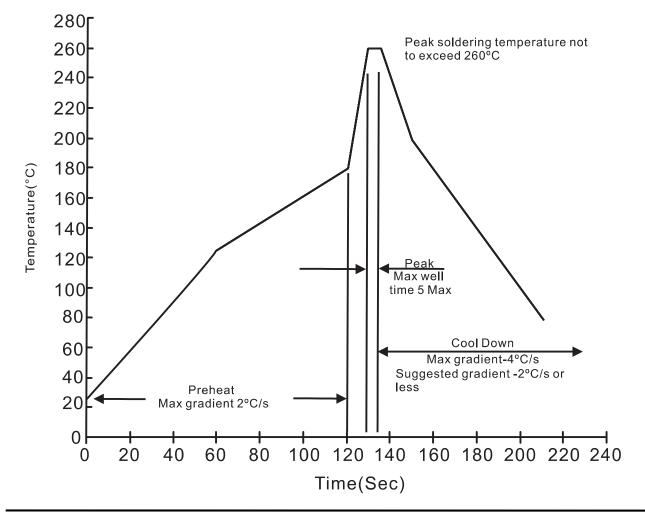
## **Pinning information**

Pin	Simplified outline	Symbol
Pin1 anode Pin2 cathode Pin3 anode		PIN 10

#### Marking

Type number	Marking code		
MUR1620FCT	MUR1620FCT		
MUR1640FCT	MUR1640FCT		
MUR1660FCT	MUR1660FCT		

#### Suggested thermal profiles for soldering processes



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