

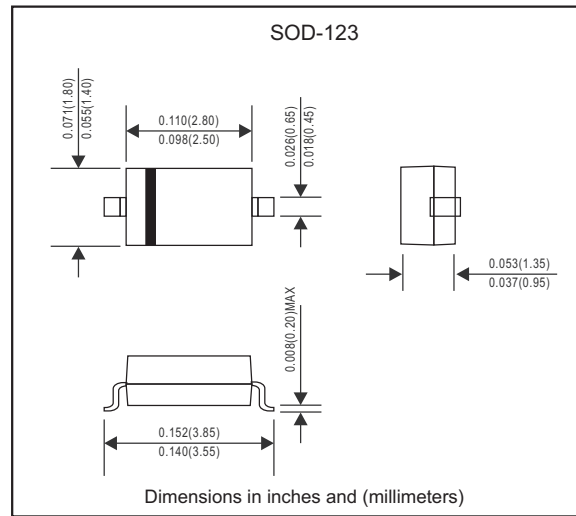
### Features

- ◆ For use in low voltage, high frequency inverters
- ◆ Free wheeling, and polarity protection applications

### Mechanical data

- ◆ **Case:** JEDEC SOD-123 molded plastic body
- ◆ **Terminals:** Solder plated, solderable per MIL-STD-750, Method 2026
- ◆ **Polarity:** Color band denotes cathode end
- ◆ **Mounting Position:** Any

### Package outline



### Maximum ratings and Electrical Characteristics (AT $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbols	SD103AW	SD103BW	SD103CW	Units	
Peak Repetitive Reverse Voltage	$V_{RRM}$	40	30	20	V	
RMS reverse voltage	$V_{RMS}$	28	21	14	V	
Working Peak Reverse Voltage	$V_{DC}$	40	30	20	V	
Peak Forward Surge Current, 8.3ms single half sine-wave superimposed on rated load (JEDEC method)	$I_{FSM}$	10			A	
Maximum Instantaneous Forward Voltage	$V_F$	$I_F=20\text{mA}$	0.37		V	
		$I_F=200\text{mA}$	0.60			
Power Dissipation	$P_D$	400			mW	
Reverse current	$I_R$	$V_R=30\text{V}$	5	–	–	uA
		$V_R=20\text{V}$	–	5	–	
		$V_R=10\text{V}$	–	–	5	
Thermal Resistance, Junction to Ambient Air	$R_{\theta JA}$	300			$^\circ\text{C/W}$	
Reverse voltage	$V_{(BR)}$	SD103AW	40		V	
		SD103BW	30			
		SD103CW	20			
Reverse recovery time	$t_{rr}$	$I_F=I_R=200\text{mA}, I_{rr}=0.1 \times I_R, R_L=100\Omega$			ns	
Forward Continuons Current	$I_{FM}$	350			mA	
Total capacitance	$C_{tot}$	$V_R=0\text{V}, f=1\text{MHZ}$			pF	
Junction temperature	$T_j$	125			$^\circ\text{C}$	
Storage temperature	$T_{stg}$	-55 ~ +150			$^\circ\text{C}$	

## Rating and characteristic curves

Fig.1 Power Derating Curve

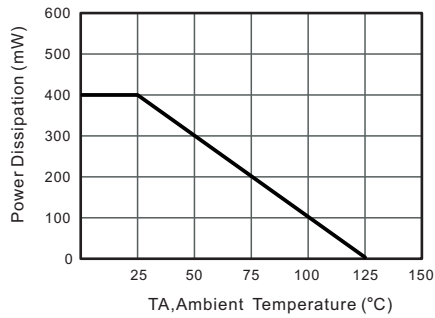


Fig.2 Typical Reverse Characteristics

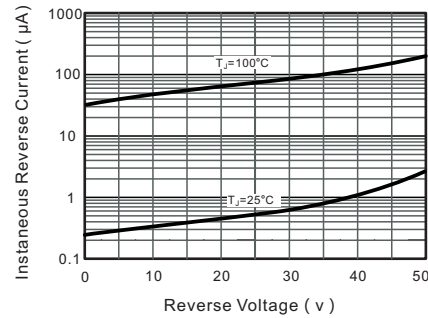


Fig.3 Forward Characteristics

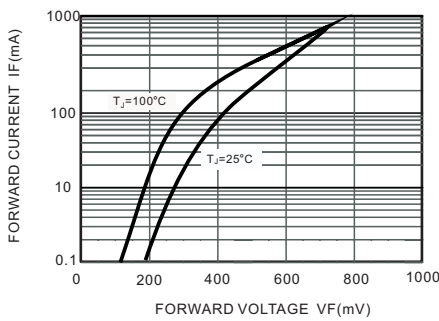


Fig.4 Maximum Non-Repetitive Peak Forward Surge Current

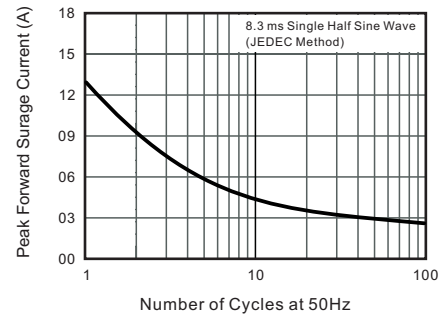


Fig.5 Typical Junction Capacitance

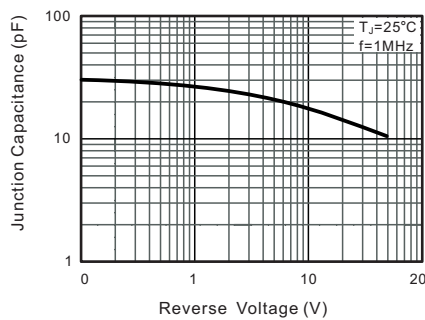
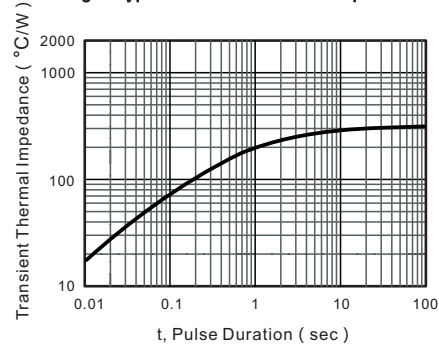


Fig.6 Typical Transient Thermal Impedance



## Marking

Type number	Marking code
SD103AW	S4
SD103BW	S5
SD103CW	S6

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