

Features

- I Wide Supply Voltage Range
- I Single Supply: 2.0V to 36V
- I Dual Supplies: $\pm 1.0V$ to $\pm 18V$
- I Low Supply Current Drain: 0.9mA
- I Low Input Bias Current: 25nA (Typical)
- I Low Input Offset Current: $\pm 5.0nA$ (Typical)
- I Low Input Offset Voltage: 2.0mV (Typical)
- I Input Common Mode Voltage Range Includes Ground
- I Differential Input Voltage Range Equals to the Power Supply Voltage
- I Low Output Saturation Voltage: 200mV at 4mA
- I Open Collector Output
- I Small Package:
LM339/339A Available in SOP-14 and TSSOP-14 Packages

General Description

The LM339/339A consist of four independent precision voltage comparators with a typical offset voltage of 2.0mV and high gain. They are specifically designed to operate from a single power supply over wide range of voltages. Operation from split power supply is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

The LM339/339A series are compatible with industry standard 339.

The LM339A has more stringent input offset voltage than the LM339.

The LM339 is available in SOP-14 and TSSOP-14 packages, and the LM339A is available in SOP-14 package.

Applications

- I Battery Charger
- I Cordless Telephone
- I Switching Power Supply
- I DC-DC Module
- I PC Motherboard
- I Communication Equipment

Pin Configuration

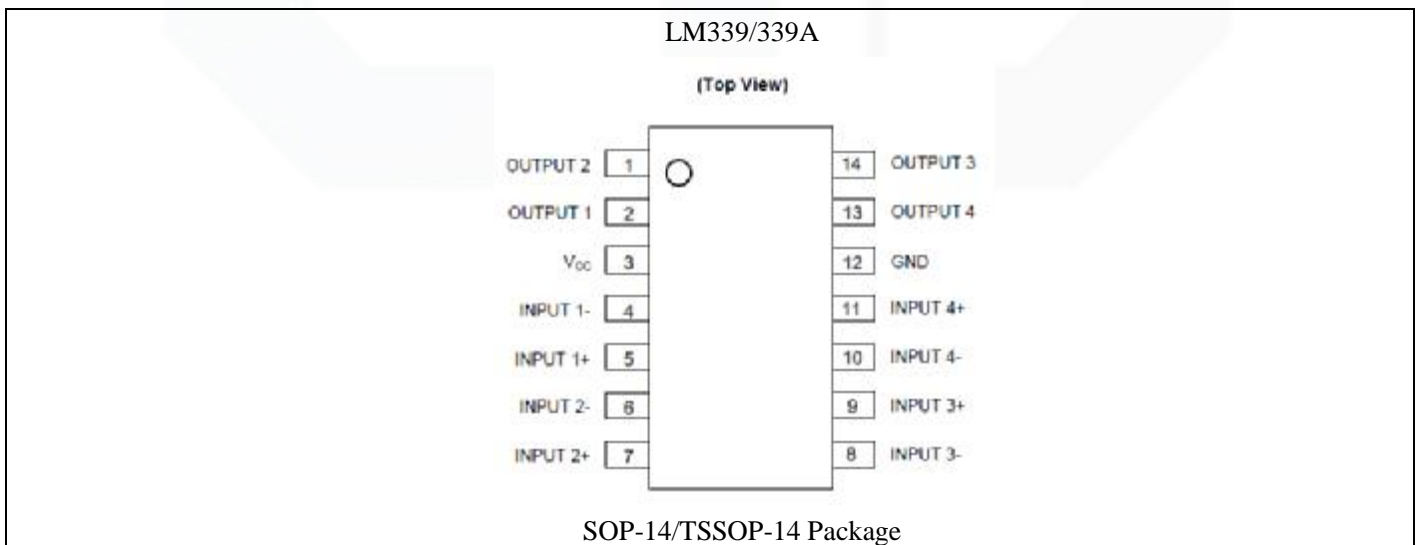


Figure 1. Pin Assignment Diagram

Functional Block Diagram

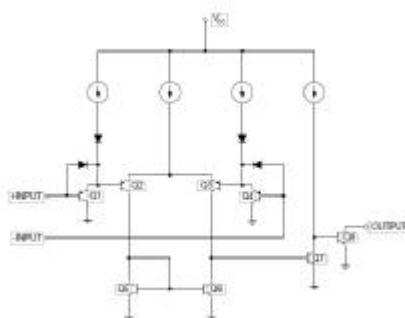


Figure 2. Functional Block Diagram of LM339/339A (Each comparator)

Absolute Maximum Ratings

Condition	Symbol	Max
Power Supply Voltage	V _{CC}	±20V or 40V
Differential input voltage	V _{I(DIFF)}	40V
Input Voltage	V _I	-0.3V~40V
Operating Temperature Range	T _{opr}	-25°C ~+125°C
Storage Temperature Range	T _{stg}	-65°C ~+150°C

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Note 2: This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action on the IC chip. This transistor action can cause the output voltages of the comparators to go to the V₊ voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than -0.3 VDC at 25°C).

Package/Ordering Information

MODEL	CHANNEL	ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION	MARKING INFORMATION
LM339	Quad	LM339-SR	SOP-14	Tape and Reel,2500	LM339
		LM339-TR	TSSOP-14	Tape and Reel,3000	LM339
LM339A	Quad	LM339A-SR	SOP-14	Tape and Reel,2500	LM339
		LM339A-TR	TSSOP-14	Tape and Reel,3000	LM339

Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
Supply Voltage	V_{CC}	2	36	V
Operating Temperature Range	T_A	-40	85	$^{\circ}\text{C}$

Electrical Characteristics

Limits in standard typeface are for $T_A=25^{\circ}\text{C}$, bold typeface applies over $T_A=-40^{\circ}\text{C}$ to 85°C (Note 3), $V_{CC}=5\text{V}$, $\text{GND}=0\text{V}$, unless otherwise specified.

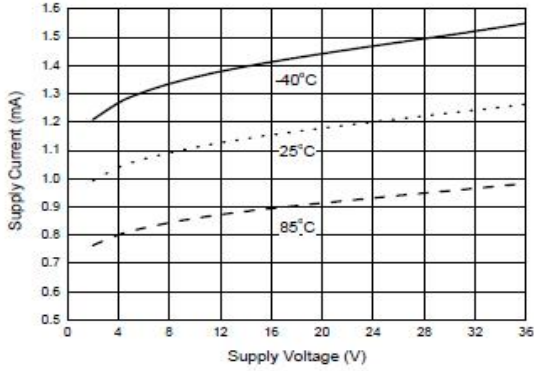
Parameter	Conditions		Min	Typ	Max	Unit
Input Offset Voltage	$V_O = 1.4\text{V}$, $R_S = 0\Omega$, V_{CC} from 5V to 30V	LM339	—	2	5	mV
			—	—	7	
		LM339A	—	2	3	
			—	—	5	
Input Bias Current	I_{IN+} or I_{IN-} with output in Linear Range, $V_{CM} = 0\text{V}$		—	25	250	nA
			—	—	400	
Input Offset Current	$I_{IN+} - I_{IN-}$, $V_{CM} = 0\text{V}$		—	5.0	50	nA
			—	—	200	
Input Common Mode Voltage Range (Note 7)	$V_{CC} = 30\text{V}$		0	—	$V_{CC}-1.5$	V
Supply Current	$R_L = \infty$	$V_{CC} = 5\text{V}$	—	0.9	2.0	mA
			—	—	3.0	
		$V_{CC} = 30\text{V}$	—	1.2	2.5	
			—	—	3.5	
Voltage Gain	$R_L \geq 15\text{k}\Omega$, $V_{CC} = 15\text{V}$, $V_O = 1\text{V}$ to 11V		50	200	—	V/mV
Large Signal Response Time	$V_{IN} = \text{TTL Logic Swing}$, $V_{REF} = 1.4\text{V}$, $V_{RL} = 5\text{V}$, $R_L = 5.1\text{k}\Omega$		—	200	—	ns
Response Time	$V_{RL} = 5\text{V}$, $R_L = 5.1\text{k}\Omega$		—	1.3	—	μs
Output Sink Current	$V_{IN-} = 1\text{V}$, $V_{IN+} = 0$, $V_O = 1.5\text{V}$		6.0	16	—	mA
Output Leakage Current	$V_{IN-} = 0\text{V}$, $V_{IN+} = 1\text{V}$, $V_O = 5\text{V}$		—	0.1	—	nA
	$V_{IN-} = 0\text{V}$, $V_{IN+} = 1\text{V}$, $V_O = 30\text{V}$		—	—	1	μA
Saturation Voltage	$V_{IN-} = 1\text{V}$, $V_{IN+} = 0$, $I_{SINK} \leq 4\text{mA}$		—	200	400	mV
			—	—	500	
Thermal Resistance (Junction to Case)	SO-14		—	15	—	$^{\circ}\text{C/W}$
	TSSOP-14		—	6	—	
Thermal Resistance (Junction to Ambient)	SO-14		—	89	—	
	TSSOP-14		—	125	—	

Note 3: These specifications are limited to $-40^{\circ}\text{C} \leq T_A \leq 85^{\circ}\text{C}$. Limits over temperature are guaranteed by design, but not tested in production.

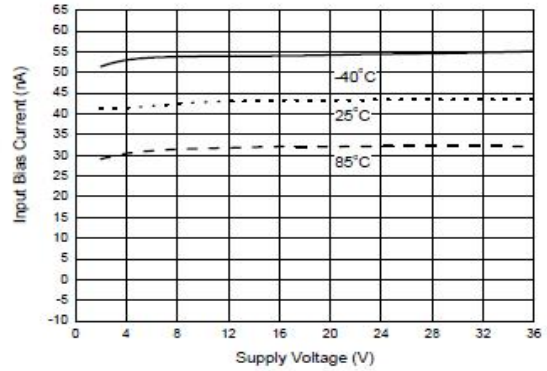
Note 4: The input common-mode voltage of either input signal voltage should not be allowed to go negatively by more than 0.3V (at 25°C). The upper end of the common-mode voltage range is $V_{CC}-1.5\text{V}$ (at 25°C), but either or both inputs can go to +36V without damages, independent of the magnitude of the VCC.

Typical Performance characteristics

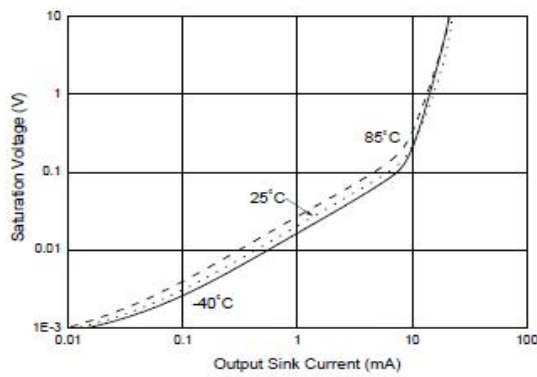
Supply Voltage vs. Supply Current



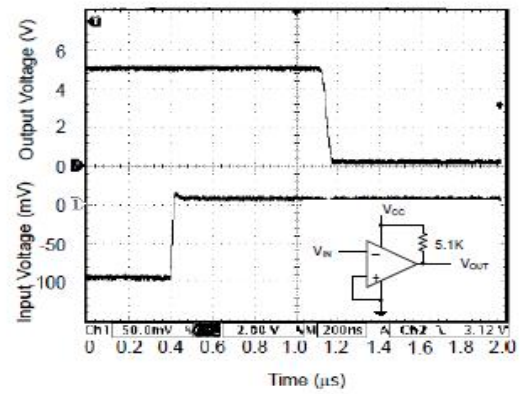
Supply Voltage vs. Input Bias Current



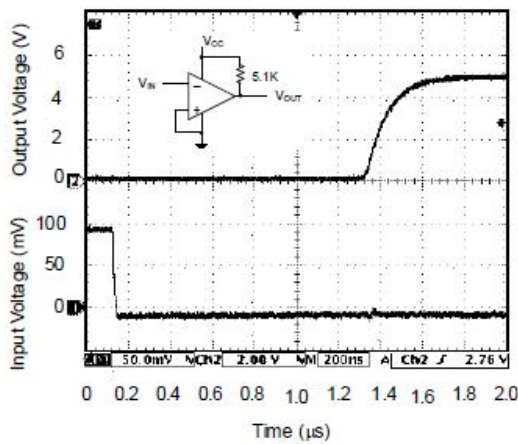
Output Sink Current vs. Saturation Voltage



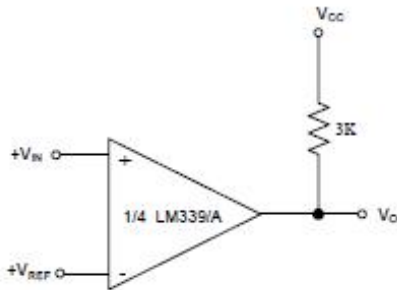
Response Time for 5mV Input Overdrive - Negative Transition



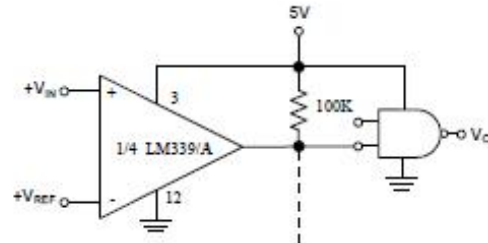
Response Time for 5mV Input Overdrive - Positive Transition



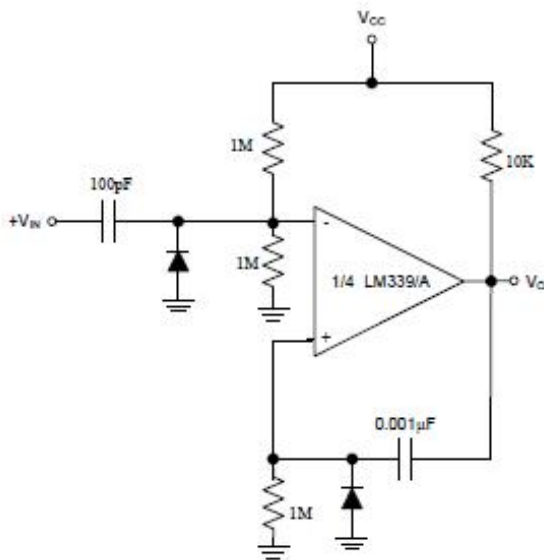
Typical Applications



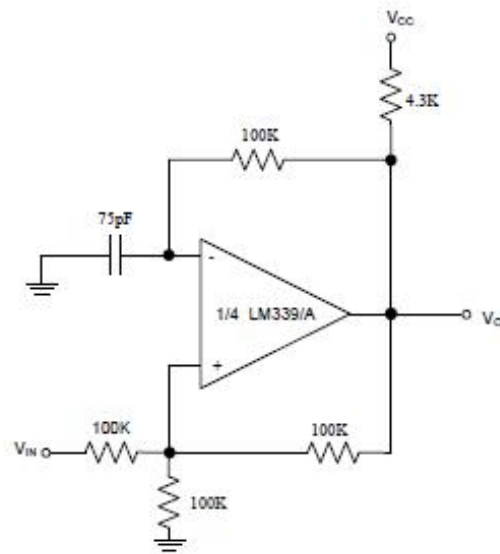
Basic Comparator



Driving CMOS



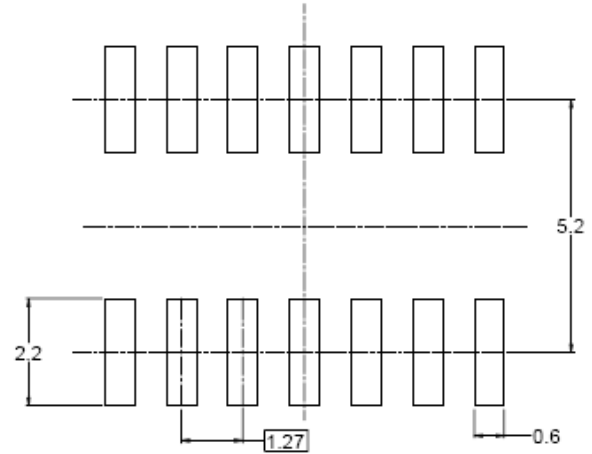
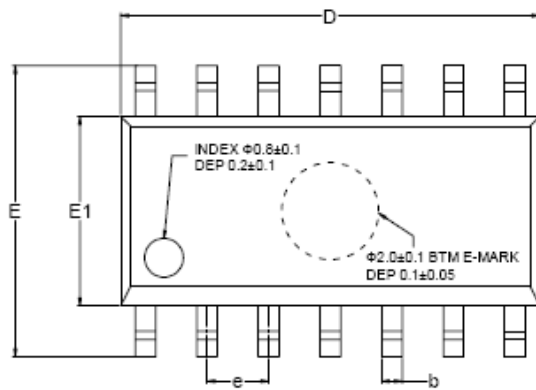
One Shot Multivibrator



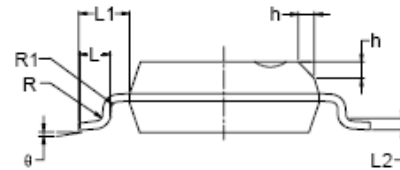
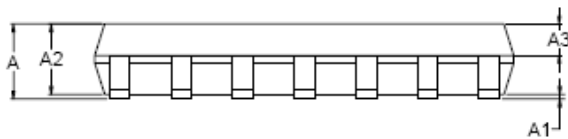
Squarewave Oscillator

Package Information

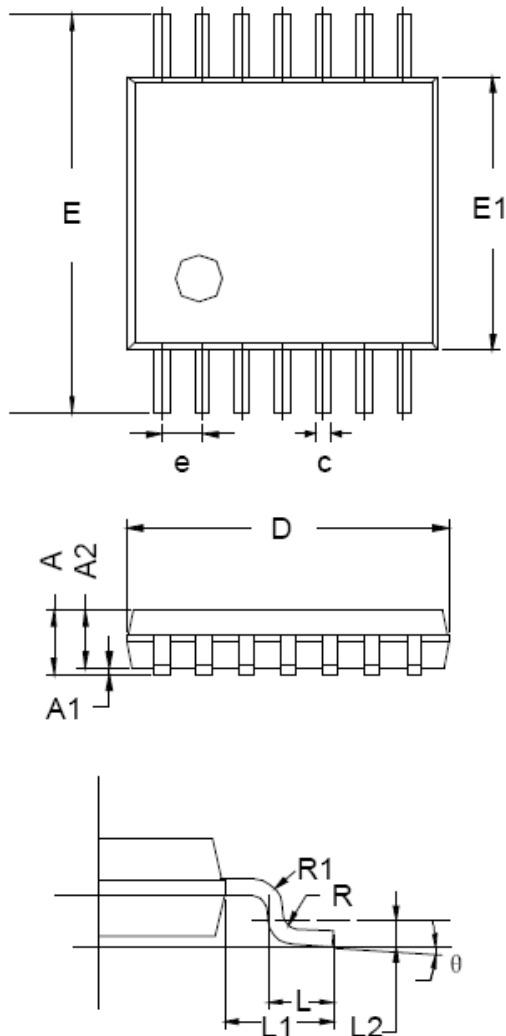
SOP-14



RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters			Dimensions In Inches		
	MIN	MOD	MAX	MIN	MOD	MAX
A	1.35		1.75	0.053		0.069
A1	0.10		0.25	0.004		0.010
A2	1.25		1.65	0.049		0.065
A3	0.55		0.75	0.022		0.030
b	0.36		0.49	0.014		0.019
D	8.53		8.73	0.336		0.344
E	5.80		6.20	0.228		0.244
E1	3.80		4.00	0.150		0.157
e	1.27 BSC			0.050 BSC		
L	0.45		0.80	0.018		0.032
L1	1.04 REF			0.040 REF		
L2	0.25 BSC			0.01 BSC		
R	0.07			0.003		
R1	0.07			0.003		
h	0.30		0.50	0.012		0.020
θ	0°		8°	0°		8°

TSSOP-14


Symbol	Dimensions In Millimeters		
	MIN	TYP	MAX
A	-	-	1.20
A1	0.05	-	0.15
A2	0.90	1.00	1.05
b	0.20	-	0.28
c	0.10	-	0.19
D	4.86	4.96	5.06
E	6.20	6.40	6.60
E1	4.30	4.40	4.50
e	0.65 BSC		
L	0.45	0.60	0.75
L1	1.00 REF		
L2	0.25 BSC		
R	0.09	-	-
θ	0°	-	8°

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