

Features

- **Single-Supply Operation from +2.1V ~ +5.5V**
- **Rail-to-Rail Input / Output**
- **Gain-Bandwidth Product: 1MHz (Typ)**
- **Low Input Bias Current: 1pA (Typ)**
- **Low Offset Voltage: $\pm 1\text{mV}$ (Max)**
- **Quiescent Current: 40 μA per Amplifier (Typ)**
- **Operating Temperature: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$**
- **Embedded RF Anti-EMI Filter**
- **Small Package:**
LMV321A Available in SOT23-5 Package
LMV358A Available in SOP-8 Package

General Description

The LMV321A/V358A family have a high gain-bandwidth product of 1MHz, a slew rate of 0.6V/ μs , and a quiescent current of 40 μA /amplifier at 5V. The LMV321A/V358A family is designed to provide optimal performance in low voltage and low noise systems. They provide rail-to-rail output swing into heavy loads. The input common mode voltage range includes ground, and the maximum input offset voltage is $\pm 1\text{mV}$ for LMV321A/V358A family. They are specified over the extended industrial temperature range (-40°C to $+125^{\circ}\text{C}$). The operating range is from 2.1V to 5.5V. The LMV321A single is available in Green SOT23-5 package. The LMV358A Dual is available in Green SOP-8 package.

Applications

- ASIC Input or Output Amplifier
- Sensor Interface
- Medical Communication
- Smoke Detectors
- Audio Output
- Piezoelectric Transducer Amplifier
- Medical Instrumentation
- Portable Systems

Pin Configuration

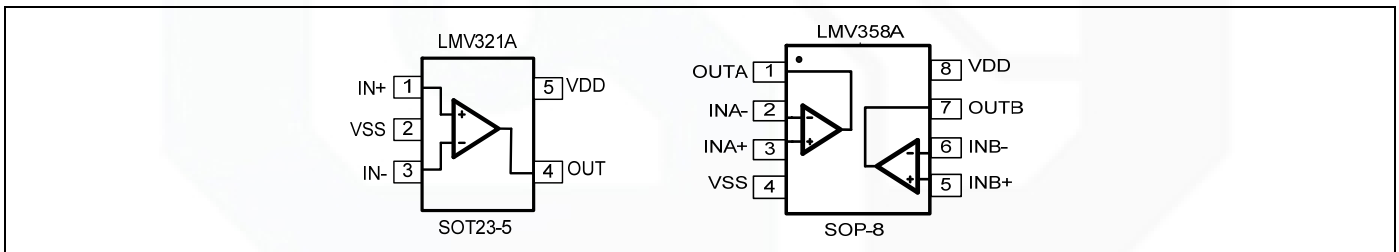


Figure 1. Pin Assignment Diagram

Absolute Maximum Ratings

| Condition | Min | Max |
|--|-----------------------|-----------------------|
| Power Supply Voltage (V _{DD} to V _{SS}) | -0.5V | +7.5V |
| Analog Input Voltage (IN+ or IN-) | V _{SS} -0.5V | V _{DD} +0.5V |
| PDB Input Voltage | V _{SS} -0.5V | +7V |
| Operating Temperature Range | -40°C | +125°C |
| Junction Temperature | +160°C | |
| Storage Temperature Range | -55°C | +150°C |
| Lead Temperature (soldering, 10sec) | +260°C | |
| Package Thermal Resistance (T_A=+25°C) | | |
| SOP-8, θ_{JA} | 125°C/W | |
| SOT23-5, θ_{JA} | 190°C/W | |
| ESD Susceptibility | | |
| HBM | 6KV | |
| MM | 300V | |

Note: Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions outside those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Package/Ordering Information

| MODEL | CHANNEL | ORDER NUMBER | PACKAGE DESCRIPTION | PACKAGE OPTION | MARKING INFORMATION |
|---------|---------|--------------|---------------------|--------------------|---------------------|
| LMV321A | Single | LMV321A-TR | SOT23-5 | Tape and Reel,3000 | 321 |
| LMV358A | Dual | LMV358A-SR | SOP-8 | Tape and Reel,4000 | LMV358 |

Electrical Characteristics

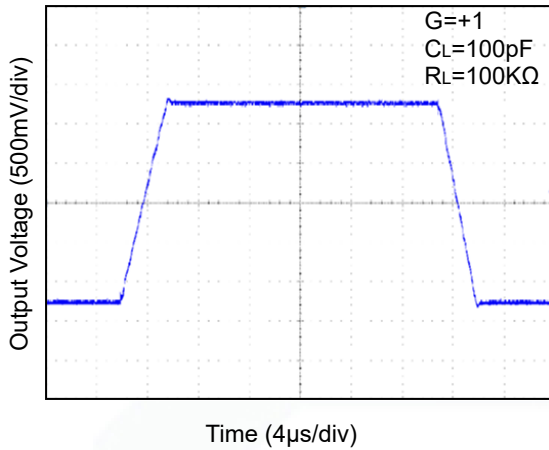
 (At $V_S = +5V$, $R_L = 100k\Omega$ connected to $V_S/2$, and $V_{OUT} = V_S/2$, unless otherwise noted.)

| PARAMETER | SYMBOL | CONDITIONS | LMV321A/V358A | | | | |
|---|--------------------------|--|---------------|--------------------------|----------------|------------------|---------|
| | | | TYP | MIN/MAX OVER TEMPERATURE | | | |
| | | | +25°C | +25°C | -40°C to +85°C | UNITS | MIN/MAX |
| INPUT CHARACTERISTICS | | | | | | | |
| Input Offset Voltage | V_{OS} | $V_{CM} = V_S/2$ | 0.1 | ± 1 | ± 1.2 | mV | MAX |
| Input Bias Current | I_B | | 1 | | | pA | TYP |
| Input Offset Current | I_{OS} | | 1 | | | pA | TYP |
| Common-Mode Voltage Range | V_{CM} | $V_S = 5.5V$ | -0.1 to +5.6 | | | V | TYP |
| Common-Mode Rejection Ratio | CMRR | $V_S = 5.5V, V_{CM} = -0.1V$ to 4V | 70 | 62 | 62 | dB | MIN |
| | | $V_S = 5.5V, V_{CM} = -0.1V$ to 5.6V | 68 | 56 | 55 | | |
| Open-Loop Voltage Gain | A_{OL} | $R_L = 5k\Omega, V_O = +0.1V$ to +4.9V | 80 | 70 | 70 | dB | MIN |
| | | $R_L = 10k\Omega, V_O = +0.1V$ to +4.9V | 100 | 94 | 85 | | |
| Input Offset Voltage Drift | $\Delta V_{OS}/\Delta T$ | | 2.7 | | | $\mu V/^\circ C$ | TYP |
| OUTPUT CHARACTERISTICS | | | | | | | |
| Output Voltage Swing from Rail | V_{OH} | $R_L = 100k\Omega$ | 4.997 | 4.990 | 4.980 | V | MIN |
| | V_{OL} | $R_L = 100k\Omega$ | 3 | 10 | 20 | mV | MAX |
| | V_{OH} | $R_L = 10k\Omega$ | 4.992 | 4.970 | 4.960 | V | MIN |
| | V_{OL} | $R_L = 10k\Omega$ | 8 | 30 | 40 | mV | MAX |
| Output Current | I_{SOURCE} | $R_L = 10\Omega$ to $V_S/2$ | 84 | 60 | 45 | mA | MIN |
| | I_{SINK} | | 75 | 60 | 45 | | |
| POWER SUPPLY | | | | | | | |
| Operating Voltage Range | | | | 2.1 | 2.5 | V | MIN |
| | | | | 5.5 | 5.5 | V | MAX |
| Power Supply Rejection Ratio | PSRR | $V_S = +2.5V$ to +5.5V, $V_{CM} = +0.5V$ | 82 | 60 | 58 | dB | MIN |
| Quiescent Current / Amplifier | I_Q | | 40 | 60 | 80 | μA | MAX |
| DYNAMIC PERFORMANCE (CL = 100pF) | | | | | | | |
| Gain-Bandwidth Product | GBP | | 1 | | | MHz | TYP |
| Slew Rate | SR | $G = +1, 2V$ Output Step | 0.6 | | | V/ μs | TYP |
| Settling Time to 0.1% | t_S | $G = +1, 2V$ Output Step | 5 | | | μs | TYP |
| Overload Recovery Time | | $V_{IN} \cdot Gain = V_S$ | 2.6 | | | μs | TYP |
| NOISE PERFORMANCE | | | | | | | |
| Voltage Noise Density | e_n | f = 1kHz | 27 | | | nV/\sqrt{Hz} | TYP |
| | | f = 10kHz | 20 | | | nV/\sqrt{Hz} | TYP |

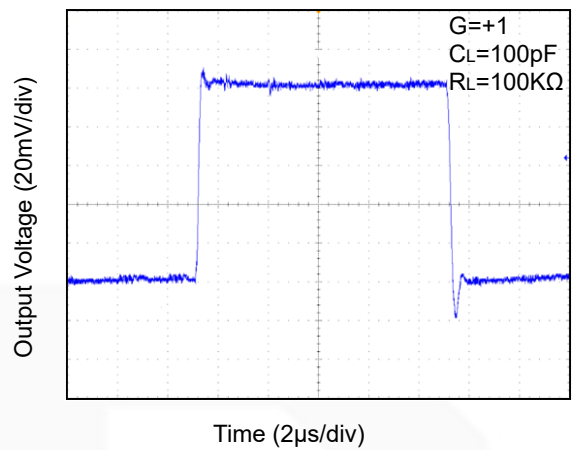
Typical Performance characteristics

At $T_A=+25^{\circ}\text{C}$, $V_S=+5\text{V}$, and $R_L=100\text{K}\Omega$ connected to $V_S/2$, unless otherwise noted.

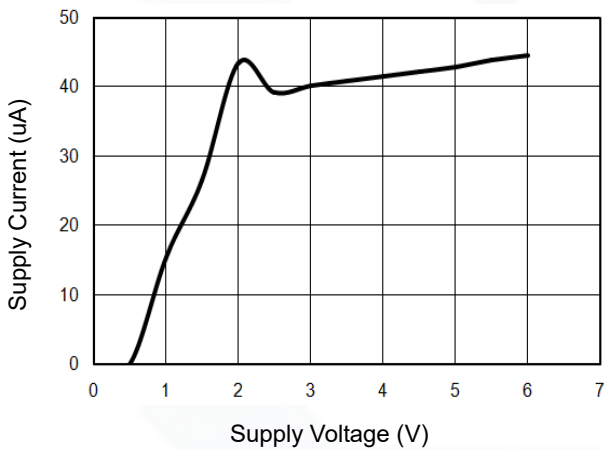
Large-Signal Step Response



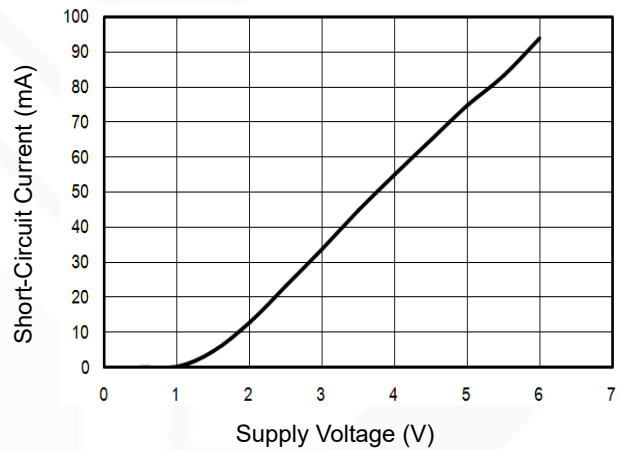
Small-Signal Step Response



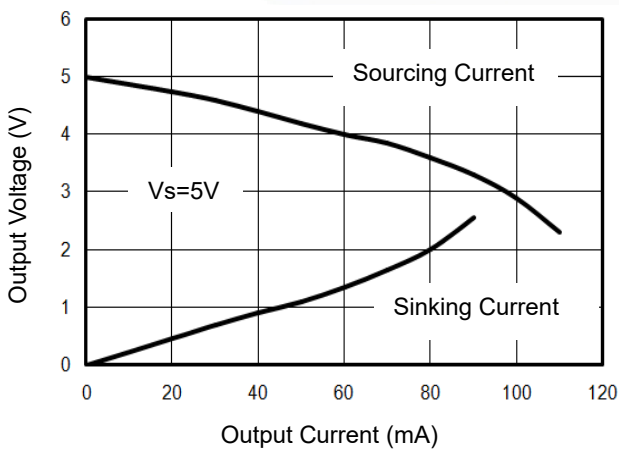
Supply Current vs. Supply Voltage



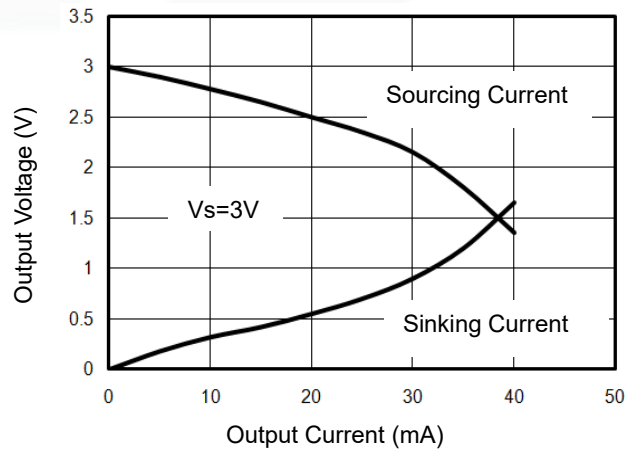
Short-Circuit Current vs. Supply Voltage



Output Voltage vs. Output Current



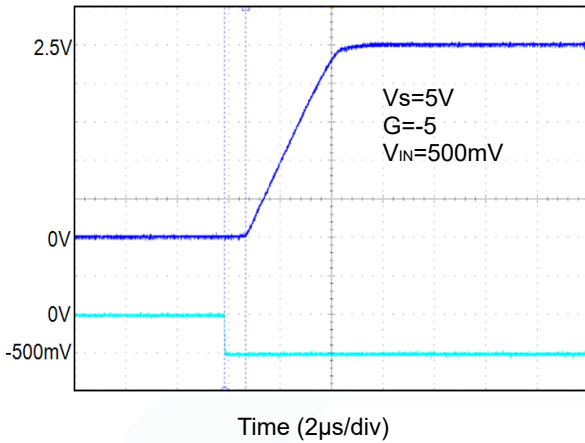
Output Voltage vs. Output Current



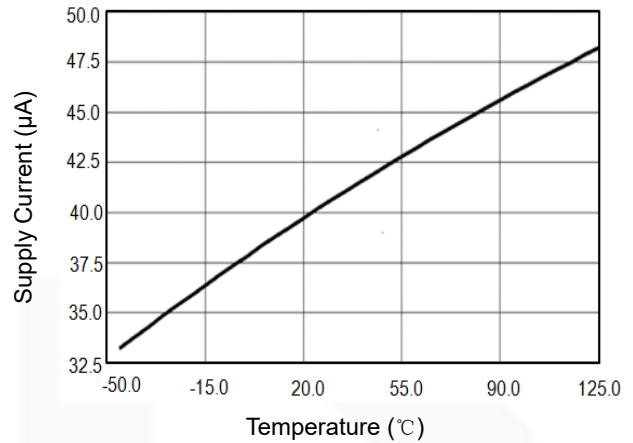
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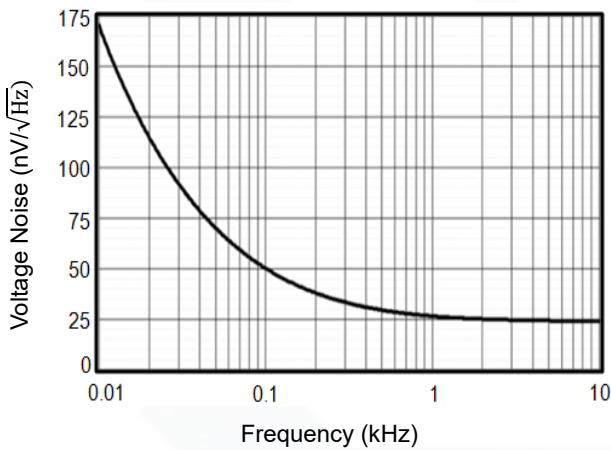
Overload Recovery Time



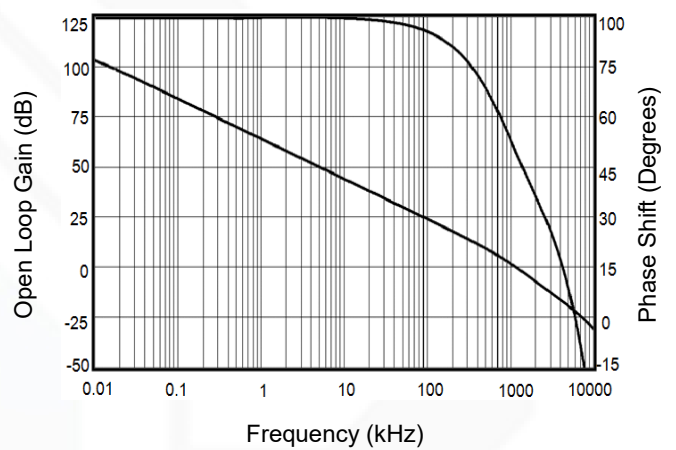
Supply Current vs. Temperature



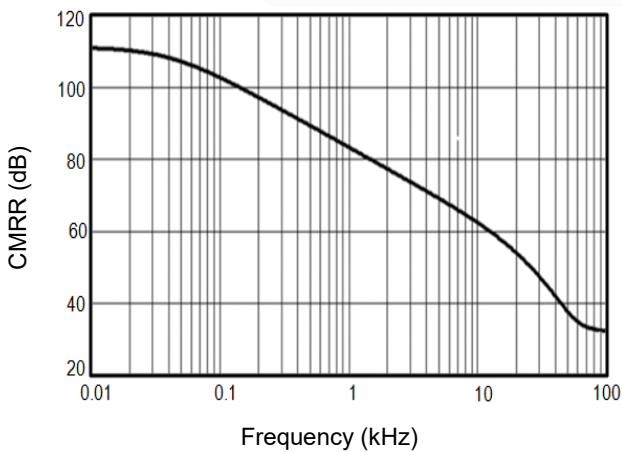
Input Voltage Noise Spectral Density vs. Frequency



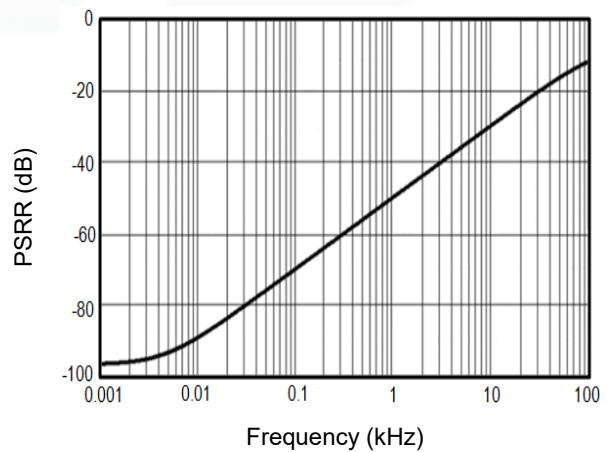
Open Loop Gain, Phase Shift vs. Frequency at +5V



CMRR vs. Frequency



PSRR vs. Frequency



Application Note

Size

LMV321A/V358A family series op amps are unity-gain stable and suitable for a wide range of general-purpose applications. The small footprints of the LMV321A/V358A family packages save space on printed circuit boards and enable the design of smaller electronic products.

Power Supply Bypassing and Board Layout

LMV321A/V358A family series operates from a single 2.1V to 5.5V supply or dual $\pm 1.05V$ to $\pm 2.75V$ supplies. For best performance, a $0.1\mu F$ ceramic capacitor should be placed close to the V_{DD} pin in single supply operation. For dual supply operation, both V_{DD} and V_{SS} supplies should be bypassed to ground with separate $0.1\mu F$ ceramic capacitors.

Low Supply Current

The low supply current (typical $40\mu A$ per channel) of LMV321A/V358A family will help to maximize battery life. They are ideal for battery powered systems.

Operating Voltage

LMV321A/V358A family operates under wide input supply voltage (2.1V to 5.5V). In addition, all temperature specifications apply from $-40\text{ }^{\circ}C$ to $+125\text{ }^{\circ}C$. Most behavior remains unchanged throughout the full operating voltage range. These guarantees ensure operation throughout the single Li-Ion battery lifetime.

Rail-to-Rail Input

The input common-mode range of LMV321A/V358A family extends 100mV beyond the supply rails ($V_{SS}-0.1V$ to $V_{DD}+0.1V$). This is achieved by using complementary input stage. For normal operation, inputs should be limited to this range.

Rail-to-Rail Output

Rail-to-Rail output swing provides maximum possible dynamic range at the output. This is particularly important when operating in low supply voltages. The output voltage of LMV321A/V358A family can typically swing to less than 5mV from supply rail in light resistive loads ($>100k\Omega$), and 60mV of supply rail in moderate resistive loads ($10k\Omega$).

Capacitive Load Tolerance

The LMV321A/V358A family is optimized for bandwidth and speed, not for driving capacitive loads. Output capacitance will create a pole in the amplifier's feedback path, leading to excessive peaking and potential oscillation. If dealing with load capacitance is a requirement of the application, the two strategies to consider are (1) using a small resistor in series with the amplifier's output and the load capacitance and (2) reducing the bandwidth of the amplifier's feedback loop by increasing the overall noise gain. Figure 2. shows a unity gain follower using the series resistor strategy. The resistor isolates the output from the capacitance and, more importantly, creates a zero in the feedback path that compensates for the pole created by the output capacitance.

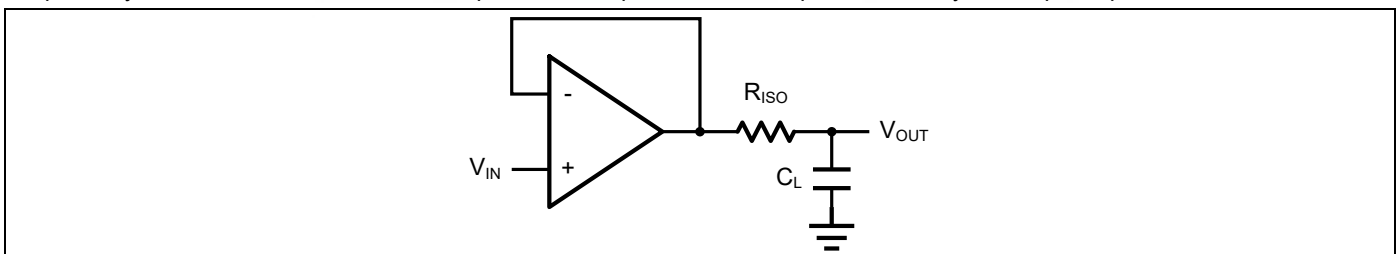


Figure 2. Indirectly Driving a Capacitive Load Using Isolation Resistor

The bigger the R_{ISO} resistor value, the more stable V_{OUT} will be. However, if there is a resistive load R_L in parallel with the capacitive load, a voltage divider (proportional to R_{ISO}/R_L) is formed, this will result in a gain error.

The circuit in Figure 3 is an improvement to the one in Figure 2. R_F provides the DC accuracy by feed-forward the V_{IN} to R_L . C_F and R_{ISO} serve to counteract the loss of phase margin by feeding the high frequency component of the output signal back to the amplifier's inverting input, thereby preserving the phase margin in the overall feedback loop. Capacitive drive can be increased by increasing the value of C_F . This in turn will slow down the pulse response.

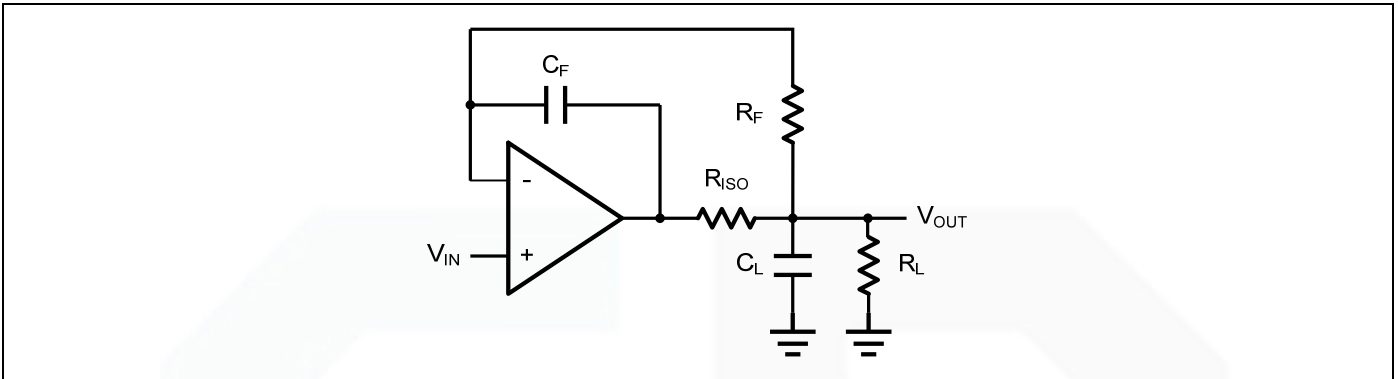


Figure 3. Indirectly Driving a Capacitive Load with DC Accuracy

Typical Application Circuits

Differential amplifier

The differential amplifier allows the subtraction of two input voltages or cancellation of a signal common to the two inputs. It is useful as a computational amplifier in making a differential to single-end conversion or in rejecting a common mode signal. Figure 4. shows the differential amplifier using LMV321A/V358A family.

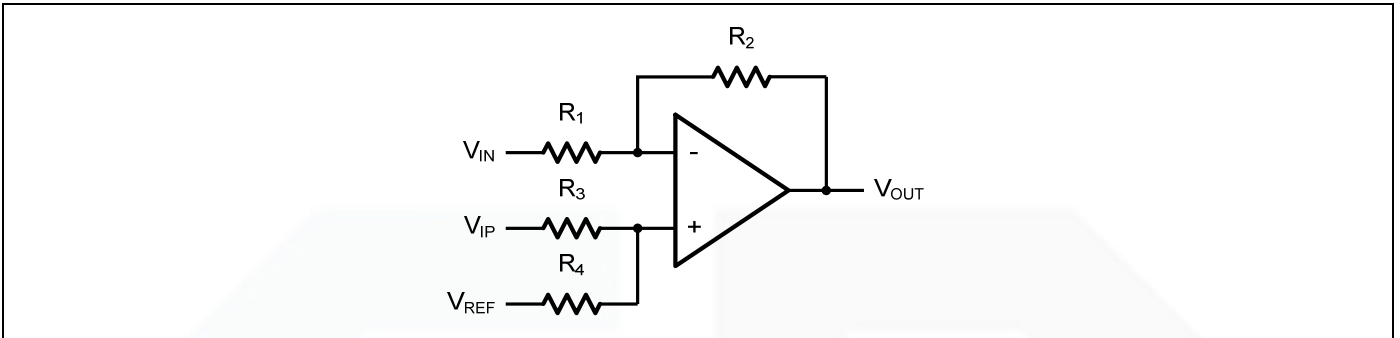


Figure 4. Differential Amplifier

$$V_{OUT} = \left(\frac{R_1+R_2}{R_3+R_4}\right) \frac{R_4}{R_1} V_{IN} - \frac{R_2}{R_1} V_{IP} + \left(\frac{R_1+R_2}{R_3+R_4}\right) \frac{R_3}{R_1} V_{REF}$$

If the resistor ratios are equal (i.e. $R_1=R_3$ and $R_2=R_4$), then

$$V_{OUT} = \frac{R_2}{R_1} (V_{IP} - V_{IN}) + V_{REF}$$

Low Pass Active Filter

The low pass active filter is shown in Figure 5. The DC gain is defined by $-R_2/R_1$. The filter has a -20dB/decade roll-off after its corner frequency $f_c=1/(2\pi R_3 C_1)$.

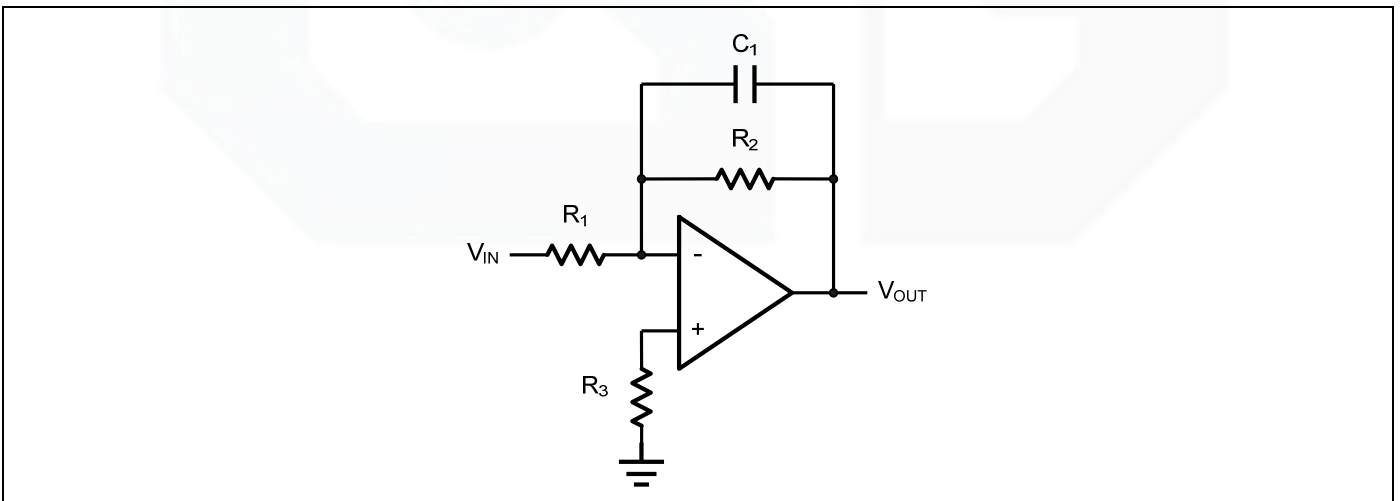


Figure 5. Low Pass Active Filter

Instrumentation Amplifier

The triple LMV321A/V358A family can be used to build a three-op-amp instrumentation amplifier as shown in Figure 6. The amplifier in Figure 6 is a high input impedance differential amplifier with gain of R_2/R_1 . The two differential voltage followers assure the high input impedance of the amplifier.

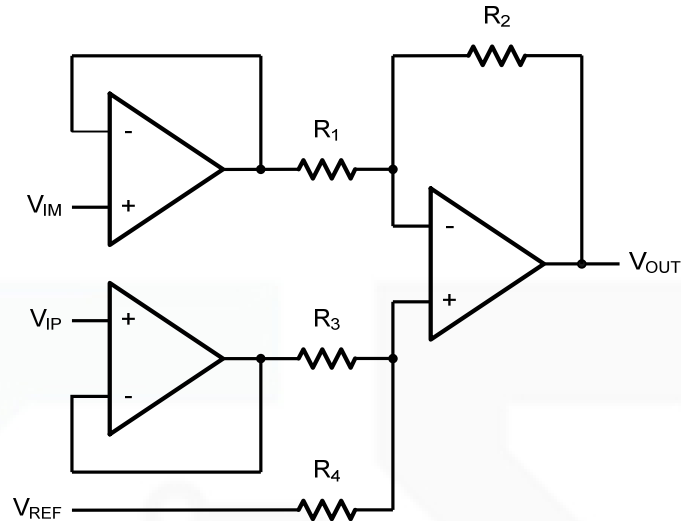
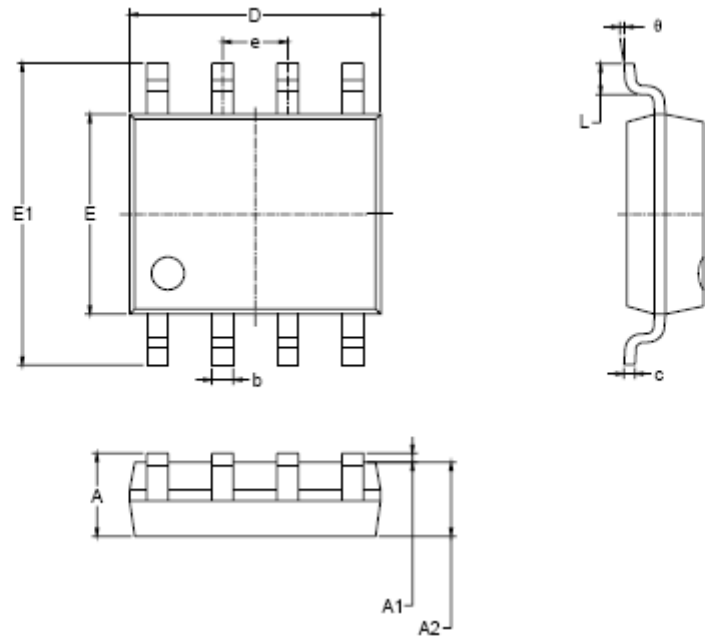


Figure 6. Instrument Amplifier

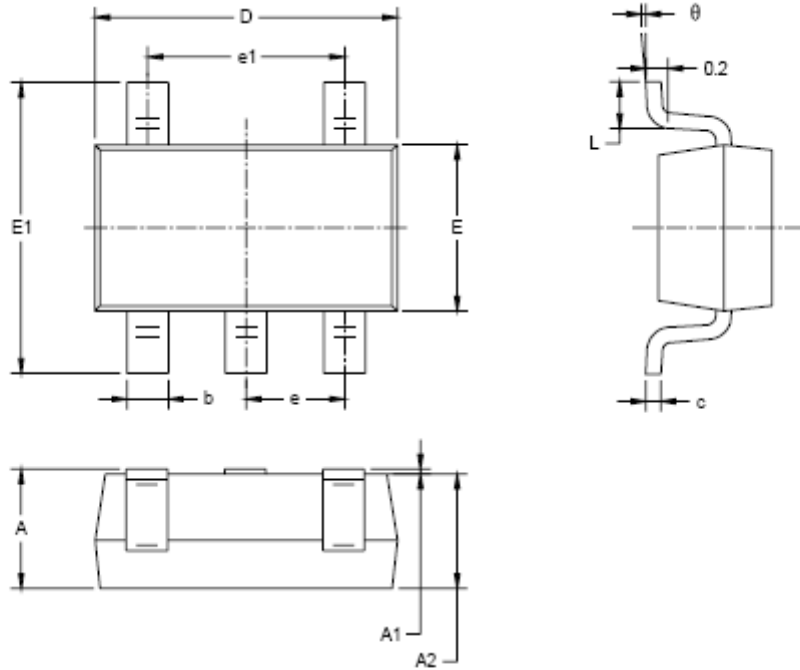
Package Information

SOP-8



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|------------------------------|-------|-------------------------|-------|
| | MIN | MAX | MIN | MAX |
| A | 1.350 | 1.750 | 0.053 | 0.069 |
| A1 | 0.100 | 0.250 | 0.004 | 0.010 |
| A2 | 1.350 | 1.550 | 0.053 | 0.061 |
| b | 0.330 | 0.510 | 0.013 | 0.020 |
| c | 0.170 | 0.250 | 0.006 | 0.010 |
| D | 4.700 | 5.100 | 0.185 | 0.200 |
| E | 3.800 | 4.000 | 0.150 | 0.157 |
| E1 | 5.800 | 6.200 | 0.228 | 0.244 |
| e | 1.27 BSC | | 0.050 BSC | |
| L | 0.400 | 1.270 | 0.016 | 0.050 |
| theta | 0° | 8° | 0° | 8° |

SOT23-5



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|----------|------------------------------|-------|-------------------------|-------|
| | MIN | MAX | MIN | MAX |
| A | 1.050 | 1.250 | 0.041 | 0.049 |
| A1 | 0.000 | 0.100 | 0.000 | 0.004 |
| A2 | 1.050 | 1.150 | 0.041 | 0.045 |
| b | 0.300 | 0.500 | 0.012 | 0.020 |
| c | 0.100 | 0.200 | 0.004 | 0.008 |
| D | 2.820 | 3.020 | 0.111 | 0.119 |
| E | 1.500 | 1.700 | 0.059 | 0.067 |
| E1 | 2.650 | 2.950 | 0.104 | 0.118 |
| e | 0.950 BSC | | 0.037 BSC | |
| e1 | 1.900 BSC | | 0.075 BSC | |
| L | 0.300 | 0.600 | 0.012 | 0.024 |
| θ | 0° | 8° | 0° | 8° |

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