



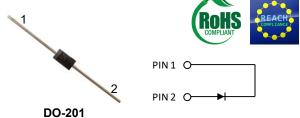
Silicon Carbide PiN Diode

 V_{RRM} = 15.0 kV $I_{F (Tc=25^{\circ}C)}$ = 1 A

Features

- 15 kV blocking
- 175 °C operating temperature
- · Fast turn off characteristics
- Soft reverse recovery characteristics
- Ultra-Fast high temperature switching

Package



Advantages

- Highest voltage rectifier commercially available
- · Reduced stacking
- Reduced system complexity/Increased reliability

Applications

- Voltage Multiplier
- Ignition/Trigger Circuits
- Oil/Downhole
- Lighting
- Defense

Maximum Ratings at T_j = 175 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	V_{RRM}		15	kV
Continuous forward current	I _F		1	Α
RMS forward current	I _{F(RMS)}		0.5	Α
Operating and storage temperature	T_j , T_stg		-55 to 175	°C

Electrical Characteristics at T_j = 175 °C, unless otherwise specified

Parameter	Symbol	Conditions		Values		Unit
		Conditions	min.	typ.	max.	Unit
Diode forward voltage	V _F	I _F = 1 A, T _j = 25 °C		6.4		V
Diode forward voltage		$I_F = 1 \text{ A}, T_j = 175 ^{\circ}\text{C}$		4.7		V
Reverse current	I_R	$V_R = 8 \text{ kV}, T_j = 25 \text{ °C}$		1	20	
		$V_R = 8 \text{ kV}, T_j = 175 ^{\circ}\text{C}$			100	μΑ
Total reverse recovery charge	Q_{rr}	$I_F \le I_{F,MAX}$ $I_F = 1.5 \text{ A}$ d $I_F/dt = 70 \text{ A/µs}$		558		nC
Switching time	t_s	$T_j = 175 ^{\circ}\text{C}$ $V_R = 100$	00 V	< 236		ns
		$V_R = 1 \text{ V}, f = 1 \text{ MHz}, T_j = 25 ^{\circ}$	С	22		
Total capacitance	С	$V_R = 400 \text{ V}, f = 1 \text{ MHz}, T_j = 25$	°C	4		pF
		$V_R = 1000 \text{ V}, f = 1 \text{ MHz}, T_j = 25$	°C	3		
Total capacitive charge	Q_{C}	$V_R = 1000 \text{ V}, f = 1 \text{ MHz}, T_j = 25$	°C	4.5		nC



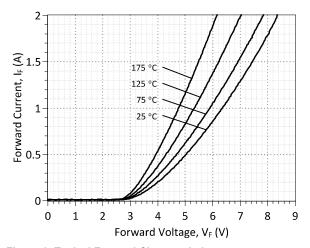


Figure 1: Typical Forward Characteristics

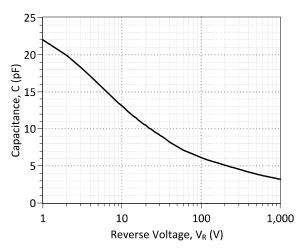


Figure 3: Typical Junction Capacitance vs Reverse Voltage Characteristics

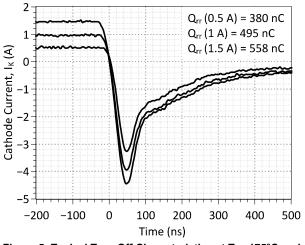


Figure 5: Typical Turn Off Characteristics at T_{j} = 175°C and V_{R} = 1000 V

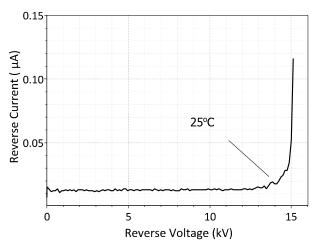


Figure 2: Typical Reverse Characteristics at 25°C

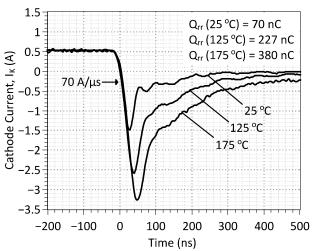


Figure 4: Typical Turn Off Characteristics at I_{k} = 0.5 A and V_{R} = 1000 V

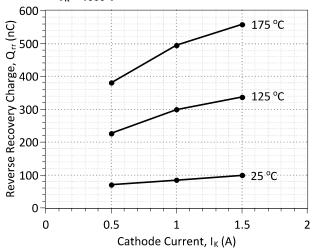


Figure 6: Reverse Recovery Charge vs Cathode Current



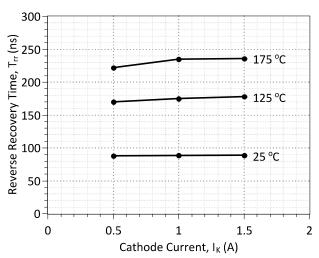
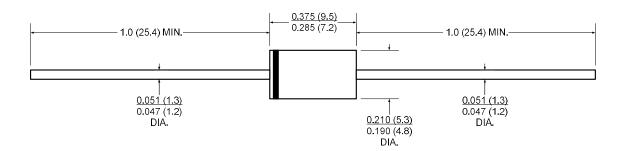


Figure 7: Reverse Recovery Time vs Cathode Current

Package Dimensions:

DO-201 PACKAGE OUTLINE



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS

Revision History						
Date	Revision	Comments	Supersedes			
2016/11/10	0	Initial release				

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SPICE Model Parameters

This is a secure document. Please copy this code from the SPICE model PDF file on our website (http://www.genesicsemi.com/sic_pin/GA01PNS150-201_SPICE.pdf) into LTSPICE (version 4) software for simulation of the GA01PNS150-201.

```
MODEL OF GeneSiC Semiconductor Inc.
     $Revision: 1.0
                                $
     $Date: 10-Nov-2016
                                $
     GeneSiC Semiconductor Inc.
     43670 Trade Center Place Ste. 155
    Dulles, VA 20166
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* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
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* Models accurate up to 2 times rated drain current.
 Start of GA01PNS150-201 SPICE Model
.MODEL GA01PNS150 D
+ IS 9.2491e-015
         2.24770
+ RS
+ N
          3.3373
         0.00011784
+ IKF
          3.23
+ EG
         25
+ XTI
+ TRS1
         -0.0024
+ CJO
          2.28E-11
+ VJ
         2.304
+ M
         0.376
+ FC
         0.5
+ BV
         8000
+ IBV
         1.00E-03
         15000
+ VPK
+ IAVE
        SiC PiN
+ TYPE
+ MFG
         GeneSiC Semi
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