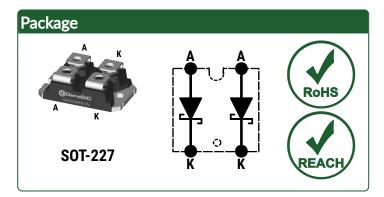
GeneSiC SEMICONDUCTOR

Silicon Carbide Schottky Diode

 V_{RRM} = 1200 V $I_{F (T_{C} = 134^{\circ}C)}$ = 60 A * Q_{C} = 194 nC *

Features

- Gen4 Thin Chip Technology for Low V_F
- Enhanced Surge and Avalanche Robustness
- Superior Figure of Merit Qc/IF
- Low Thermal Resistance
- Low Reverse Leakage Current
- Temperature Independent Fast Switching
- Positive Temperature Coefficient of V_F
- High dV/dt Ruggedness



Advantages

- Improved System Efficiency
- High System Reliability
- Optimal Price Performance
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- Easy to Parallel without Thermal Runaway
- Enables Extremely Fast Switching

Applications

- Power Factor Correction (PFC)
- Electric Vehicles and Battery Chargers
- Solar Inverters
- High Frequency Converters
- Switched Mode Power Supply (SMPS)
- Motor Drives
- Anti-Parallel / Free-Wheeling Diode
- Induction Heating & Welding

Absolute Maximum Ratings (At T _C = 25°C Unless Otherwise Stated)							
Parameter	Symbol	Conditions	Values	Unit	Note		
Repetitive Peak Reverse Voltage (Per Leg)	V_{RRM}		1200	٧			
		$T_C = 75^{\circ}C$, D = 1 52 / 104					
Continuous Forward Current (Per Leg / Per Device)	I _F	$T_C = 100^{\circ}C, D = 1$	44 / 88	Α	Fig. 4		
		$T_C = 134$ °C, D = 1	30 / 60				
Non-Repetitive Peak Forward Surge Current, Half Sine	leau	$T_C = 25^{\circ}C$, $t_P = 10 \text{ ms}$	300	Α			
Wave (Per Leg)	I _{F,SM}	T_C = 150°C, t_P = 10 ms	240	Α			
Repetitive Peak Forward Surge Current, Half Sine Wave	lenu	$T_C = 25^{\circ}C$, $t_P = 10 \text{ ms}$	180	Α			
(Per Leg)	I _{F,RM}	$T_C = 150^{\circ}C$, $t_P = 10 \text{ ms}$	126	A			
Non-Repetitive Peak Forward Surge Current (Per Leg)	I _{F,MAX}	T_C = 25°C, t_P = 10 μ s	1500	Α			
i ² t Value (Per Leg)	∫i²dt	$T_C = 25^{\circ}C$, $t_P = 10 \text{ ms}$	450	A^2s			
Non-Repetitive Avalanche Energy (Per Leg)	Eas	$L = 0.7 \text{ mH, } I_{AS} = 30 \text{ A}$	325	mJ			
Diode Ruggedness (Per Leg)	dV/dt	$V_R = 0 \sim 960 \text{ V}$	200	V/ns			
Power Dissipation (Per Leg / Per Device)	P _{TOT}	T _C = 25°C	211 / 422	W	Fig. 3		
Operating and Storage Temperature	T _j , T _{stg}		-55 to 175	°C			

^{*} Per Device





Electrical Characteristics (Per Leg)								
Parameter	Symbol	Conditions		Values			Unit	Note
r al allietei	Зушьог			Min.	Тур.	Max.	Ullit	Note
Diode Forward Voltage V _F		I _F = 30 A, T _j = 25°C			1.5	1.8	V	Fig. 1
Diode Forward Voltage	VF	$I_F = 30 \text{ A, } T_j = 175^{\circ}\text{C}$			1.9		V	Fig. 1
Reverse Current	l _n	V _R = 1200 V, T _j = 25°C			2	10	μΑ	Fig. 2
neverse Guireiit	I _R	$V_R = 1200 \text{ V, } T_j = 175^{\circ}\text{C}$			20			
Total Capacitive Charge	0-		$V_{R} = 400 V$		67		nC	Fig. 7
	Qc	$I_{F} \leq I_{F,MAX}$	$V_{R} = 800 V$		97			
Switching Time	+-	dl _F /dt = 200 A/μs V _R = 400 V			- 10		no	
	ts		$V_R = 800 V$		< 10		ns	
Total Canacitanas	0	V _R = 1 V, f = 1MHz			1101		"r	Fig. 6
Total Capacitance	C	V _R = 800 V, f = 1MHz			64		pF	Fig. 6

Thermal/Package Characteristics							
Parameter	meter Symbol Conditions	Conditions	Values			Unit	Note
raianietei		Min.	Typ.	Max.	Ollit	Note	
Thermal Resistance, Junction - Case (Per Leg)	R _{thJC}			0.71		°C/W	Fig. 9
Weight	W _T			28.0		g	
Mounting Torque	T _M	Screws to Heatsink			1.5	Nm	
Terminal Connection Torque	Tc	M4 Screws			1.3	Nm	
Isolation Voltage(RMS)	V _{ISO}	t = 1s (50/60 Hz)	3000			V	
		t = 60s (50/60 Hz)		2500	2500		
Creepage Distance on Surface	dctt	Terminal to Terminal	Terminal to Terminal 10.5		na na		
	d_{Ctb}	Terminal to Backside		8.5		mm	
Striking Distance Through Air	dstt	Terminal to Terminal 3.2			na na		
	d _{Stb}	Terminal to Backside	nal to Backside 6.8			mm	



Figure 1: Typical Forward Characteristics (Per Leg)

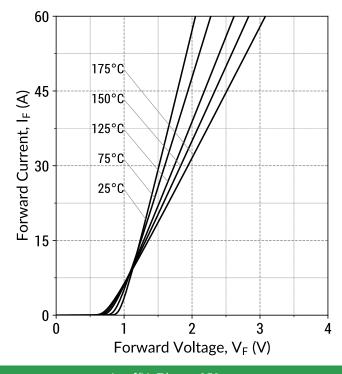
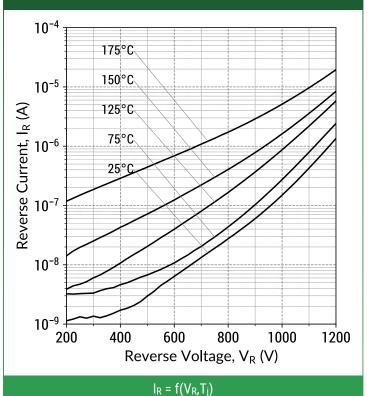
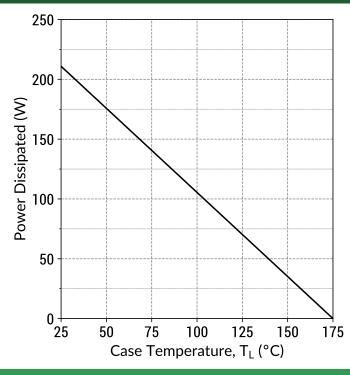


Figure 2: Typical Reverse Characteristics (Per Leg)



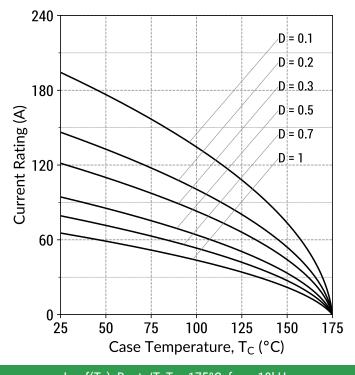
 $I_F = f(V_F, T_j); t_P = 250 \ \mu s$

Figure 3: Power Derating Curves (Per Leg)



 $P_{TOT} = f(T_C); T_j = 175^{\circ}C$

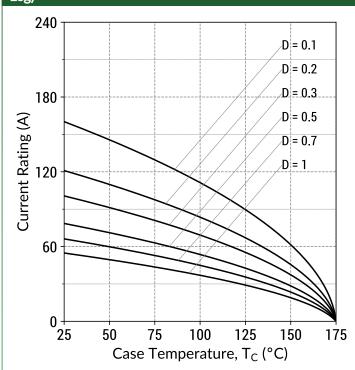
Figure 4: Current Derating Curves (Typical V_F) (Per Leg)



 $I_F = f(T_C); D = t_P/T; T_j \le 175^{\circ}C; f_{SW} > 10kHz$

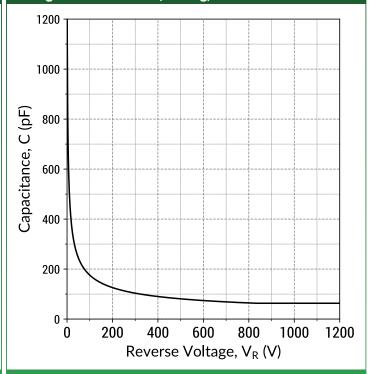


Figure 5: Current Derating Curves (Maximum V_F) (Per Leg)



 $I_F = f(T_C); D = t_P/T; T_j \le 175^{\circ}C; f_{SW} > 10kHz$

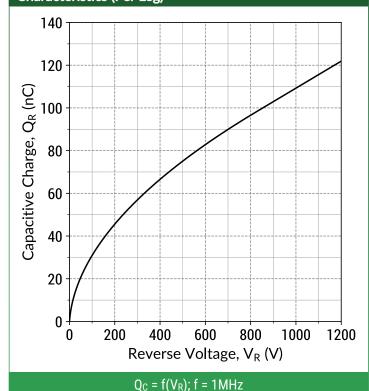
Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics (Per Leg)



 $C = f(V_R)$; f = 1MHz

Figure 8: Typical Capacitive Energy vs Reverse Voltage

Figure 7: Typical Capacitive Charge vs Reverse Voltage Characteristics (Per Leg)



Characteristics (Per Leg)

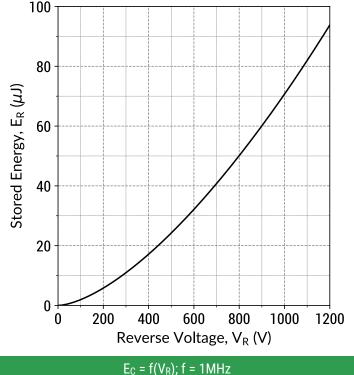
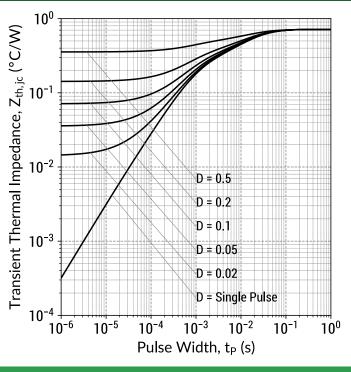


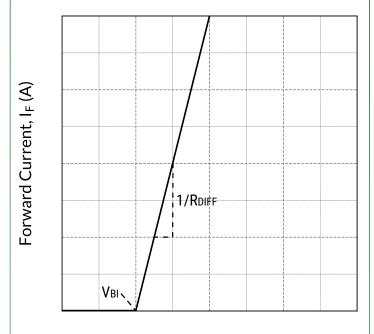


Figure 9: Transient Thermal Impedance (Per Leg)



 $Z_{th,jc} = f(t_P,D); D = t_P/T$

Figure 10: Forward Curve Model (Per Leg)



 $I_F = f(V_F, T_j)$

Forward Power Loss Equation:

 $P_{LOSS} = V_{BI}(T_i) \times I_{AVG} + R_{DIFF}(T_i) \times I_{RMS}^2$ Forward Voltage, V_F (V)

Forward Curve Model Equation:

 $I_F = (V_F - V_{BI})/R_{DIFF}(A)$

Built-In Voltage (V_{BI}):

$$V_{BI}(T_j) = m \times T_j + n (V)$$

 $m = -0.00119 (V/^{\circ}C)$
 $n = 1.01 (V)$

Differential Resistance (RDIFF):

$$R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c (\Omega)$$

 $a = 3.97e-07 (\Omega/^{\circ}C^2)$
 $b = 5.5e-05 (\Omega/^{\circ}C)$
 $c = 0.0163 (\Omega)$



Package Dimensions SOT-227 Package Outline 0.472 (11.9) 0.480 (12.19) 1.240 (31.5) 0.372 (9.45) 1.255 (31.88) 0.378 (9.60) 0.310 (7.87) 0.322 (8.18) 0.108 (2.74) 0.124 (3.15) Ø <u>0.163 (4.14)</u> 0.169 (4.29) R 3.97 1.049 (26.6) 1.059 (26.90) 0.163 (4.14) 0.990 (25.1) 1.000 (25.40) 0.495 (12.5) 0.506 (12.85) 0.172 (4.37) 0.164 (4.16) 0.174(4.42) 0.080 (2.03) 0.234 (5.94) 0.084 (2.13) 0.165 (4.19) 0.169 (4.29) 0.030 (0.76) 0.033 (0.84) 0.588 (14.9) 0.594 (15.09) 1.186 (30.1) 1.192 (30.28) 1.494 (37.9) 1.504 (38.20) Package View **Isolated Base**

NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.





Compliance

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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Revision History

Date	Revision	Comments	Supersedes
Jul. 27, 2020	Rev 1	Initial Release	



www.genesicsemi.com/sic-schottky-mps/



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