

# GD30MPS06H

## 650V 30A SiC Schottky MPS™ Diode



### Silicon Carbide Schottky Diode

$V_{RRM}$	=	650 V
$I_F (T_C = 141^\circ\text{C})$	=	30 A
$Q_C$	=	46 nC

#### Features

- Gen4 Thin Chip Technology for Low  $V_F$
- Superior Power Efficiency
- Superior Figure of Merit  $Q_C/I_F$
- Enhanced Surge Current Robustness
- Low Thermal Resistance
- Temperature Independent Fast Switching
- Positive Temperature Coefficient of  $V_F$
- High  $dV/dt$  Ruggedness

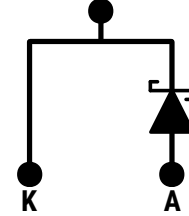
#### Package

Case(K)



T0-247-2

Case



#### Advantages

- Optimal Price Performance
- Improved System Efficiency
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- High System Reliability
- Easy to Parallel without Thermal Runaway
- Enables Extremely Fast Switching

#### Applications

- Power Factor Correction (PFC)
- Electric Vehicles and Battery Chargers
- Solar Inverters
- High Frequency Converters
- Switched Mode Power Supply (SMPS)
- Motor Drives
- Anti-Parallel / Free-Wheeling Diode
- Induction Heating & Welding

#### Absolute Maximum Ratings (At $T_C = 25^\circ\text{C}$ Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	Note
Repetitive Peak Reverse Voltage	$V_{RRM}$		650	V	
Continuous Forward Current	$I_F$	$T_C = 100^\circ\text{C}, D = 1$	49	A	Fig. 4
		$T_C = 135^\circ\text{C}, D = 1$	33		
		$T_C = 141^\circ\text{C}, D = 1$	30		
Non-Repetitive Peak Forward Surge Current, Half Sine Wave	$I_{F,SM}$	$T_C = 25^\circ\text{C}, t_P = 10 \text{ ms}$	210	A	
		$T_C = 150^\circ\text{C}, t_P = 10 \text{ ms}$	168		
Repetitive Peak Forward Surge Current, Half Sine Wave	$I_{F,RM}$	$T_C = 25^\circ\text{C}, t_P = 10 \text{ ms}$	126	A	
		$T_C = 150^\circ\text{C}, t_P = 10 \text{ ms}$	89		
Non-Repetitive Peak Forward Surge Current	$I_{F,MAX}$	$T_C = 25^\circ\text{C}, t_P = 10 \mu\text{s}$	1050	A	
$i^2t$ Value	$\int i^2 dt$	$T_C = 25^\circ\text{C}, t_P = 10 \text{ ms}$	220	$\text{A}^2\text{s}$	
Non-Repetitive Avalanche Energy	$E_{AS}$	$L = 0.6 \text{ mH}, I_{AS} = 30 \text{ A}$	276	mJ	
Diode Ruggedness	$dV/dt$	$V_R = 0 \sim 520 \text{ V}$	200	V/ns	
Power Dissipation	$P_{TOT}$	$T_C = 25^\circ\text{C}$	237	W	Fig. 3
Operating and Storage Temperature	$T_j, T_{stg}$		-55 to 175	$^\circ\text{C}$	

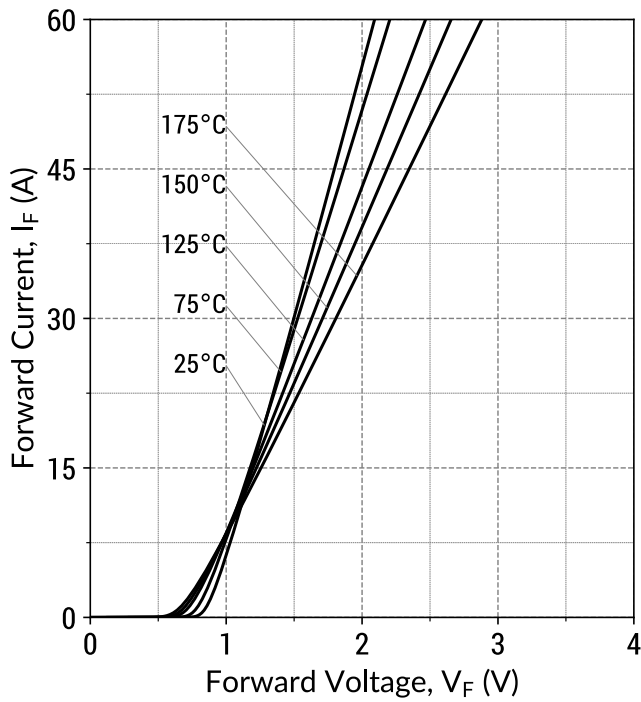
### Electrical Characteristics

Parameter	Symbol	Conditions	Values			Unit	Note
			Min.	Typ.	Max.		
Diode Forward Voltage	$V_F$	$I_F = 30\text{ A}, T_j = 25^\circ\text{C}$		1.5	1.8	V	Fig. 1
		$I_F = 30\text{ A}, T_j = 175^\circ\text{C}$		1.8			
Reverse Current	$I_R$	$V_R = 650\text{ V}, T_j = 25^\circ\text{C}$		1	5	$\mu\text{A}$	Fig. 2
		$V_R = 650\text{ V}, T_j = 175^\circ\text{C}$		6			
Total Capacitive Charge	$Q_C$	$I_F \leq I_{F,MAX}$ $di_F/dt = 200\text{ A}/\mu\text{s}$	$V_R = 200\text{ V}$	31		nC	Fig. 7
			$V_R = 400\text{ V}$	46			
Switching Time	$t_s$	$di_F/dt = 200\text{ A}/\mu\text{s}$	$V_R = 200\text{ V}$	< 10		ns	
			$V_R = 400\text{ V}$				
Total Capacitance	C	$V_R = 1\text{ V}, f = 1\text{ MHz}$		735		pF	Fig. 6
		$V_R = 400\text{ V}, f = 1\text{ MHz}$		63			

### Thermal/Package Characteristics

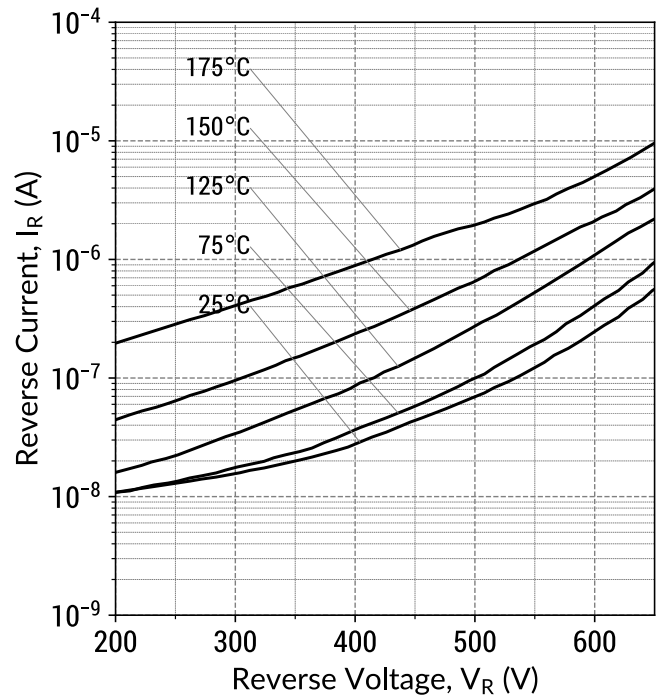
Parameter	Symbol	Conditions	Values			Unit	Note
			Min.	Typ.	Max.		
Thermal Resistance, Junction - Case	$R_{thJC}$			0.63		$^\circ\text{C}/\text{W}$	Fig. 9
Weight	$W_T$			6.0		g	
Mounting Torque	$T_M$	Screws to Heatsink			1.1	Nm	

Figure 1: Typical Forward Characteristics



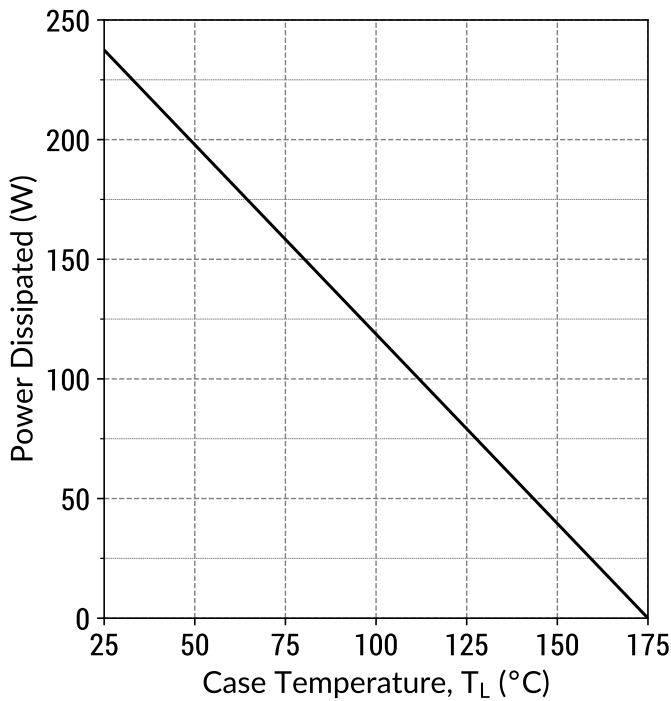
$$I_F = f(V_F, T_j); t_P = 250 \mu s$$

Figure 2: Typical Reverse Characteristics



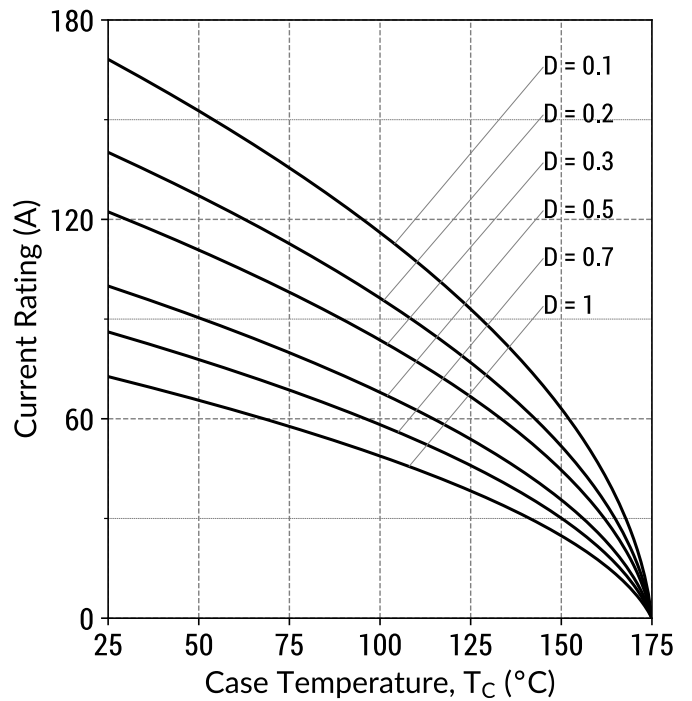
$$I_R = f(V_R, T_j)$$

Figure 3: Power Derating Curves



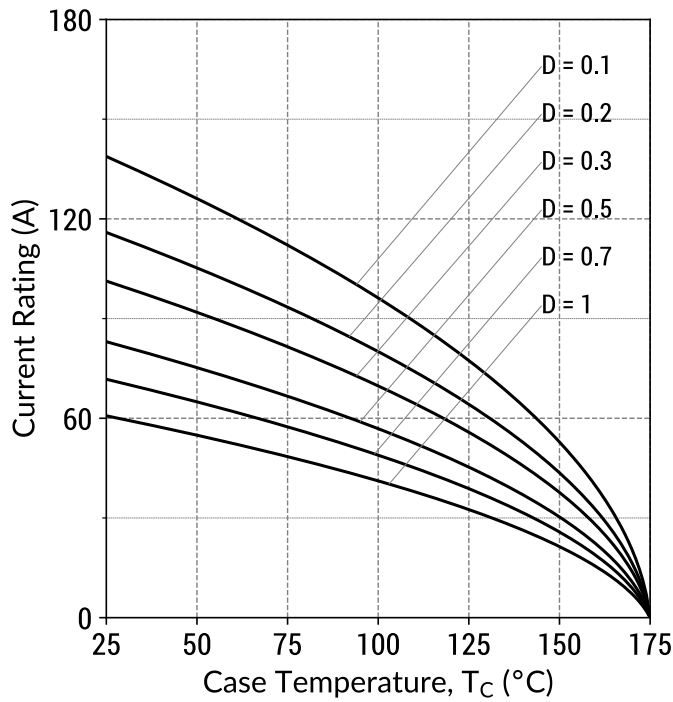
$$P_{TOT} = f(T_C); T_j = 175^\circ C$$

Figure 4: Current Derating Curves (Typical  $V_F$ )



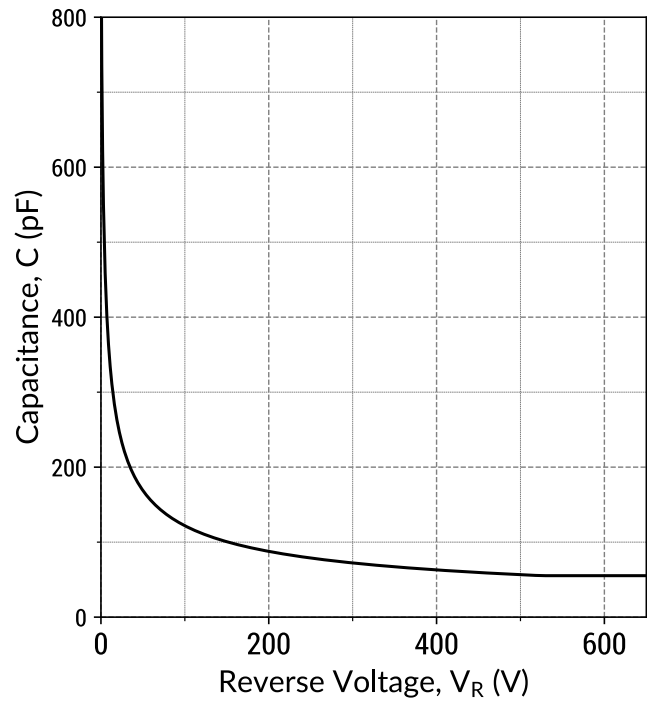
$$I_F = f(T_C); D = t_P/T; T_j \leq 175^\circ C; f_{sw} > 10kHz$$

Figure 5: Current Derating Curves (Maximum  $V_F$ )



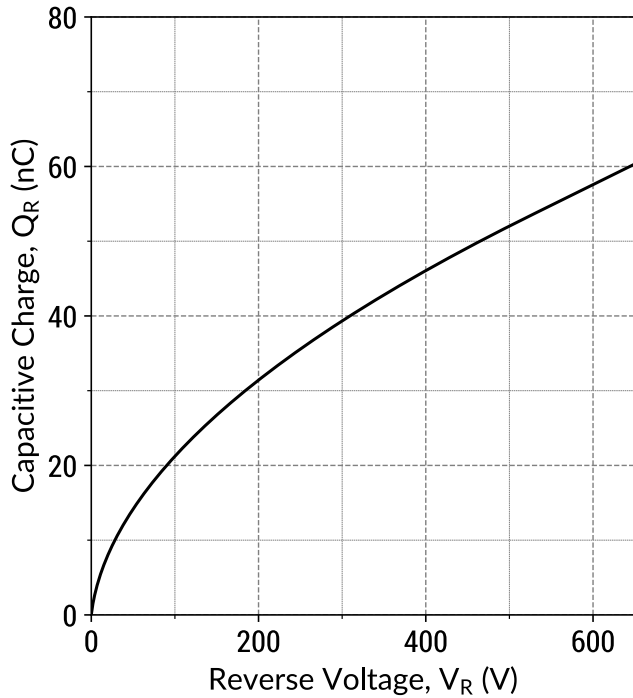
$I_F = f(T_C)$ ;  $D = t_P/T$ ;  $T_J \leq 175^\circ\text{C}$ ;  $f_{SW} > 10\text{kHz}$

Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics



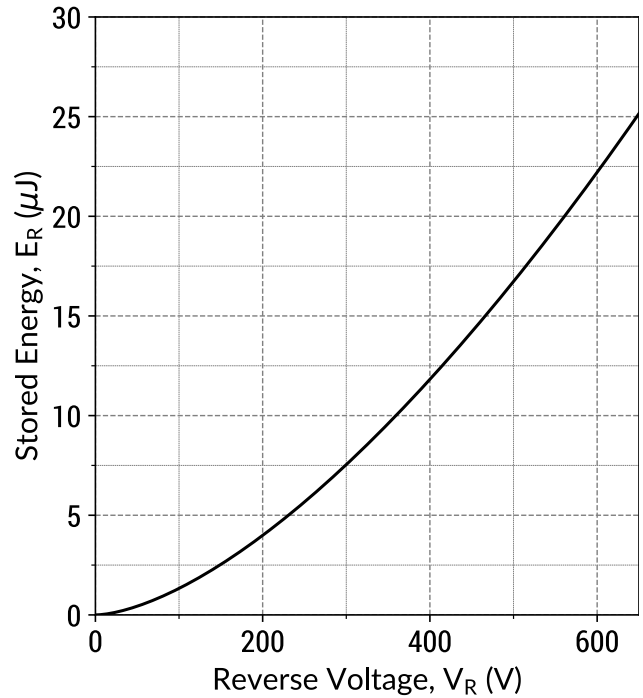
$C = f(V_R)$ ;  $f = 1\text{MHz}$

Figure 7: Typical Capacitive Charge vs Reverse Voltage Characteristics



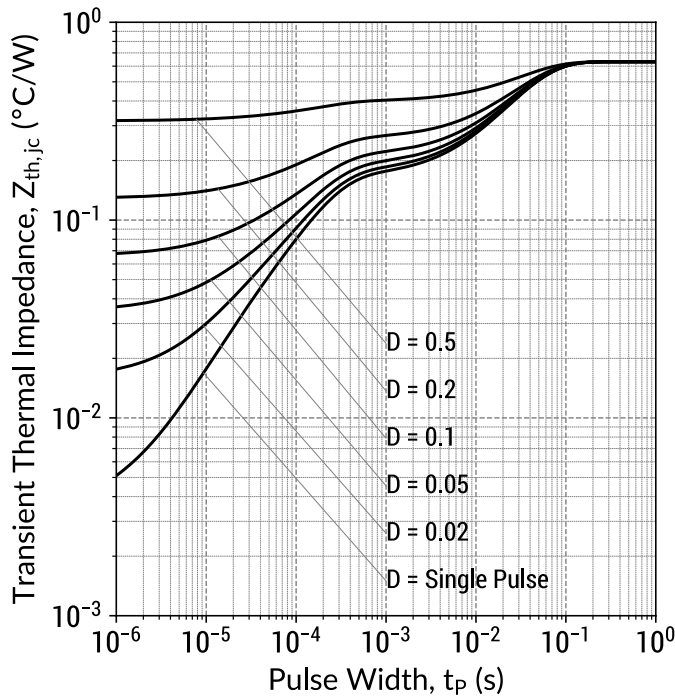
$Q_C = f(V_R)$ ;  $f = 1\text{MHz}$

Figure 8: Typical Capacitive Energy vs Reverse Voltage Characteristics



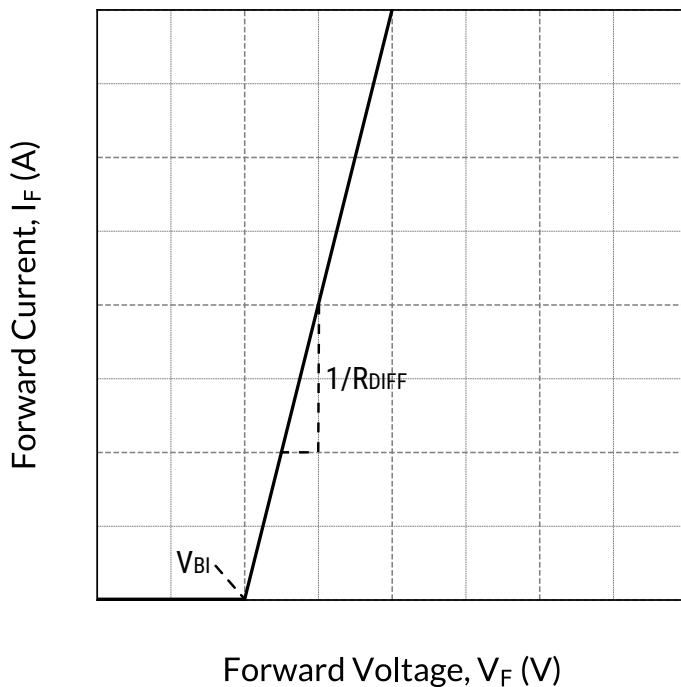
$E_C = f(V_R)$ ;  $f = 1\text{MHz}$

Figure 9: Transient Thermal Impedance



$$Z_{th,jc} = f(t_p, D); D = t_p/T$$

Figure 10: Forward Curve Model



$$I_F = f(V_F, T_j)$$

**Forward Curve Model Equation:**

$$I_F = (V_F - V_{BI})/R_{DIFF} \text{ (A)}$$

**Built-In Voltage ( $V_{BI}$ ):**

$$V_{BI}(T_j) = m \times T_j + n \text{ (V)}$$

$$m = -0.00115 \text{ (V/°C)}$$

$$n = 9.31e-01 \text{ (V)}$$

**Differential Resistance ( $R_{DIFF}$ ):**

$$R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c \text{ (}\Omega\text{)}$$

$$a = 5.07e-07 \text{ (}\Omega\text{/°C}^2\text{)}$$

$$b = 5.5e-06 \text{ (}\Omega\text{/°C)}$$

$$c = 0.0194 \text{ (}\Omega\text{)}$$

**Forward Power Loss Equation:**

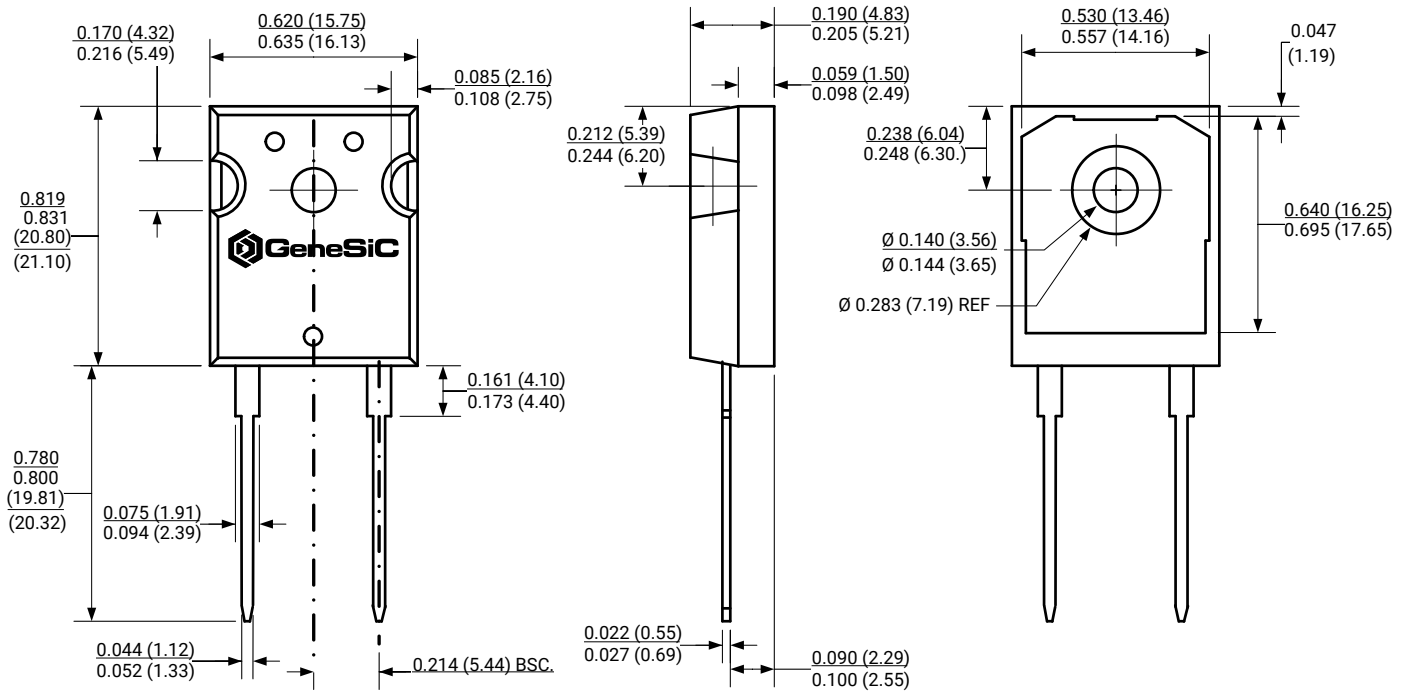
$$P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$$

# GD30MPS06H 650V 30A SiC Schottky MPS™ Diode

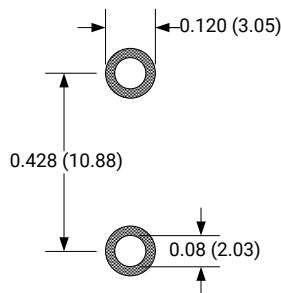


## Package Dimensions

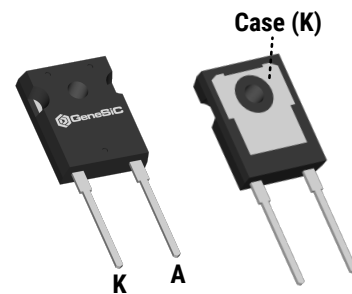
### TO-247-2 Package Outline



### Recommended Solder Pad Layout



### Package View



#### NOTE

1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.

## Compliance

### RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

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## Revision History

Date	Revision	Comments	Supersedes
Jul. 27, 2020	Rev 1	Initial Release	



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