

## N-Channel Enhancement Mode Power MOSFET

### Description

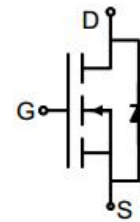
The 9N90 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge. It can be used in a wide variety of applications.

### General Features

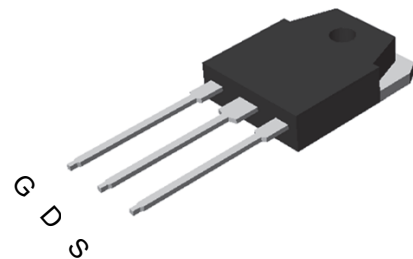
- $V_{DS}$  900V
- $I_D$  (at  $V_{GS} = 10V$ ) 9A
- $R_{DS(ON)}$  (at  $V_{GS} = 10V$ ) < 1.3 $\Omega$
- 100% Avalanche Tested
- RoHS Compliant
- Ultra-fast body diode

### Application

- Power switch
- DC/DC converters



Schematic diagram



TO-3P

### Ordering Information

Device	Package	Marking	Packaging
9N90	TO-3P	9N90	30pcs/Tube

### Absolute Maximum Ratings $T_C = 25^\circ C$ , unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	900	V
Continuous Drain Current	$I_D$	9	A
Pulsed Drain Current (note1)	$I_{DM}$	60	A
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Power Dissipation	$P_D$	50	W
Single pulse avalanche energy (note2)	$E_{AS}$	720	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	$^\circ C$

### Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	40	$^\circ C/W$
Maximum Junction-to-Case	$R_{thJC}$	0.25	$^\circ C/W$

Specifications $T_J = 25^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	900	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 650V, V_{GS} = 0V$	--	--	10	$\mu A$
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = \pm 30V$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.5	3.5	4.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 20A$	--	1	1.5	$\Omega$
Forward Transconductance	$g_{FS}$	$V_{GS} = 5V, I_D = 20A$	--	0.84	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V,$ $V_{DS} = 400V,$ $f = 0.25\text{MHz}$	--	7668	--	pF
Output Capacitance	$C_{oss}$		--	157	--	
Reverse Transfer Capacitance	$C_{rss}$		--	0.6	--	
Total Gate Charge	$Q_g$	$V_{DD} = 400V,$ $I_D = 20A,$ $V_{GS} = 10V$	--	160	--	nC
Gate-Source Charge	$Q_{gs}$		--	35	--	
Gate-Drain Charge	$Q_{gd}$		--	55	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 400V,$ $I_D = 20A,$ $R_G = 4.7\Omega$	--	55	--	ns
Turn-on Rise Time	$t_r$		--	65	--	
Turn-off Delay Time	$t_{d(off)}$		--	175	--	
Turn-off Fall Time	$t_f$		--	48	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	70	A
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = 20A, V_{GS} = 0V$	--	--	1.2	V
Reverse Recovery Charge	$Q_{rr}$	$I_F = 20A, V_{GS} = 0V$ $di/dt = 100A/\mu s$	--	1.5	--	nC
Reverse Recovery Time	$T_{rr}$		--	207	--	ns

### Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition :  $T_J = 25^\circ\text{C}, V_{DD} = 50V, V_{GS} = 10V, L = 10\text{mH}, R_G = 25\Omega$
3. Identical low side and high side switch with identical  $R_G$

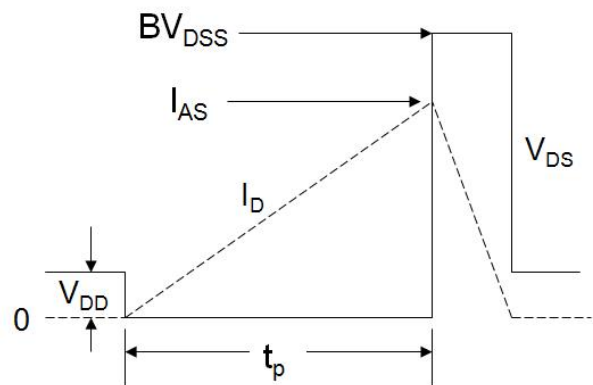
### Gate Charge Test Circuit



### Switch Time Test Circuit

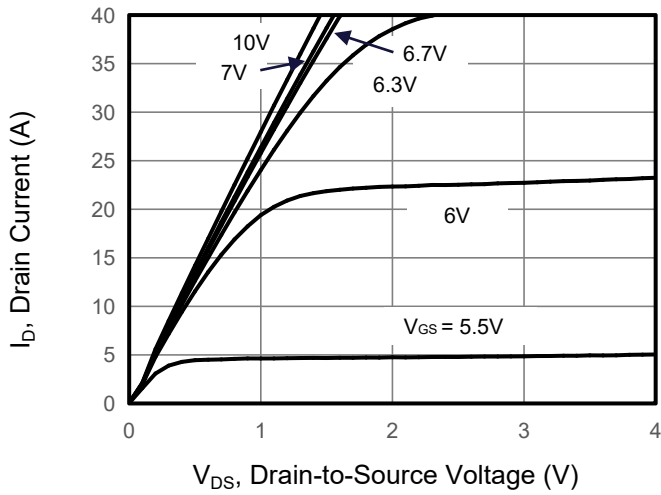


### EAS Test Circuit

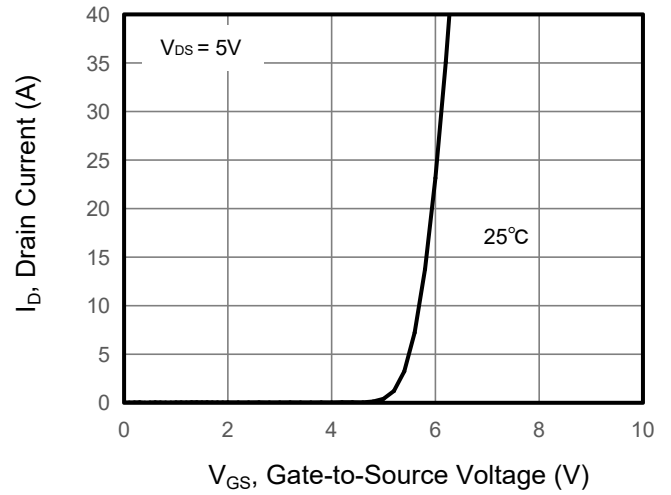


## Typical Characteristics $T_J = 25^\circ\text{C}$ , unless otherwise noted

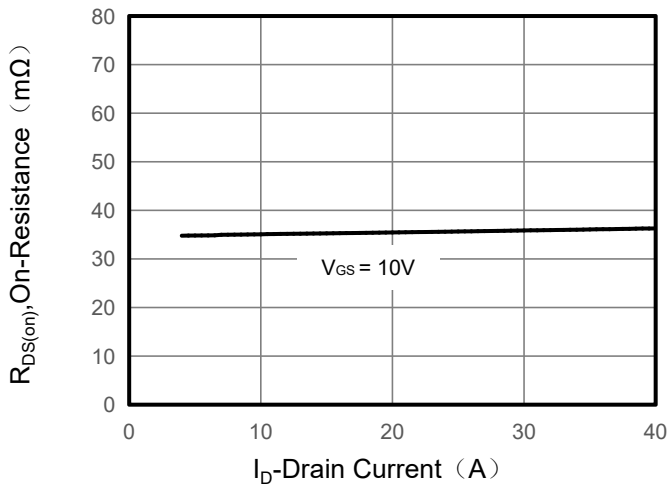
**Figure 1. Output Characteristics**



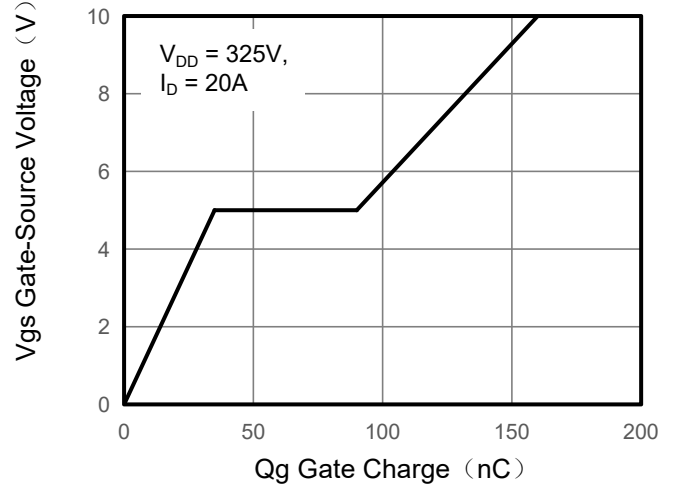
**Figure 2. Transfer Characteristics**



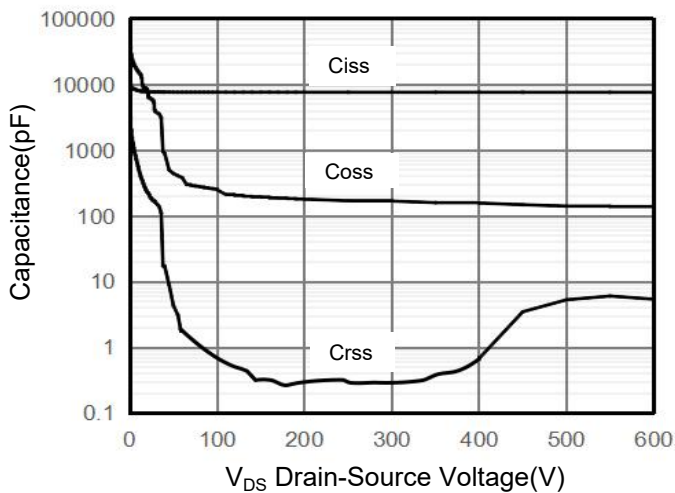
**Figure 3. Drain Source On Resistance**



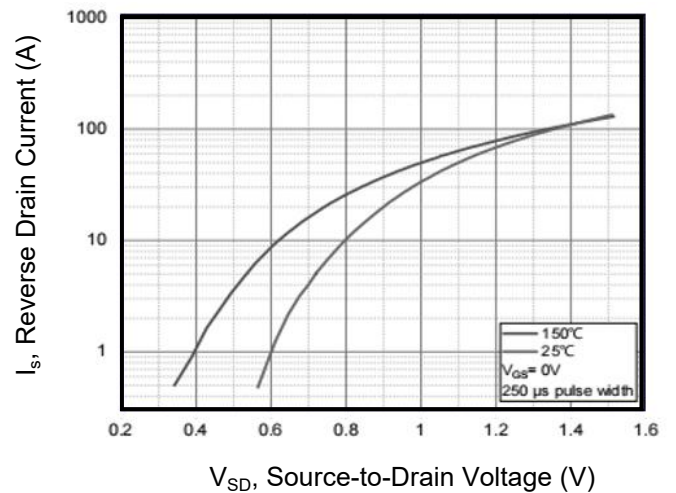
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

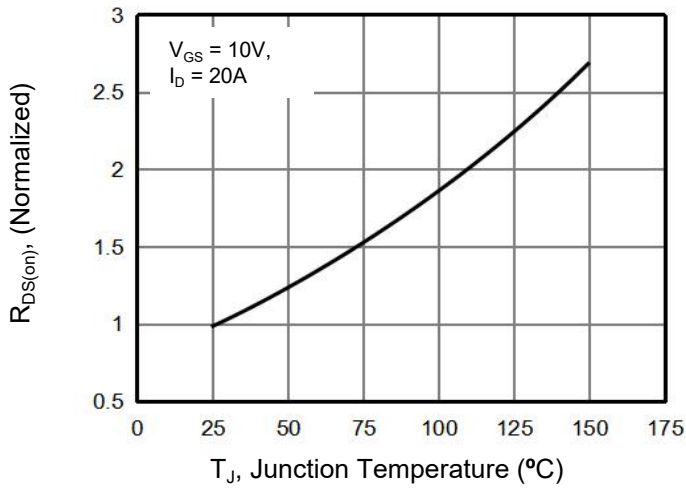


**Figure 6. Source-Drain Diode Forward**

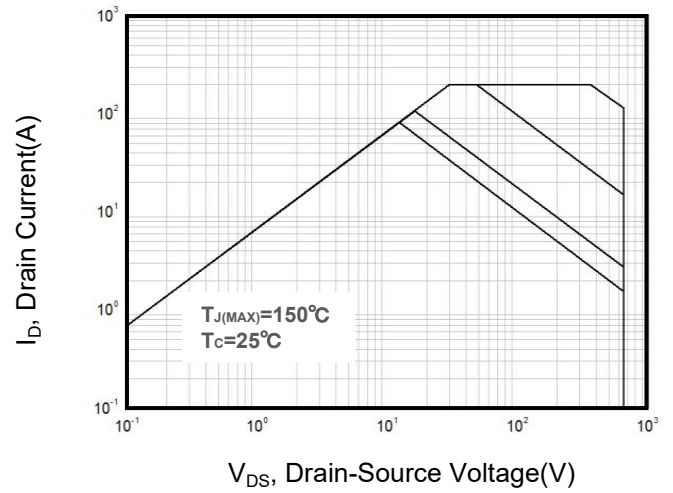


## Typical Characteristics $T_J = 25^\circ\text{C}$ , unless otherwise noted

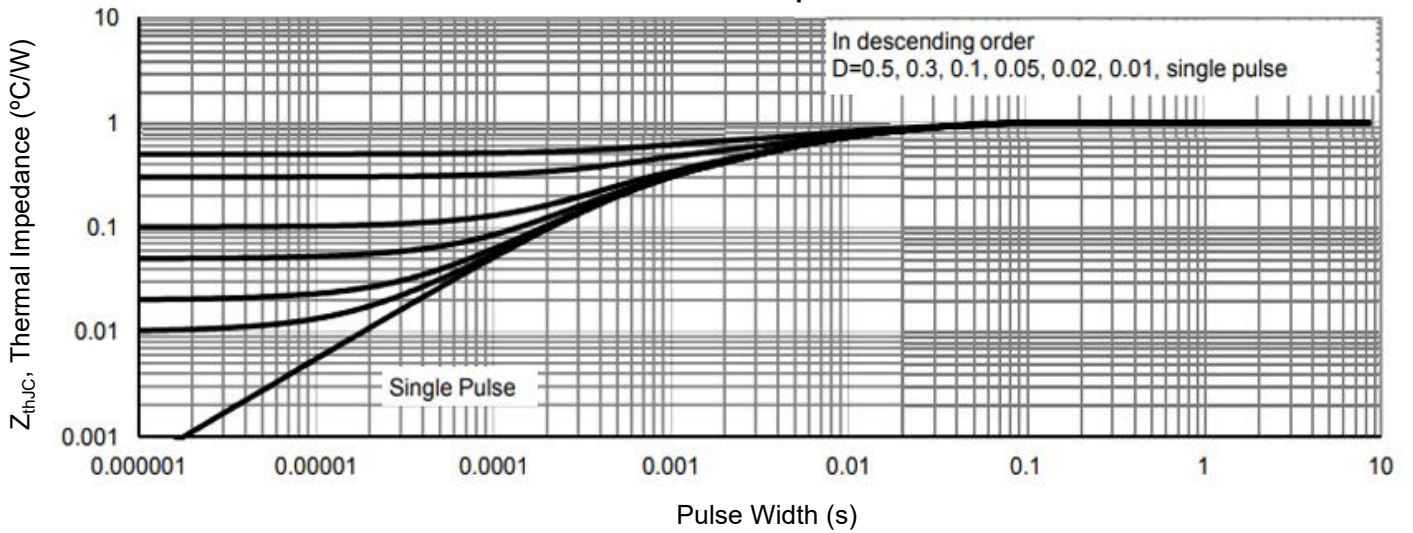
**Figure 7. Drain-Source On-Resistance**



**Figure 8. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



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