

P-Channel Enhancement Mode Power MOSFET

Description

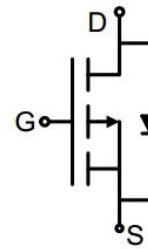
The G900P15T uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.

General Features

- V_{DS} -150V
- I_D (at $V_{GS} = -10V$) -60A
- $R_{DS(ON)}$ (at $V_{GS} = -10V$) < 80mΩ
- 100% Avalanche Tested
- RoHS Compliant

Application

- Power switch
- DC/DC converters



Schematic diagram



TO-220

Ordering Information

Device	Package	Marking	Packaging
G900P15T	TO-220	G900P15	50pcs/Tube

Absolute Maximum Ratings $T_C = 25^\circ C$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-150	V
Continuous Drain Current	I_D	-60	A
Pulsed Drain Current (note1)	I_{DM}	-240	A
Gate-Source Voltage	V_{GS}	± 20	V
Power Dissipation	P_D	100	W
Single pulse avalanche energy (note2)	E_{AS}	132	mJ
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 To 150	$^\circ C$

Thermal Resistance

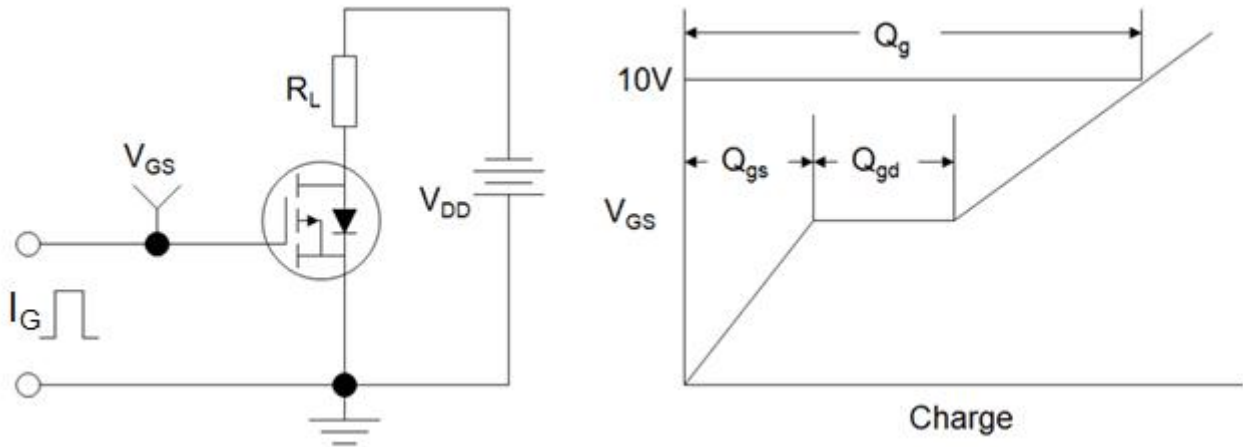
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	R_{thJA}	50	$^\circ C/W$
Maximum Junction-to-Case	R_{thJC}	1.25	$^\circ C/W$

Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-150	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -150V, V_{GS} = 0V$	--	--	-1	μA
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 20V$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1	-2.5	-4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -5A$	--	62	80	m Ω
Forward Transconductance	g_{FS}	$V_{DS} = -5V, I_D = -5A$	--	8	--	S
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{GS} = 0V,$ $V_{DS} = -75V,$ $f = 1.0MHz$	--	3932	--	pF
Output Capacitance	C_{oss}		--	152	--	
Reverse Transfer Capacitance	C_{rss}		--	108	--	
Total Gate Charge	Q_g	$V_{DD} = -75V,$ $I_D = -5A,$ $V_{GS} = -10V$	--	27	--	nC
Gate-Source Charge	Q_{gs}		--	7	--	
Gate-Drain Charge	Q_{gd}		--	3	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -75V,$ $I_D = -5A,$ $R_G = 3\Omega$	--	10	--	ns
Turn-on Rise Time	t_r		--	9	--	
Turn-off Delay Time	$t_{d(off)}$		--	29	--	
Turn-off Fall Time	t_f		--	4	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^\circ\text{C}$	--	--	-60	A
Body Diode Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{SD} = -5A, V_{GS} = 0V$	--	--	-1.2	V
Reverse Recovery Charge	Q_{rr}	$I_F = -5A, V_{GS} = 0V$ $di/dt = -500A/\mu s$	--	434	--	nC
Reverse Recovery Time	T_{rr}		--	51	--	ns

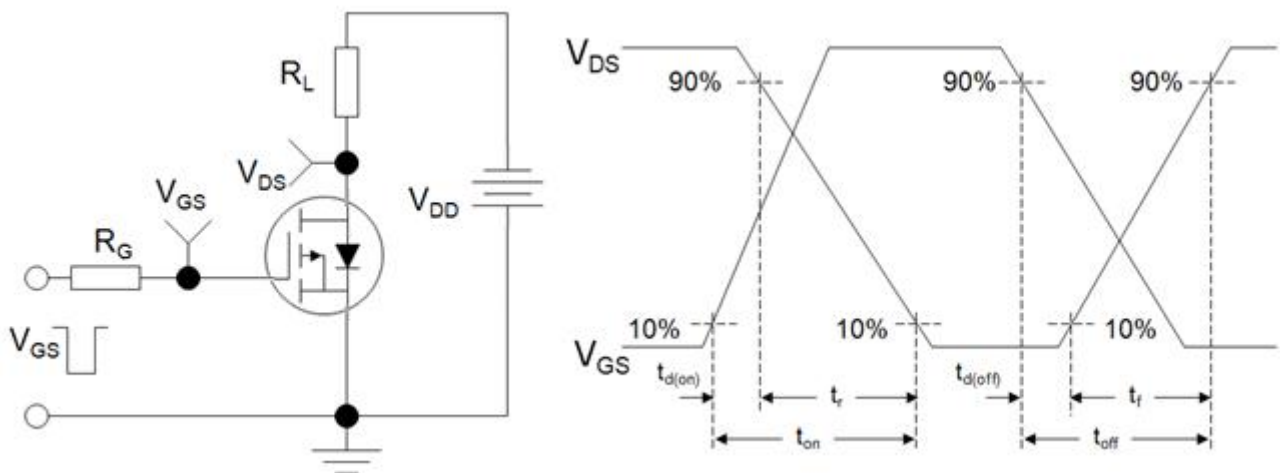
Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition : $T_J = 25^\circ\text{C}, V_{DD} = -50V, V_{GS} = -10V, L = 0.5mH, R_G = 25\Omega$
3. Identical low side and high side switch with identical R_G

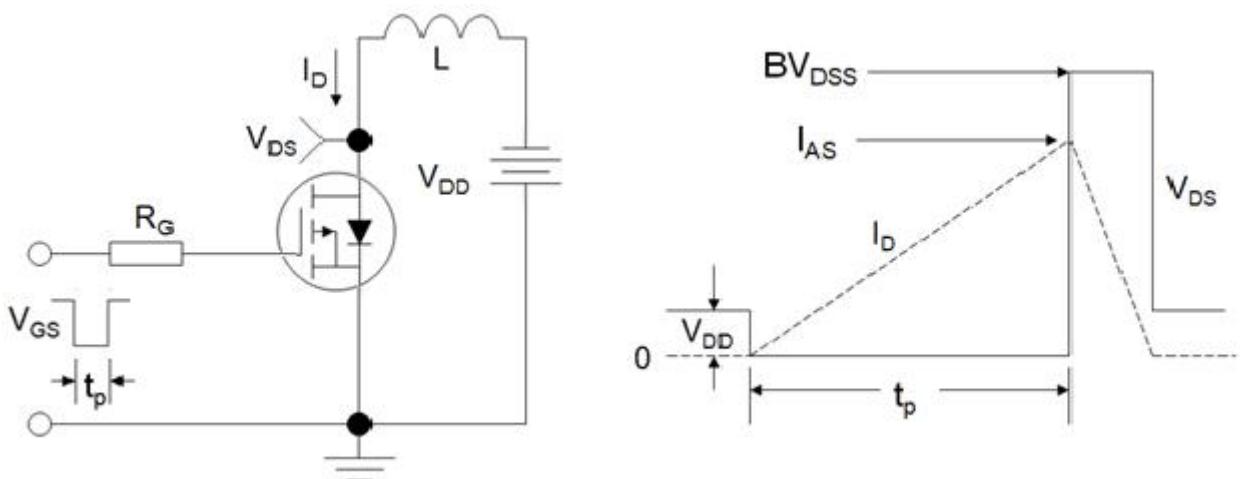
Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

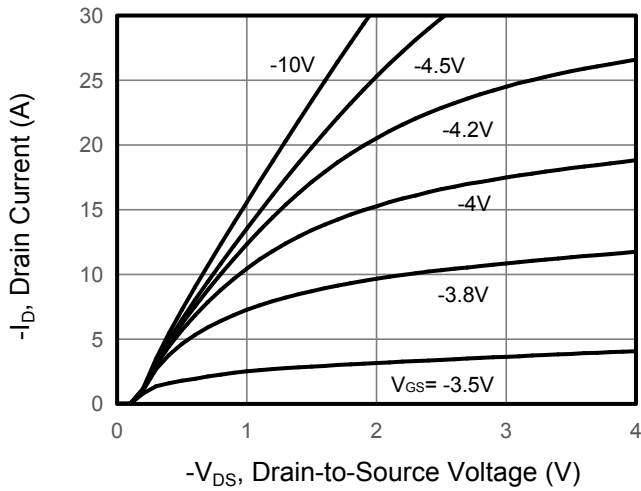


Figure 2. Transfer Characteristics

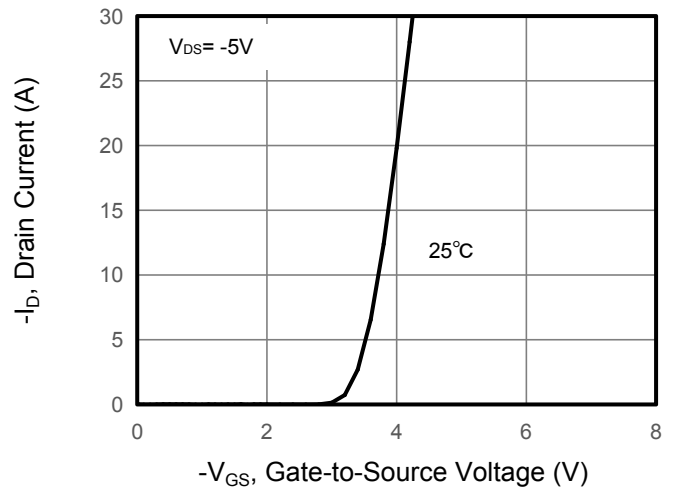


Figure 3. Drain Source On Resistance

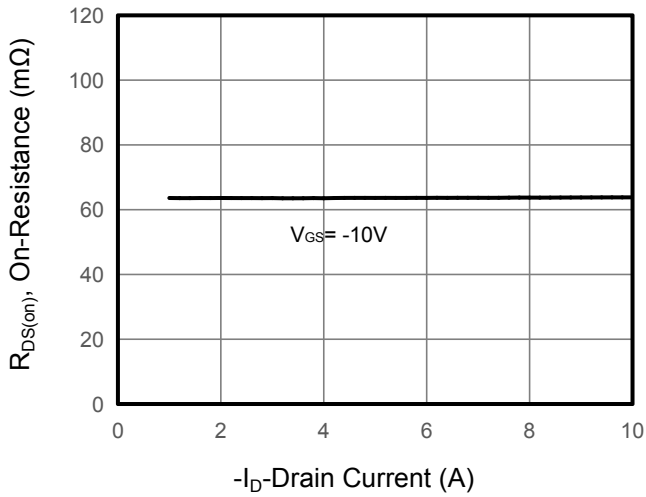


Figure 4. Gate Charge

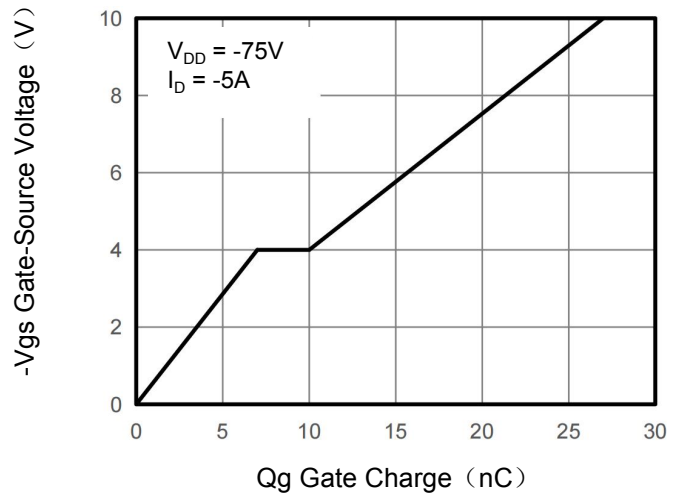


Figure 5. Capacitance

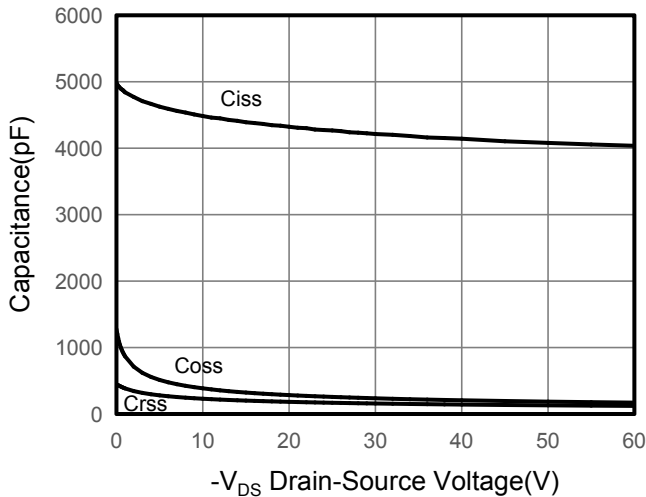
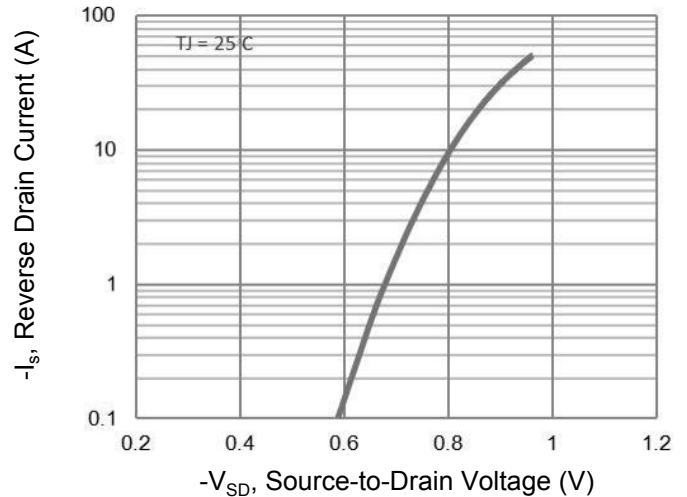


Figure 6. Source-Drain Diode Forward



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain-Source On-Resistance

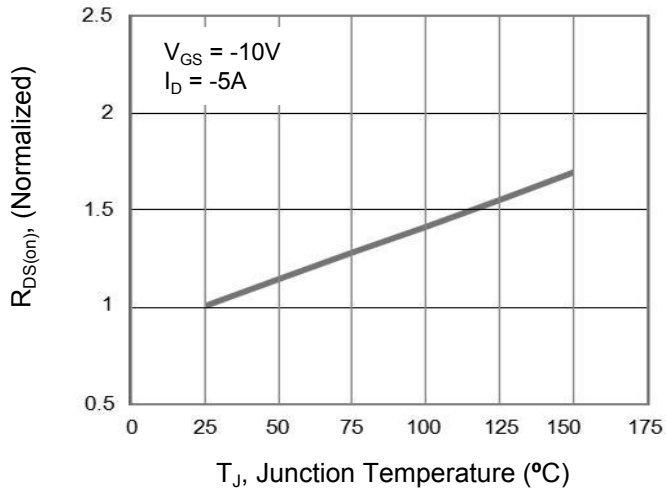


Figure 10. Safe Operation Area

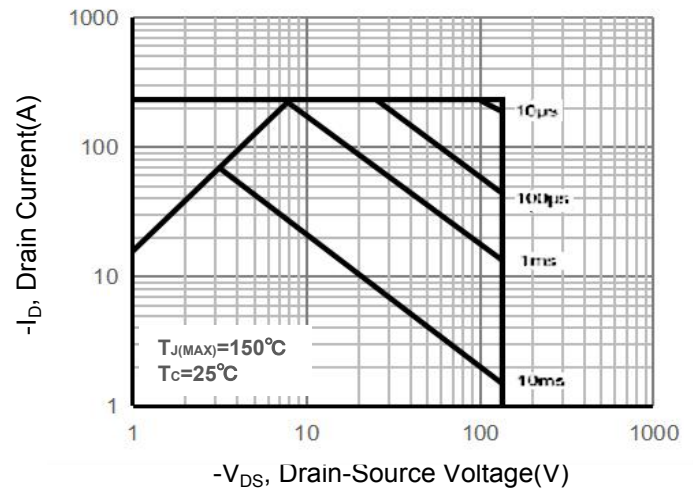
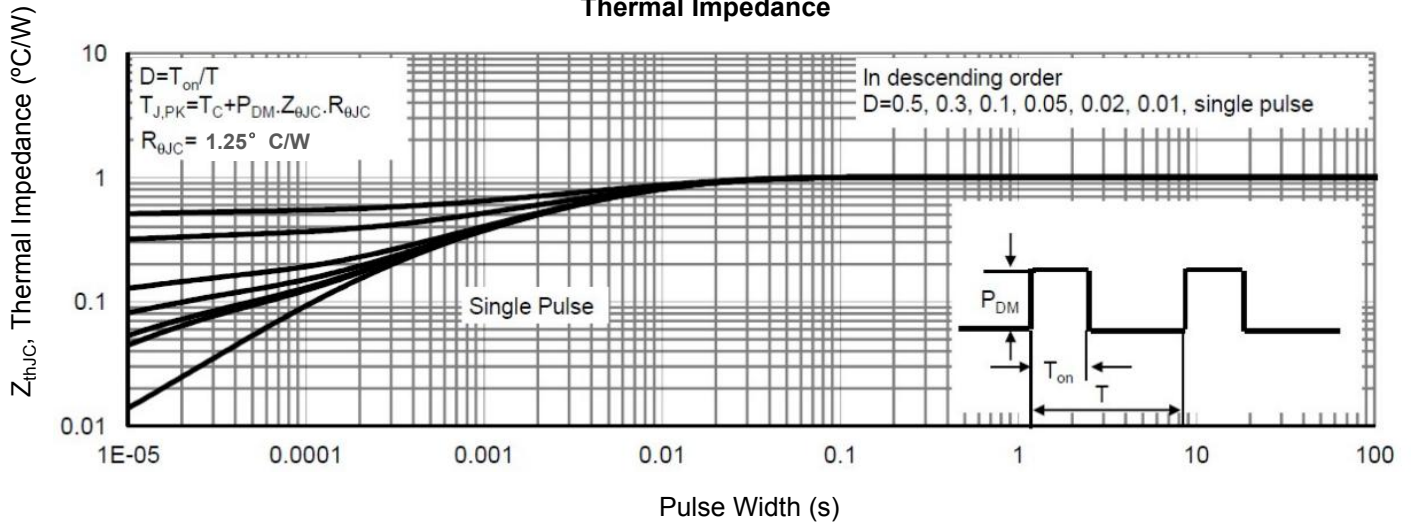
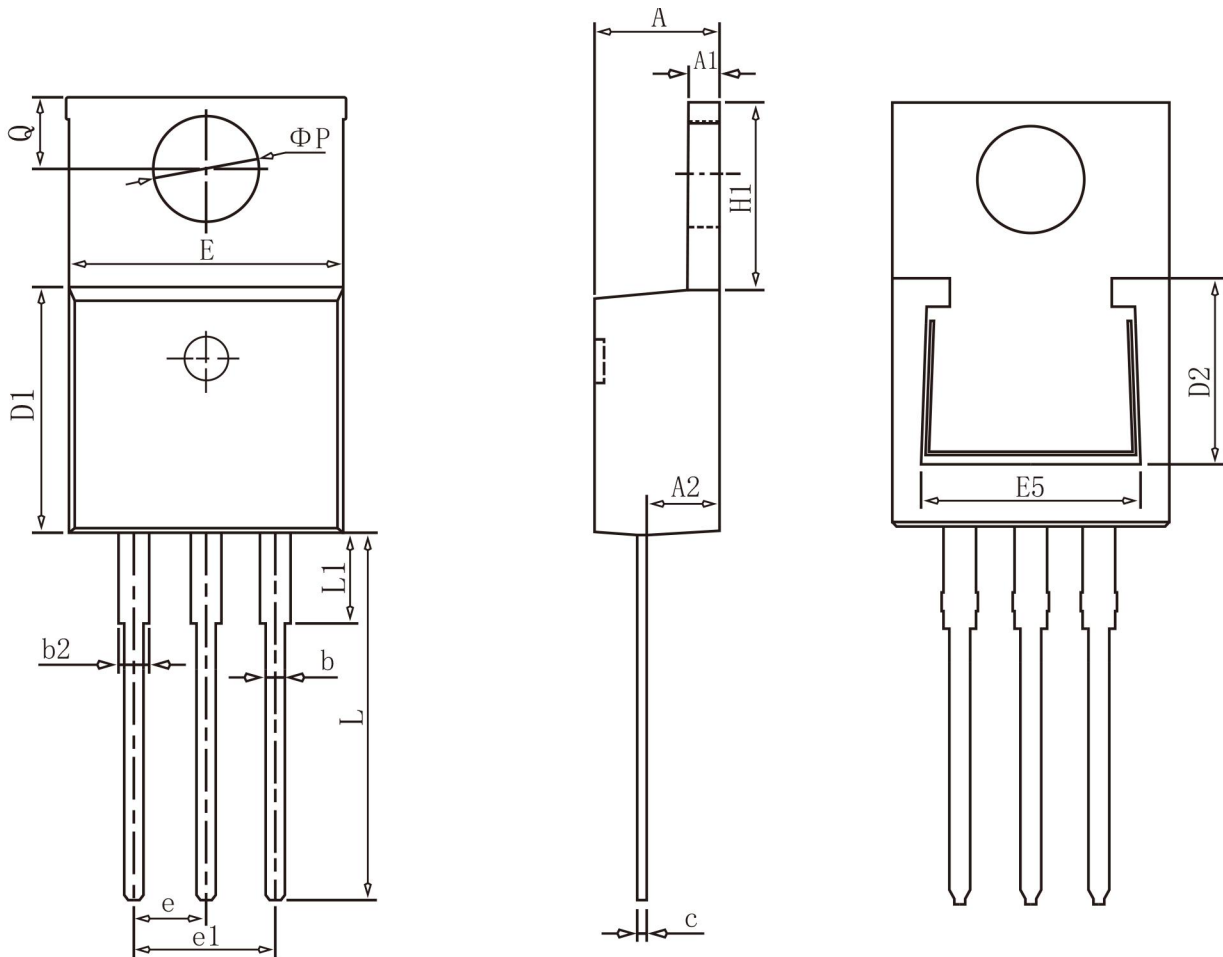


Figure 9. Normalized Maximum Transient Thermal Impedance



TO-220 Package Information



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	4.37	4.57	4.77
A1	1.22	1.27	1.42
A2	2.49	2.69	2.89
b	0.75	0.81	0.96
b2	1.22	1.27	1.47
c	0.30	0.38	0.48
D1	8.50	8.70	8.90
D2	5.20	-	-
E	9.86	10.16	10.36
E5	7.06	-	-
e	2.54BSC		
e1	5.08BSC		
H1	6.10	6.30	6.50
L	13.10	13.40	13.70
L1	-	3.75	4.10
ΦP	3.70	3.84	3.99
Q	2.54	2.74	2.94

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