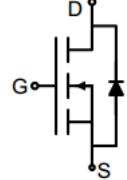


# N-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The GT60N10 uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> 100V</li> <li>● <math>I_D</math> (at <math>V_{GS} = 10V</math>) 75A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 10V</math>) &lt; 9.5mΩ</li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	 <p>Schematic Diagram</p>  <p>Marking and pin assignment</p>  <p>TO-220</p>		
<b>Device</b>	<b>Package</b>	<b>Marking</b>	<b>Packaging</b>
GT60N10	TO-220	GT60N10	50pcs/Tube

<b>Absolute Maximum Ratings</b> $T_C = 25^\circ\text{C}$ , unless otherwise noted			
Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Continuous Drain Current	$I_D$	75	A
Pulsed Drain Current (note1)	$I_{DM}$	300	A
Gate-Source Voltage	$V_{GS}$	$\pm 18$	V
Power Dissipation	$P_D$	107	W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	$^\circ\text{C}$

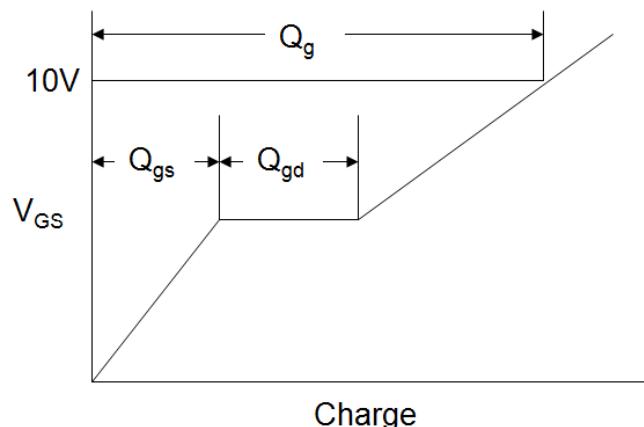
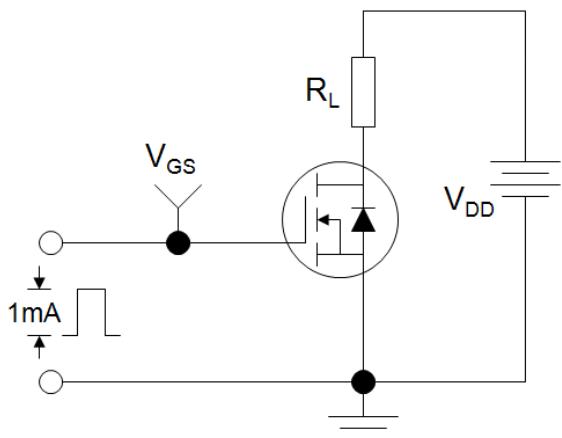
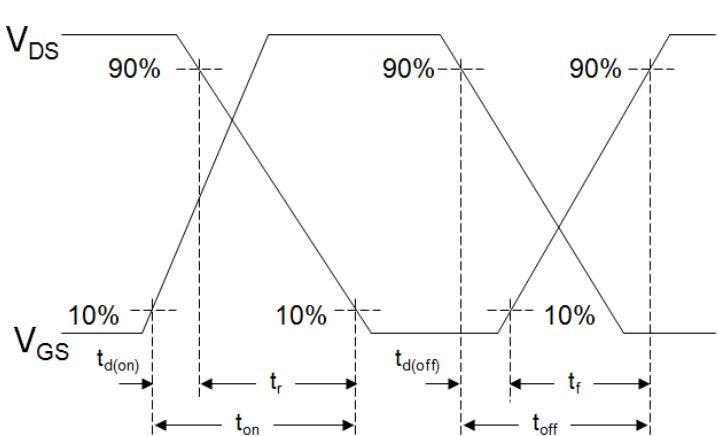
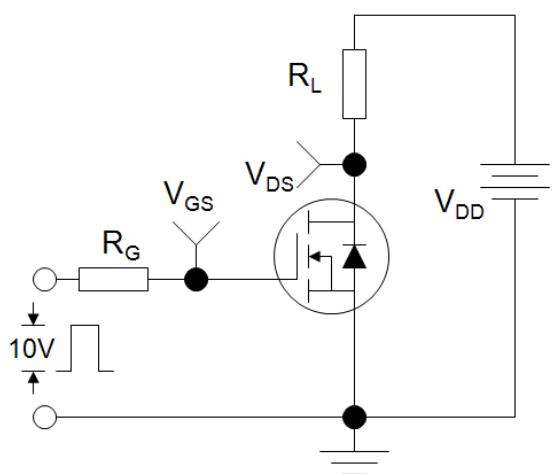
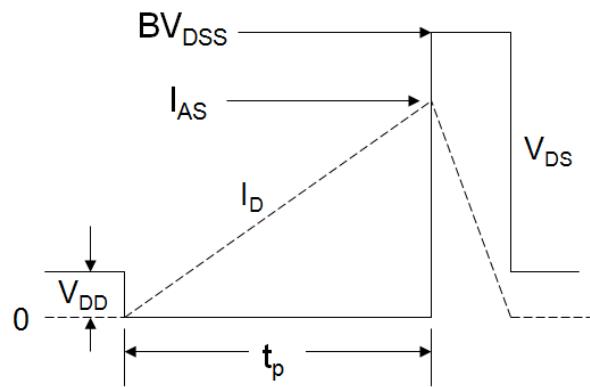
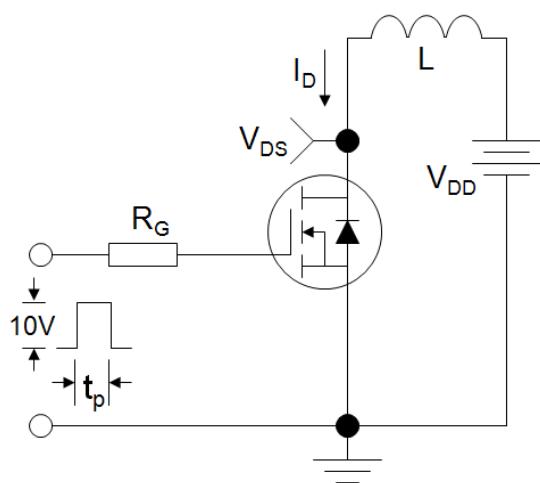
<b>Thermal Resistance</b>			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	12	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Case	$R_{thJC}$	1.1	$^\circ\text{C/W}$

**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	100	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 100\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 18\text{V}$	--	--	$\pm 200$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	2.2	3	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 4\text{A}$	--	7.1	9.5	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=5\text{V}, I_D=20\text{A}$	--	62	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 50\text{V}, f = 1.0\text{MHz}$	--	2785	--	pF
Output Capacitance	$C_{\text{oss}}$		--	238	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	12	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 50\text{V}, I_D = 20\text{A}, V_{\text{GS}} = 10\text{V}$	--	37	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	11.5	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	5	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 50\text{V}, I_D = 20\text{A}, R_G = 3\Omega$	--	13	--	nS
Turn-on Rise Time	$t_r$		--	8.5	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	29	--	
Turn-off Fall Time	$t_f$		--	4	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_s$	$T_C = 25^\circ\text{C}$	--	--	75	A
Pulsed Diode Forward Current	$I_{\text{SM}}$		--	--	300	
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 20\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.2	V

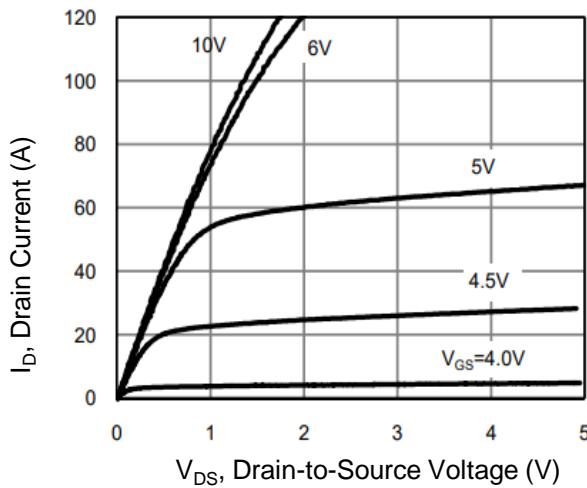
**Notes**

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Identical low side and high side switch with identical  $R_G$

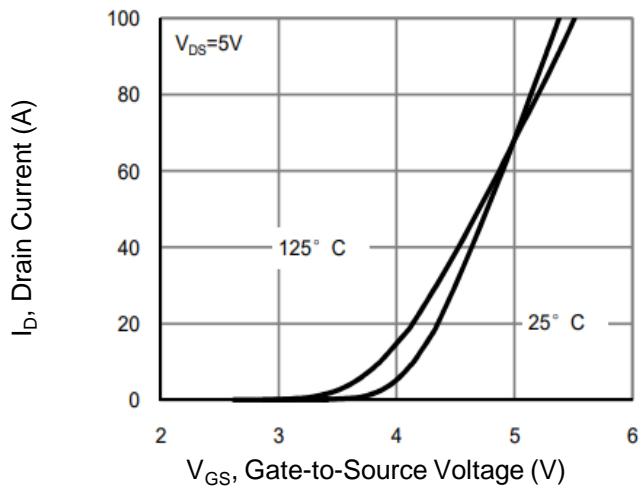
**Gate Charge Test Circuit****EAS Test Circuit****Switch Time Test Circuit**

**Typical Characteristics**  $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

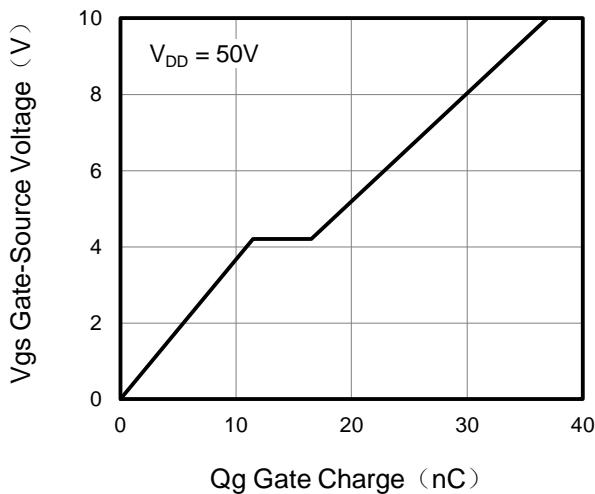
**Figure 1. Output Characteristics**



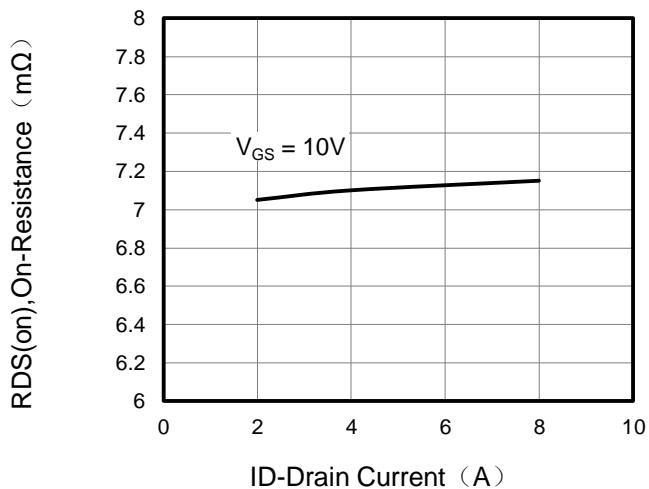
**Figure 2. Transfer Characteristics**



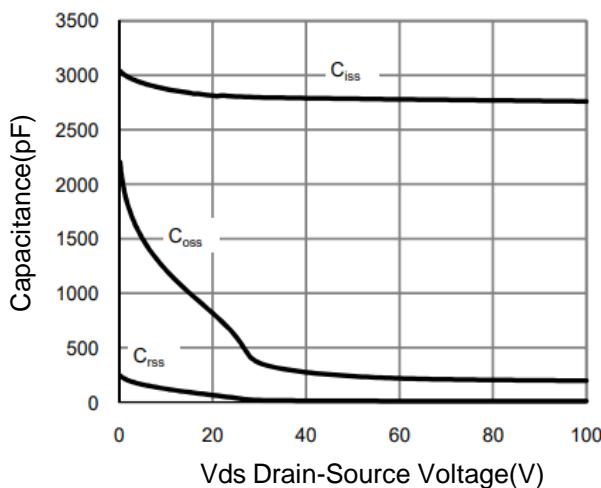
**Figure 3. Gate Charge**



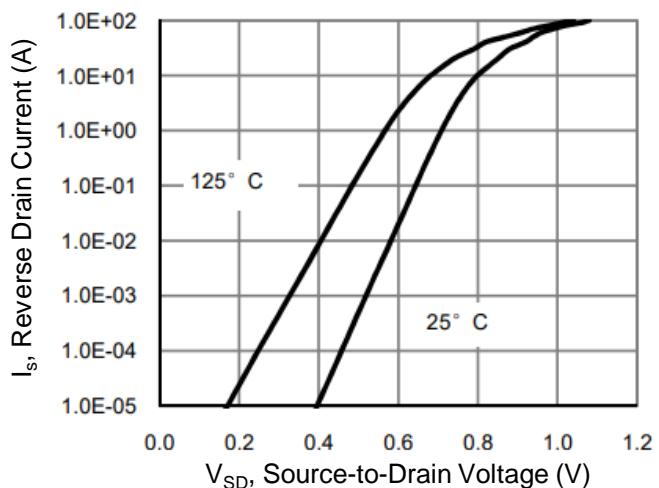
**Figure 4. Drain Source On Resistance**



**Figure 5. Capacitance vs Vds**

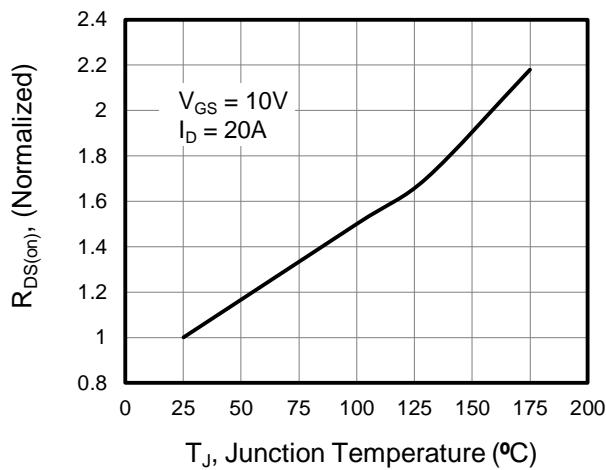


**Figure 6. Source-Drain Diode Forward**

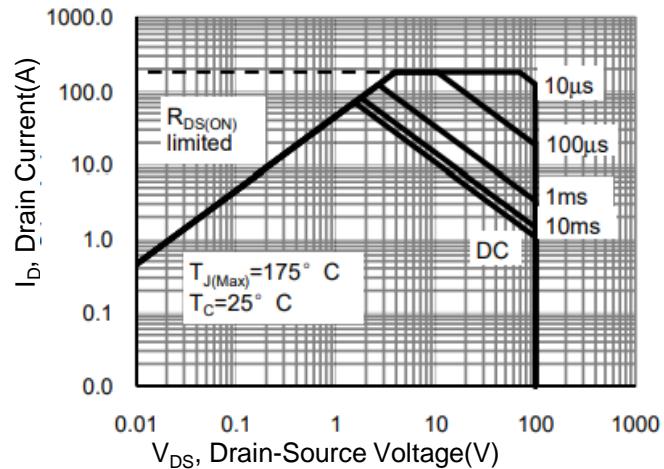


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

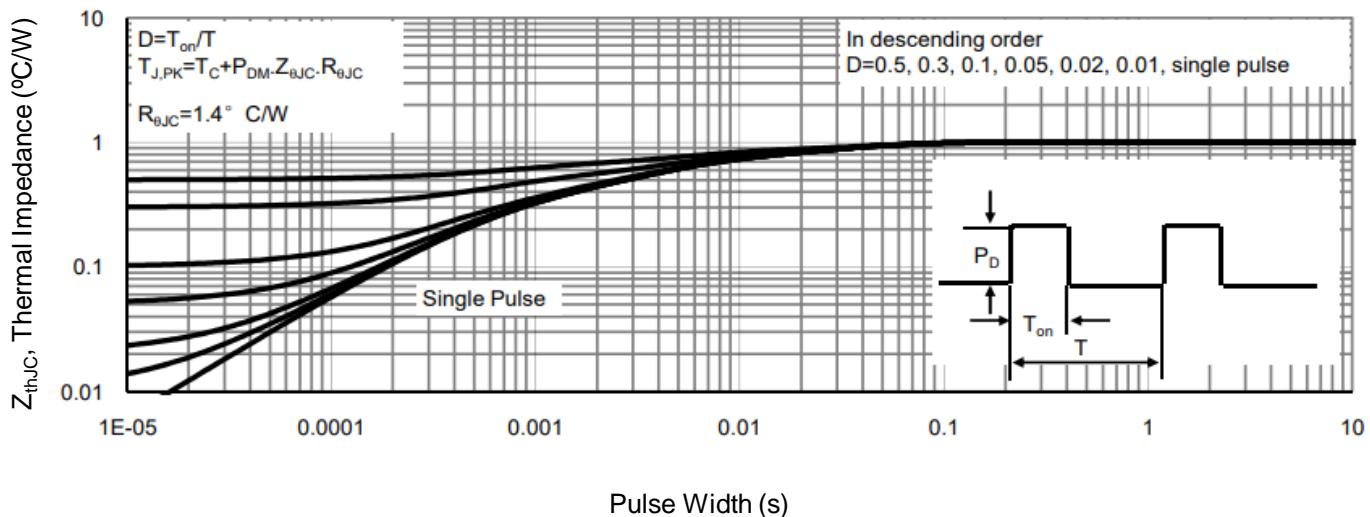
**Figure 7. Drain-Source On-Resistance**



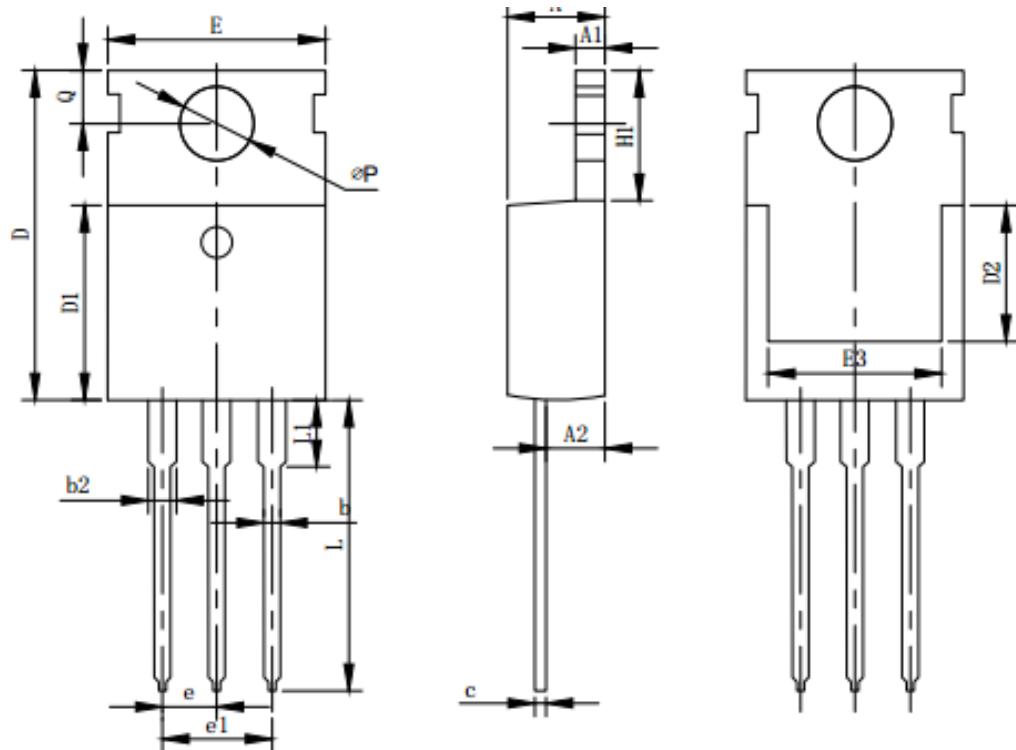
**Figure 8. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



## TO-220 Package Information



Symbol	Dimensions in Millimeters		
	MIN.	NOM.	MAX.
A	4.37	4.57	4.7
A1	1.25	1.3	1.4
A2	2.2	2.4	2.6
b	0.7	0.8	0.95
b2	1.7	1.27	1.47
c	0.45	0.5	0.6
D	15.1	15.6	16.1
D1	8.8	9.1	9.4
D2	5.5		
E	9.7	10	10.3
e	2.54BSC		
e1	5.08BSC		
H1	6.25	6.5	6.85
L	12.75	13.5	13.8
L1		3.1	3.4
ØP	3.4	3.6	3.8
Q	2.6	2.8	3

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