

Description

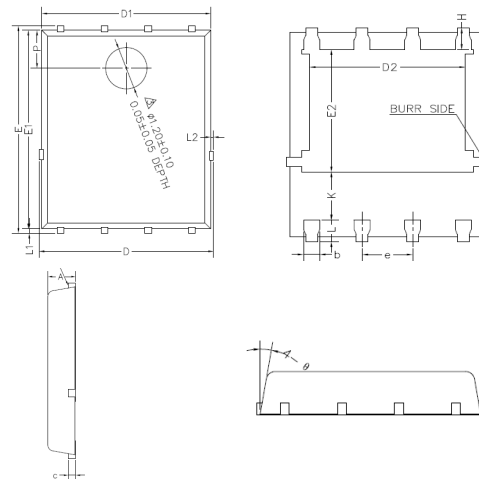
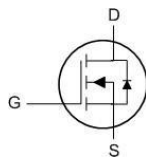
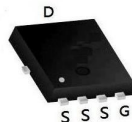
The 80N03NF is the high cell density Trench MOSFET, which provide excellent R_{DS(on)} and gate charge for DC/DC converters application.

The 80N03NF meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

100% EAS Guaranteed
Green Device Available
Super Low Gate Charge

Low R_{DS(on)}

Advanced high cell density Trench technology



PDFN5X6

Product Summary

BVDSS	R _{DS(on)}	ID
30V	4.3mΩ	82A

Dimensions In Millimeters

SYMBOL	min	max	SYMBOL	min	max	SYMBOL	min	max
A	0.95	1.20	E	5.9	6.1	L2		0.2
b	0.25	0.40	E1	5.7	5.8	θ		13°
c	0.21	0.34	E2	3.34	3.54	P	1.0	1.2
D		5.1	H	0.51	0.71			
D1	4.8	5.0	K	1.1				
D2	3.91	4.20	L	0.51	0.71			
e	1.17	1.37	L1	0.06	0.2			

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	82	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	53	A
I _{DM}	Pulsed Drain Current ²	155	A
EAS	Single Pulse Avalanche Energy ³	38.8	mJ
I _{AS}	Avalanche Current	34	A
P _D @T _C =25°C	Total Power Dissipation ⁴	37	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	50	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	4.6	°C/W

80N03NF

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =20A	---	4.3	6.2	mΩ
		V _{GS} =4.5V, I _D =15A	---	5.7	8	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	---	2.5	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =24V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =20A	---	67	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.7	---	Ω
Q _g	Total Gate Charge (4.5V)	V _{DS} =15V, V _{GS} =4.5V, I _D =15A	---	8	---	nC
Q _{gs}	Gate-Source Charge		---	2.4	---	
Q _{gd}	Gate-Drain Charge		---	3.2	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V, V _{GS} =10V, R _G =3.3Ω I _D =15A	---	7.1	---	ns
T _r	Rise Time		---	40	---	
T _{d(off)}	Turn-Off Delay Time		---	15	---	
T _f	Fall Time		---	6	---	
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	814	---	pF
C _{oss}	Output Capacitance		---	498	---	
C _{rss}	Reverse Transfer Capacitance		---	41	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V _G =V _D =0V, Force Current	---	---	82	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1	V
t _{rr}	Reverse Recovery Time	I _F =20A, di/dt=100A/μs,	---	15	---	nS
Q _{rr}	Reverse Recovery Charge	T _J =25°C	---	25	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=24A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

RATING AND CHARACTERISTIC CURVES (80N03NF)

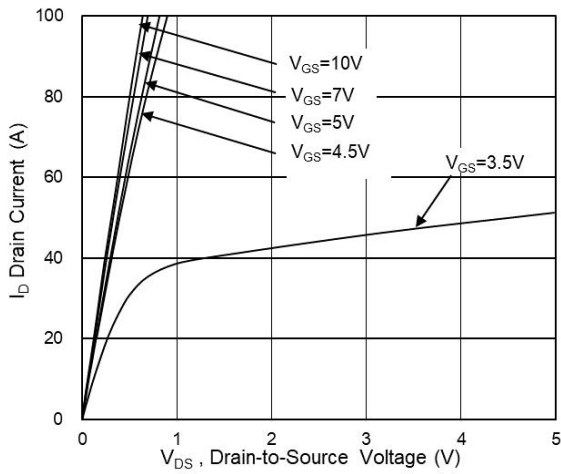


Fig.1 Typical Output Characteristics

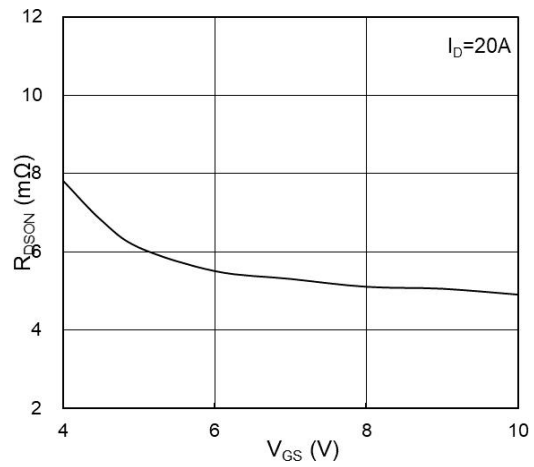


Fig.2 On-Resistance vs G-S Voltage

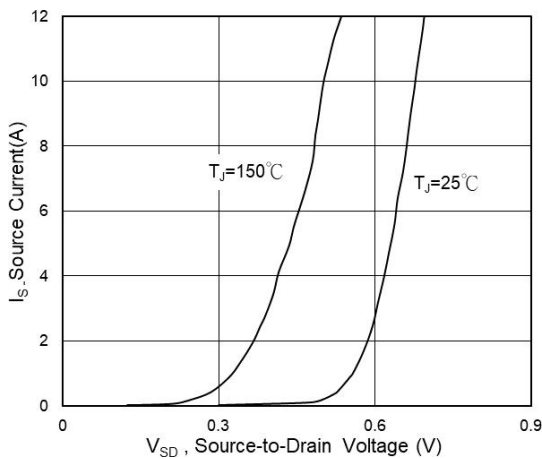


Fig.3 Source Drain Forward Characteristics

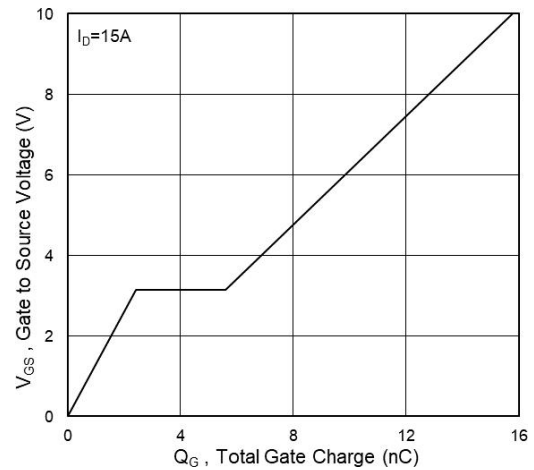


Fig.4 Gate-Charge Characteristics

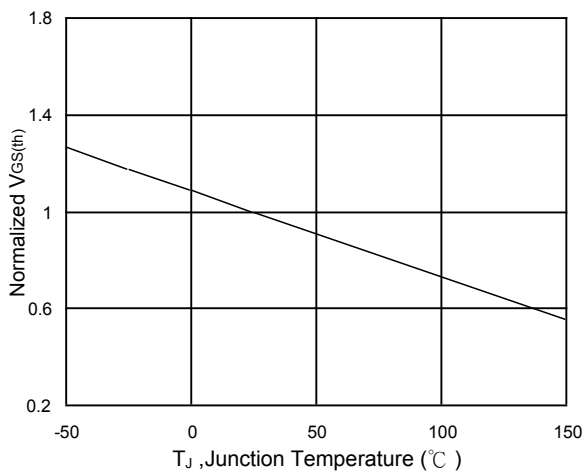


Fig.5 Normalized $V_{GS(th)}$ vs T_J

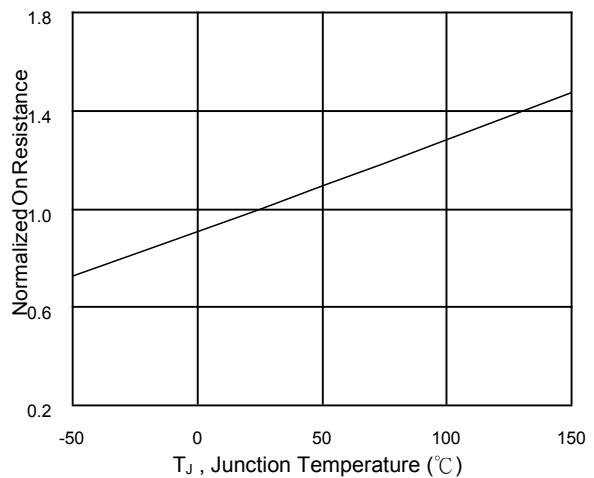


Fig.6 Normalized $R_{DS(on)}$ vs T_J

RATING AND CHARACTERISTIC CURVES (80N03NF)

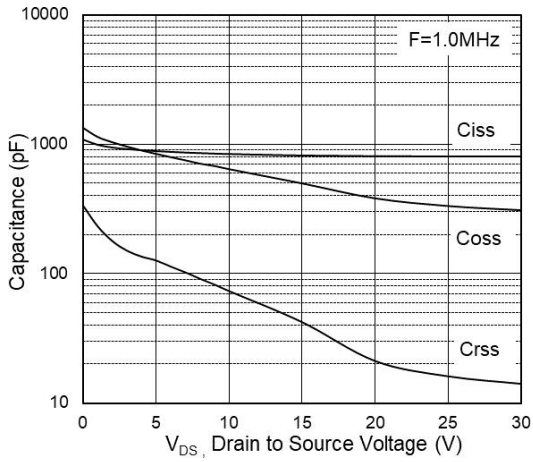


Fig.7 Capacitance

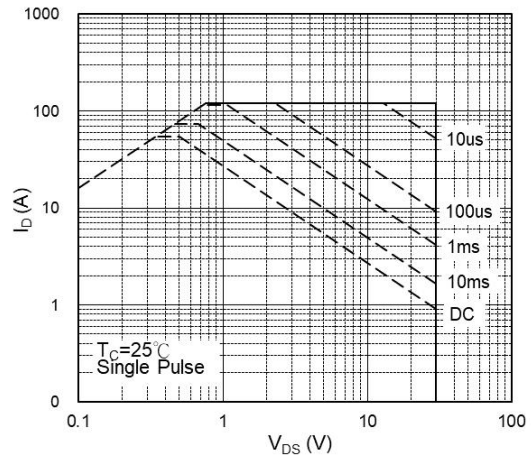


Fig.8 Safe Operating Area

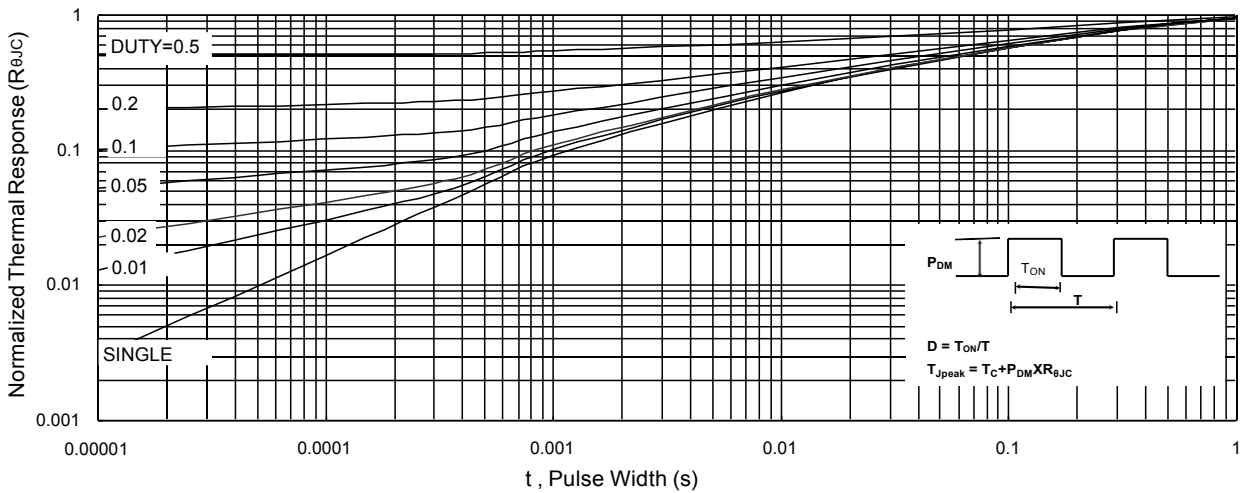


Fig.9 Normalized Maximum Transient Thermal Impedance

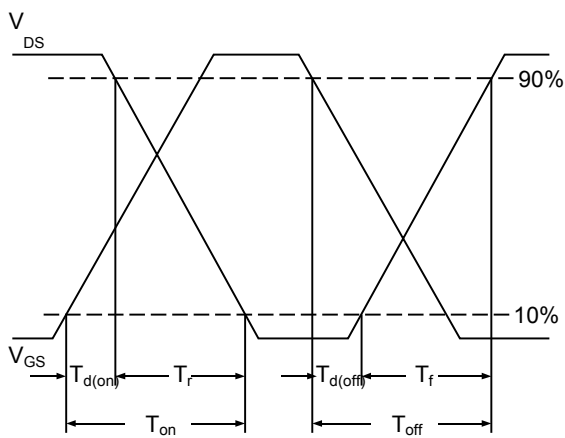


Fig.10 Switching Time Waveform

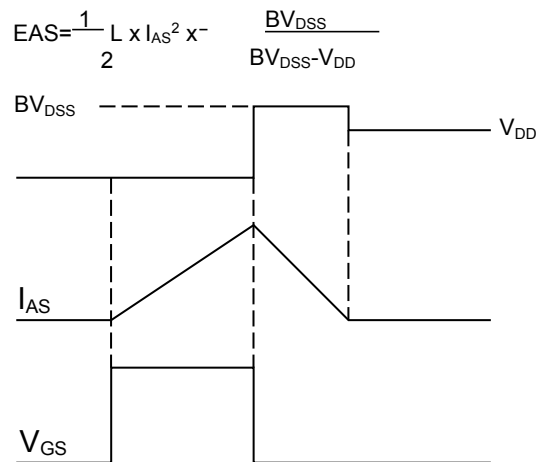


Fig.11 Unclamped Inductive Switching Waveform

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