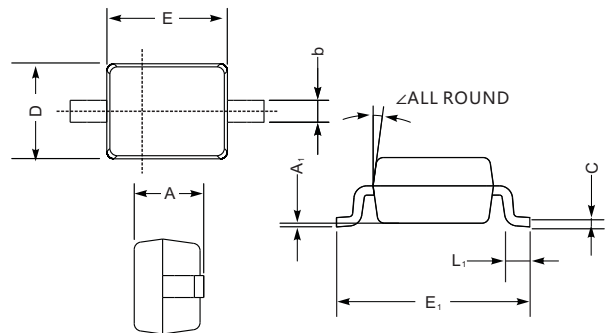
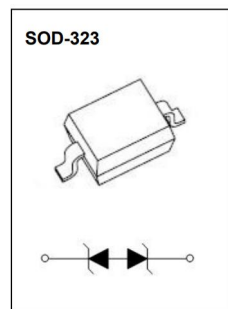
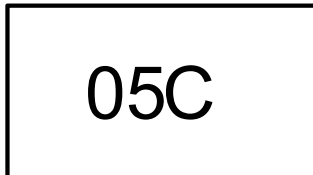


## Features

- Bi-directional ESD protection of one line
- Reverse stand-off voltage: 5V
- Low reverse clamping voltage
- Low leakage current
- Fast response time
- IEC 61000-4-2 (ESD) immunity test :
- Air discharge:  $\pm 30\text{kV}$
- Contact discharge:  $\pm 30\text{kV}$

## Marking Diagram



SOD-323 mechanical data

UNIT		A	C	D	E	E <sub>1</sub>	b	L <sub>1</sub>	A <sub>1</sub>	∠
mm	max	1.1	0.15	1.4	1.8	2.75	0.4	0.45	0.2	9°
	min	0.8	0.08	1.2	1.4	2.55	0.25	0.2	—	
mil	max	43	5.9	55	70	108	16	16	8	
	min	32	3.1	47	63	100	9.8	7.9	—	

## Description

Designed to protect voltage sensitive electronic components from ESD and other transients. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

The combination of small size, low capacitance, and high level of ESD protection makes them a flexible solution for applications such as HDMI, Display Port TM, and MDDI interfaces. It is designed to replace multiplayer varistors (MLV) in consumer equipments applications such as mobile phone, notebook, PAD, STB, LCD TV etc.

## Applications

- Computers and peripherals
- High speed data lines
- Audio and video equipment
- Cellular handsets and accessories
- Subscriber identity module(SIM) card protection
- Portable electronics
- FireWire
- Other electronics equipments communi- cation systems

## ES05D1HA03

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise specified)

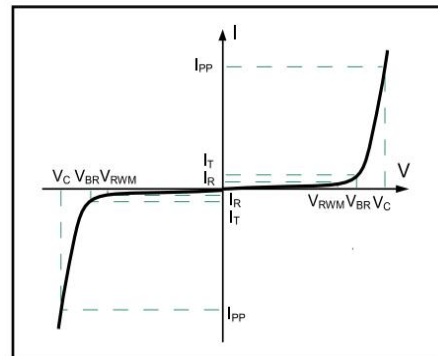
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 $\mu\text{s}$ )	Ppk	350	W
Peak Pulse Current (8/20 $\mu\text{s}$ )	IPP	20	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	VESD	$\pm 30$ $\pm 30$	kV
Operating Temperature Range	TJ	-55to +125	$^\circ\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^\circ\text{C}$

### Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise specified)

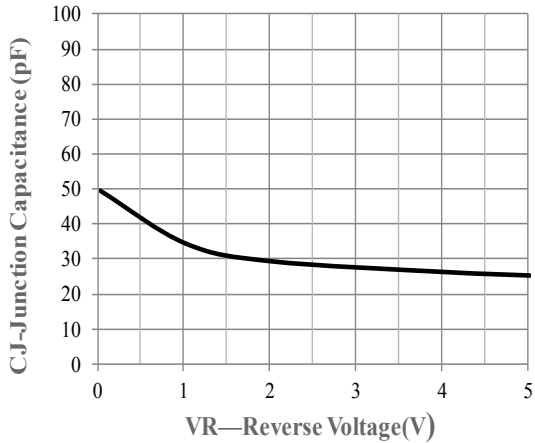
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	$V_{RWM}$				5	V
Breakdown Voltage	$V_{BR}$	$I_T = 1\text{mA}$	5.6		8	V
Reverse Leakage Current	$I_R$	$V_{RWM} = \pm 5\text{V}$			0.5	$\mu\text{A}$
Clamping Voltage	$V_C$	$I_{PP} = 20\text{A}$ (8 x 20 $\mu\text{s}$ pulse)			16	V
Junction Capacitance	$C_j$	$V_R = 0\text{V}$ , $f = 1\text{MHz}$			50	pF

### Electronics Parameter

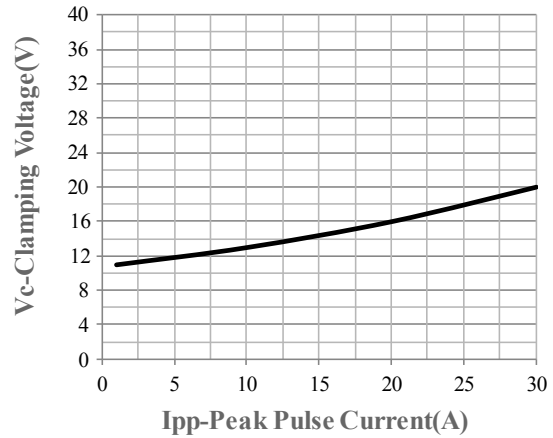
Symbol	Parameter
$I_T$	Test Current
$I_{PP}$	Maximum Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_C$
$V_{BR}$	Breakdown Voltage @ $I_T$
$I_R$	Reverse Leakage Current @ $V_{RWM}$
$V_{RWM}$	Reverse Standoff Voltage



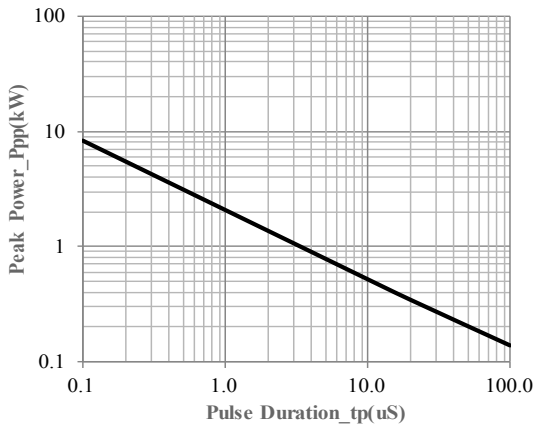
## Typical Performance Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise Specified)



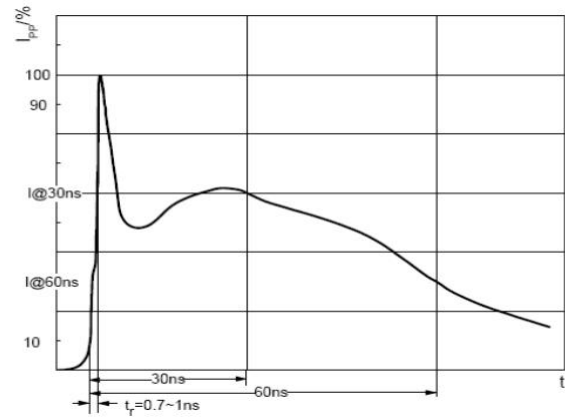
Junction Capacitance vs. Reverse Voltage



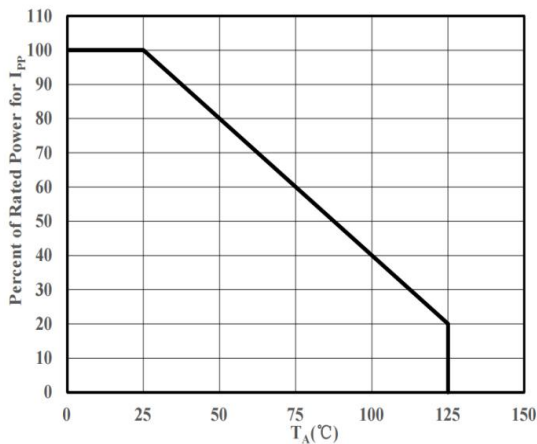
Clamping Voltage vs. Peak Pulse Current



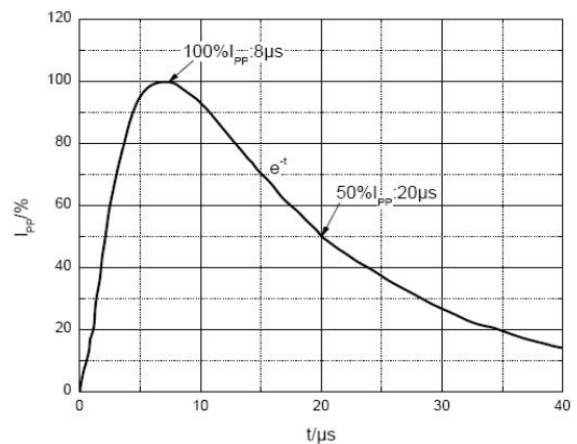
Peak Pulse Power vs. Pulse Time



ESD pulse waveform according to IEC61000-4-2



Power Derating Curve



8/20µs pulse waveform according to IEC 61000-4-5

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