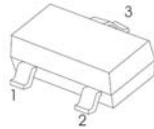


FEATURE

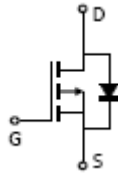
- High density cell design for low $R_{DS(ON)}$
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability

SOT-23

1. GATE
2. SOURCE
3. DRAIN

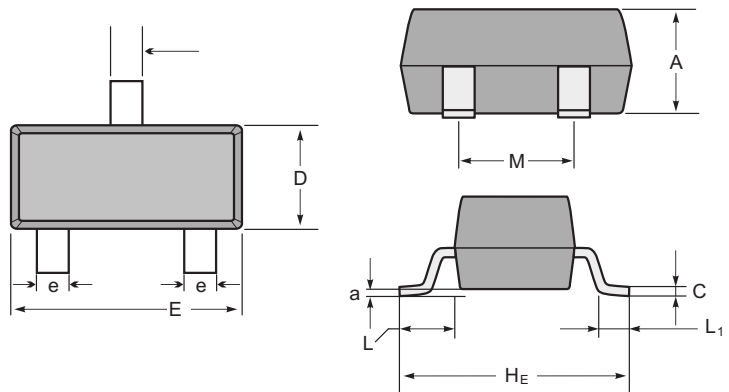


Equivalent Circuit



Marking

Type number	Marking code
SI2301	A1SHB



SOT-23 mechanical data

UNIT		A	C	D	E	H _E	e	M	L	L ₁	a
mm	max	1.1	0.15	1.4	3.0	2.6	0.5	1.95	0.55 (ref)	0.36 (ref)	0.0
	min	0.9	0.08	1.2	2.8	2.2	0.3	1.7			0.15
mil	max	43	6	55	118	102	20	77	22 (ref)	14 (ref)	0.0
	min	35	3	47	110	87	12	67			6

Maximum ratings ($T_a=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 8	
Continuous Drain Current	I_D	-3	A
Pulsed Drain Current	I_{DM}	-10	
Continuous Source-Drain Diode Current	I_S	-0.72	
Maximum Power Dissipation	P_D	1.2	W
Thermal Resistance from Junction to Ambient($t \leq 5s$)	$R_{\theta JA}$	357	$^{\circ}\text{C/W}$
Junction Temperature	T_J	150	$^{\circ}\text{C}$
Storage Temperature	T_{stg}	-55 ~ +150	

SI2301

Electrical characteristics (T_a=25°C unless otherwise noted)

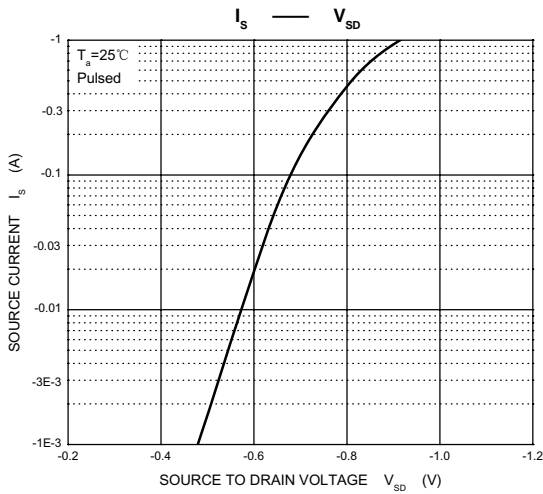
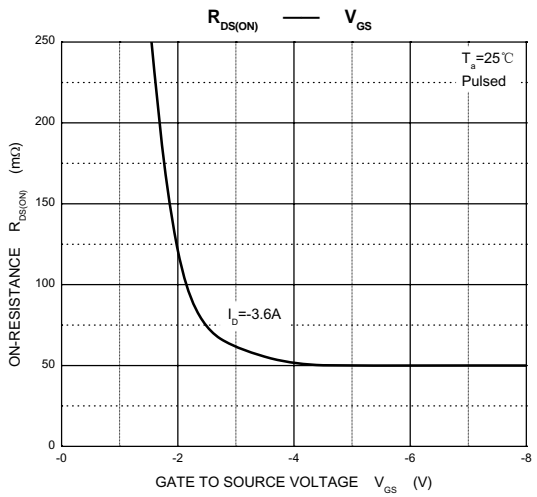
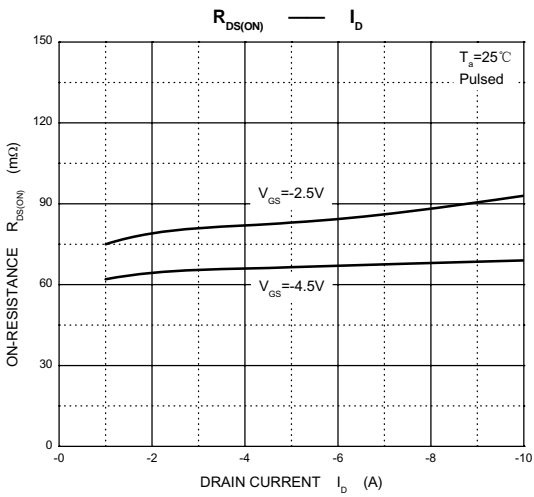
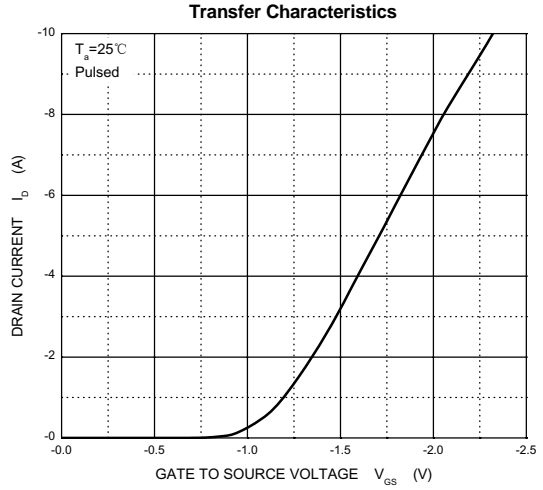
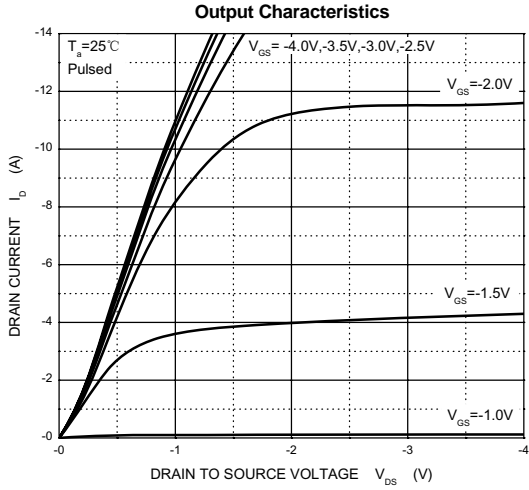
Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Static						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-20			V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-0.4		-1	
Gate-source leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±8V			±100	nA
Zero gate voltage drain current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0V			-1	μA
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = -4.5V, I _D = -3A		0.070	0.092	Ω
		V _{GS} = -2.5V, I _D = -2.0A		0.090	0.122	
Forward transconductance ^a	g _{fs}	V _{DS} = -5V, I _D = -3A		6.5		S
Dynamic^b						
Input capacitance	C _{iSS}	V _{DS} = -10V, V _{GS} = 0V, f = 1MHz		405		pF
Output capacitance	C _{oSS}			75		
Reverse transfer capacitance	C _{rSS}			55		
Total gate charge	Q _g	V _{DS} = -10V, V _{GS} = -4.5V, I _D = -3A		5.5	10	nC
		V _{DS} = -10V, V _{GS} = -2.5V, I _D = -3A		3.3	6	
Gate-source charge	Q _{gs}			0.7		
Gate-drain charge	Q _{gd}			1.3		
Gate resistance	R _g	f = 1MHz		6.0		Ω
Turn-on delay time	t _{d(on)}	V _{DD} = -10V, R _L = 10Ω, I _D = -1A, V _{GEN} = -4.5V, R _g = 1Ω		11	20	ns
Rise time	t _r			35	60	
Turn-off delay time	t _{d(off)}			30	50	
Fall time	t _f			10	20	
Drain-source body diode characteristics						
Continuous source-drain diode current	I _S	T _C = 25°C			-1.3	A
Pulse diode forward current ^a	I _{SM}				-10	
Body diode voltage	V _{SD}	I _S = -0.7A		-0.8	-1.2	V

Notes :

a. Pulse Test : Pulse Width < 300μs, Duty Cycle ≤2%.

b. Guaranteed by design, not subject to production testing.

RATING AND CHARACTERISTIC CURVES (SI2301)



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