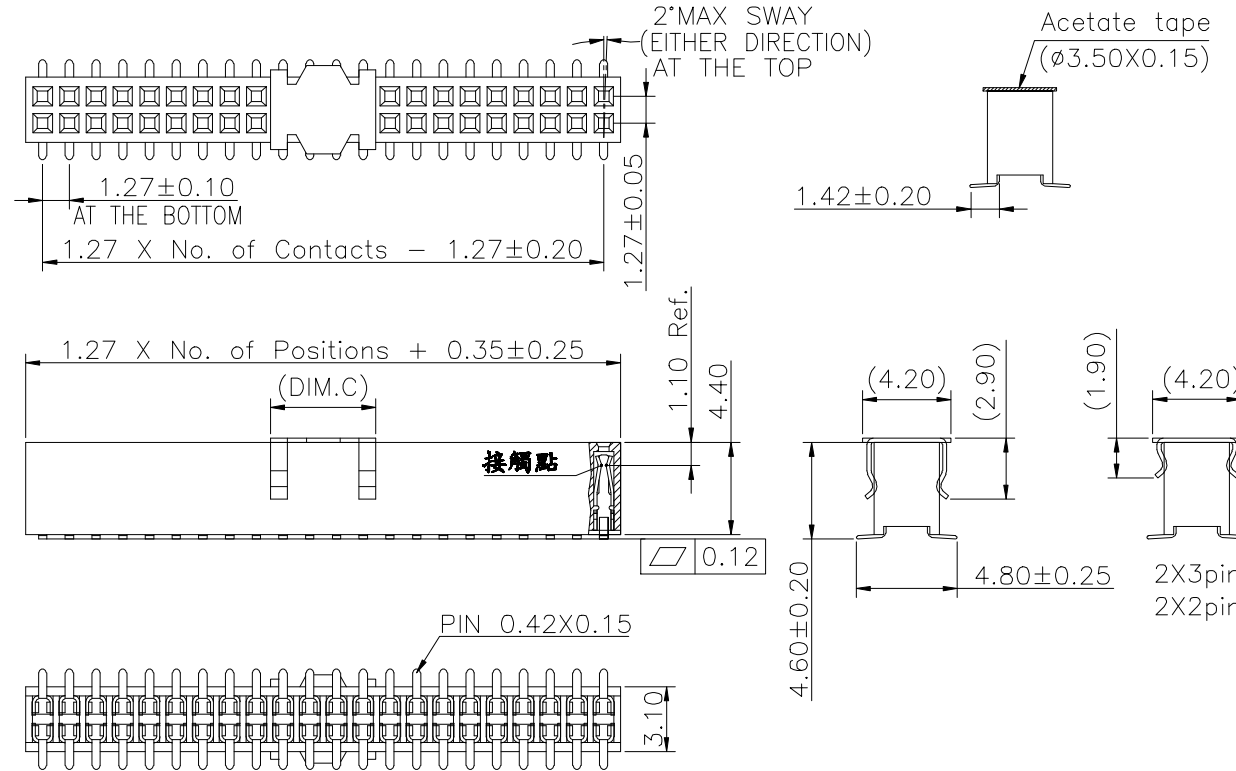
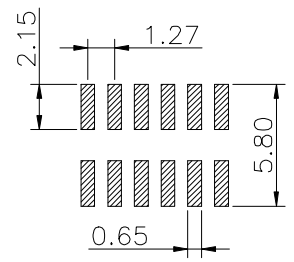


REV.	LOCAS.	DESCRIPTION	DATE	DRAWN
01	NEW	BATCH PRODUCTION INITIAL RELEASE.	2021.08.18	郭康
02	△	Modify the number of pins applicable to the cap(N-N210904)	2021.09.02	郭康



Specifications
 Current Rating:1.0Amp
 Insulation Resistance:1000MΩMin.
 Dielectric Withstanding Voltage:300VAC
 Contact Resistance:20mΩMax.
 Operating Temperature:-55°C to +125°C
 Materials
 Contact Material:Phosphor Bronze
 Insulator Material:High Temperature Thermoplastic, UL 94V-0 Nylon (**Halogen Free**) or the equivalent material
 All materials must meet Greenconn requirement for environment protection.

Recommended P.C.Board Layout
 (Tolerance:±0.05)



Ordering Information

C SEC202 - XX XX A 001 R 1 A X

No. of Pins per Row 02~50
 Standard Plating 02=Gold Flash
 Selective Plating 01=Tin 05=Gold Flash/Tin
 Color 1=Black
 Standard Packing C=Tube(used 04~50pin)
 Selective Packing E=Cap+Tube(used 04~50pin)
 F=Cap+Tape&Reel
 G=Acetate tape+Tube(used 04~50pin)
 H=Acetate tape+T&R

DIM.C	NOTE
4mm	03PIN △
5mm	04~50PIN



3RD.ANG PROJ.
 3RD.ANG PROJ.
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GENERAL TOLERANCE	
X'±5.0'	X.±0.30
.X'±3.0'	.X±0.25
.XX'±2.0'	.XX±0.15
.XXX'±1.0'	.XXX±0.10

DESIGNED: 郭康 DATE: 2021.09.02
 CHECKED: 張春進 DATE: 2021.09.02
 APPROVED: 邱國延 DATE: 2021.09.02

DWG. NO. CSEC202-CA1RA
 PART NO. CSEC202-XXXX A001R1AX

TITLE. 1.27 FEMALE HEADER DUAL ROWS SMT TYPE BASE 4.40H

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