

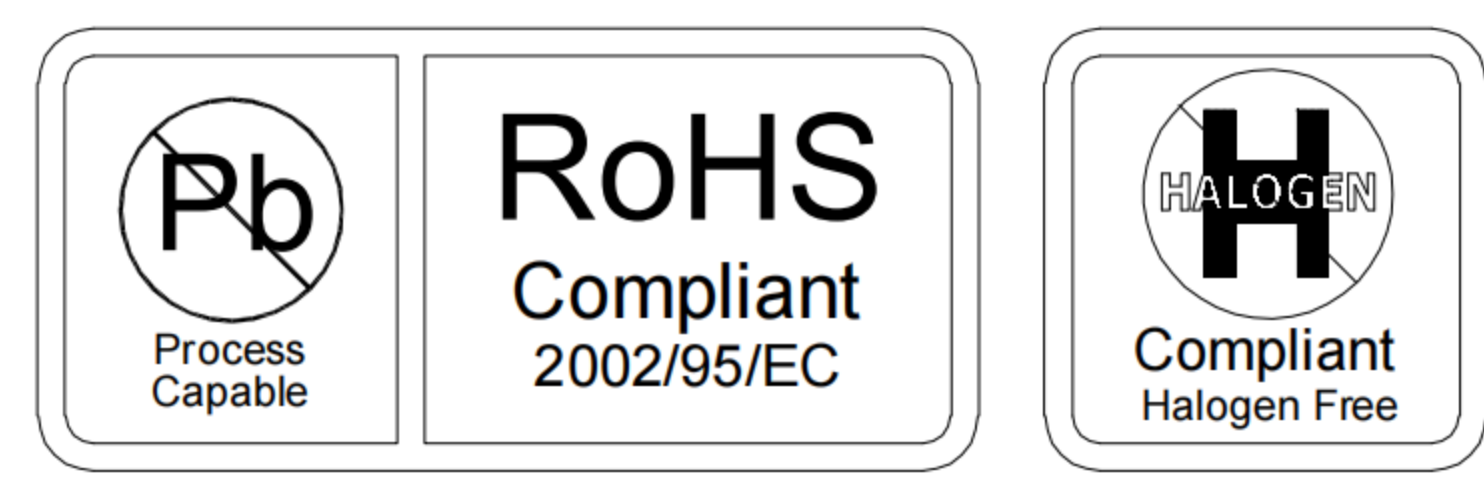
RECOMMENDED PCB LAYOUT
TOLERANCE: ±0.05

- NOTES:
- FINISH:
 - HOUSING: LCP BLACK, UL94V-0
 - CONTACT: C7025, GOLD (SEE P/N) PLATED IN CONTACT AREA, 80u" MIN MATTE TIN PLATED IN SOLDER AREA, 50u" MIN. NICKEL UNDERPLATED OVERALL.
 - ELECTRICAL:
 - CURRENT RATING: 12Amp;
 - DIELECTRIC WITHSTANDING VOLTAGE: 750V AC/DC.
 - CONTACT RESISTANCE: 20mΩ MAX.
 - INSULATION RESISTANCE: 1000MΩ MIN.
 - OPERATION TEMPERATURE: -40°C TO +105°C.
 - ALL MATERIALS MUST BE ROHS MATERIALS

TABLE:

No. OF PIN	A	B	C
006=2*3PIN	10.16	5.08	13.12
008=2*4PIN	12.70	7.62	15.66
010=2*5PIN	15.24	10.16	18.20
012=2*6PIN	17.78	12.70	20.74
014=2*7PIN	20.32	15.24	23.28
016=2*8PIN	22.86	17.78	25.82
018=2*9PIN	25.40	20.32	28.36
020=2*10PIN	27.94	22.86	30.90
022=2*11PIN	30.48	25.40	33.44
024=2*12PIN	33.02	27.94	35.98

SCALE: 1:1	UNIT: mm	Title: Card Edge 2.54mm Pitch 2x10Pin			
SIZE: A4		Molde Code: GT-CEDVA-20P-01			
Unless otherwise General tolerance:		DRAWN	CHECKED	APPROVED	DATE
X.	±0.50	Xia	Chen	Chen	2023.02.01
X.x	±0.30				
X.xx	±0.20				
Angles	±5°				



REV: A0 SHEET: 1/1

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