Released

GRF2373

Low-current LNA/Driver
0.1—3.8 GHz

Features

Reference: 3.3V/15mA/1950 MHz

- EVB NF: 1.3 dB
- Gain: 18.0 dB
- OP1dB: 13.0 dBm
- OIP3: 25.0

- Flexible Bias Voltage and Current
- Process: InGaP HBT

Applications

- Drones
- Small Cells and Cellular Repeaters
- Distributed Antenna Systems
- Set Top Boxes
- General Purpose Amplification
- VHF/UHF/900/2400 ISM

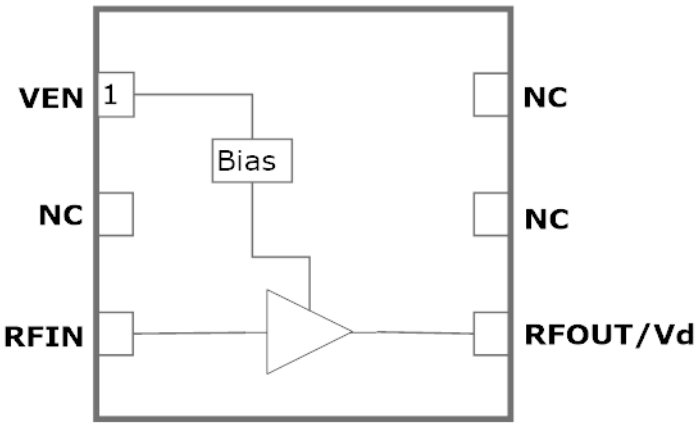
Product Description

GRF2373 can serve as a low current, high gain LNA or linear driver tunable over 100 to 3800 MHz. It exhibits outstanding gain and NF with a typical bias condition of 3.3 volts and 15 mA. Iccq.

The device is operated from a supply voltage (V_{CC}) of 1.8 to 5.0 V with a selectable I_{CCQ} range of 10 to 25 mA for optimal efficiency and linearity.

Consult with the GRF applications engineering team for custom tuning/evaluation board data and device s-parameters.

1.5 x 1.5 mm DFN-6



Absolute Ratings:

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V_{CC}	0	5.5	V
RF Input Power: (Load VSWR < 2:1; V_{CC}: 5.0 volts)	$P_{IN\ MAX}$		22	dBm
Operating Temperature (Package Heat Sink)	T_{AMB}	-40	85	°C
Maximum Junction Temperature (MTTF > 10^6 Hours)	T_{MAX}		150	°C
Maximum Dissipated Power	$P_{DISS\ MAX}$		100	mW
Electrostatic Discharge:				
Charged Device Model: (TBD)	CDM	1500		V
Human Body Model: (TBD)	HBM	250		V
Storage:				
Storage Temperature	T_{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		TBD	--

RoHS Compliant

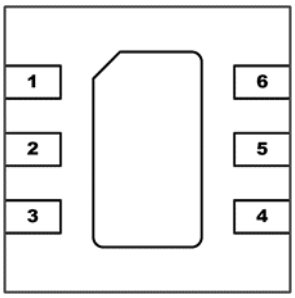
Caution! ESD Sensitive Device

Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

Note: For manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF2373 landing page: Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.

Link to manufacturing note

Pin Out (Top View)



Pin Assignments:

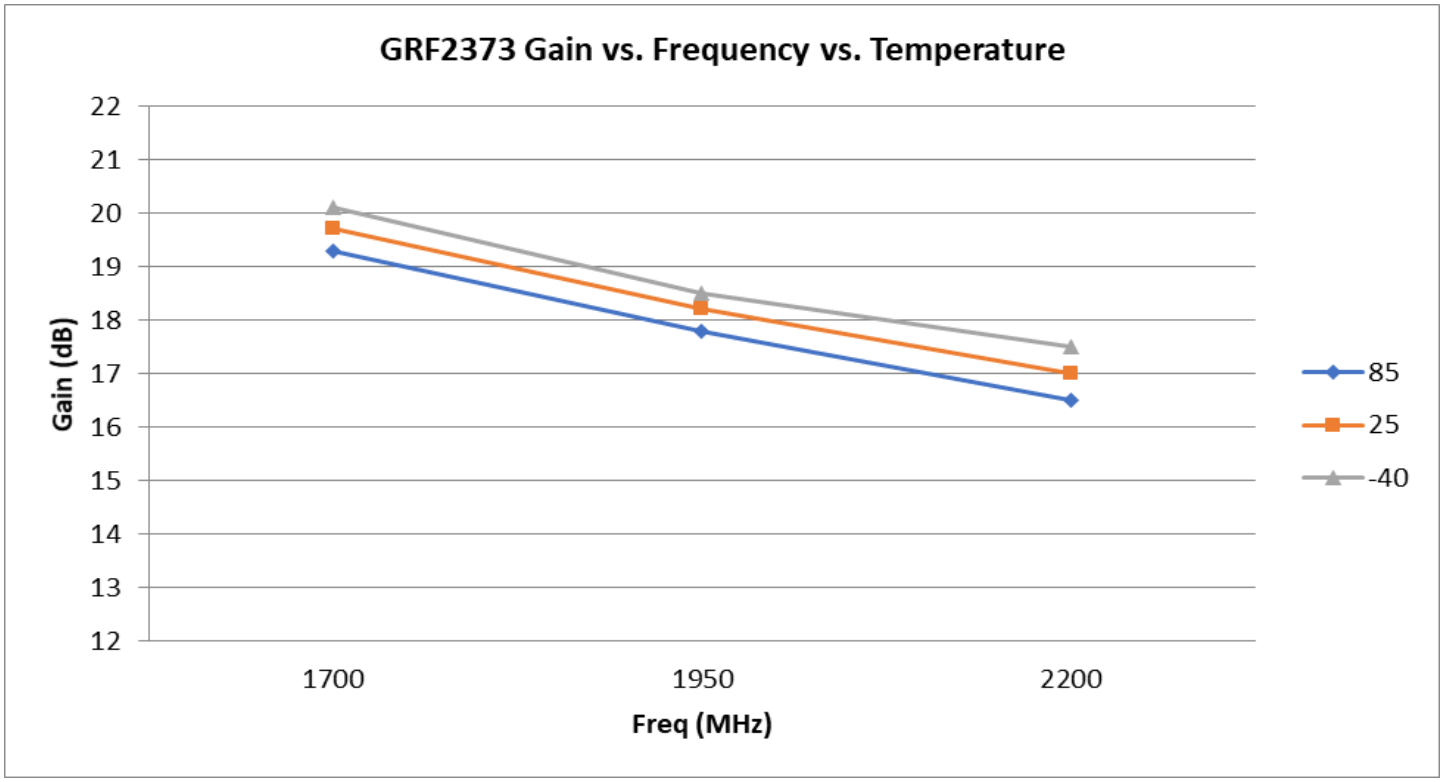
Pin	Name	Description	Note
1	V_{ENABLE}	Enable Voltage Input	V_{ENABLE} and series resistor set I_{DDQ}. V_{ENABLE} < =0.2 volts disables device. On -die pull-down resistor will turn the part off if this node is allowed to float.
2	NC	No Connect or Ground	No internal connection to die
3	RF_In	LNA RF input	An external DC blocking cap must be used
4	RF_Out	LNA RF output	V_{CC} must be applied through a choke to this pin
5	NC	No Connect or Ground	No internal connection to die
6	NC	No Connect or Ground	No internal connection to die
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.

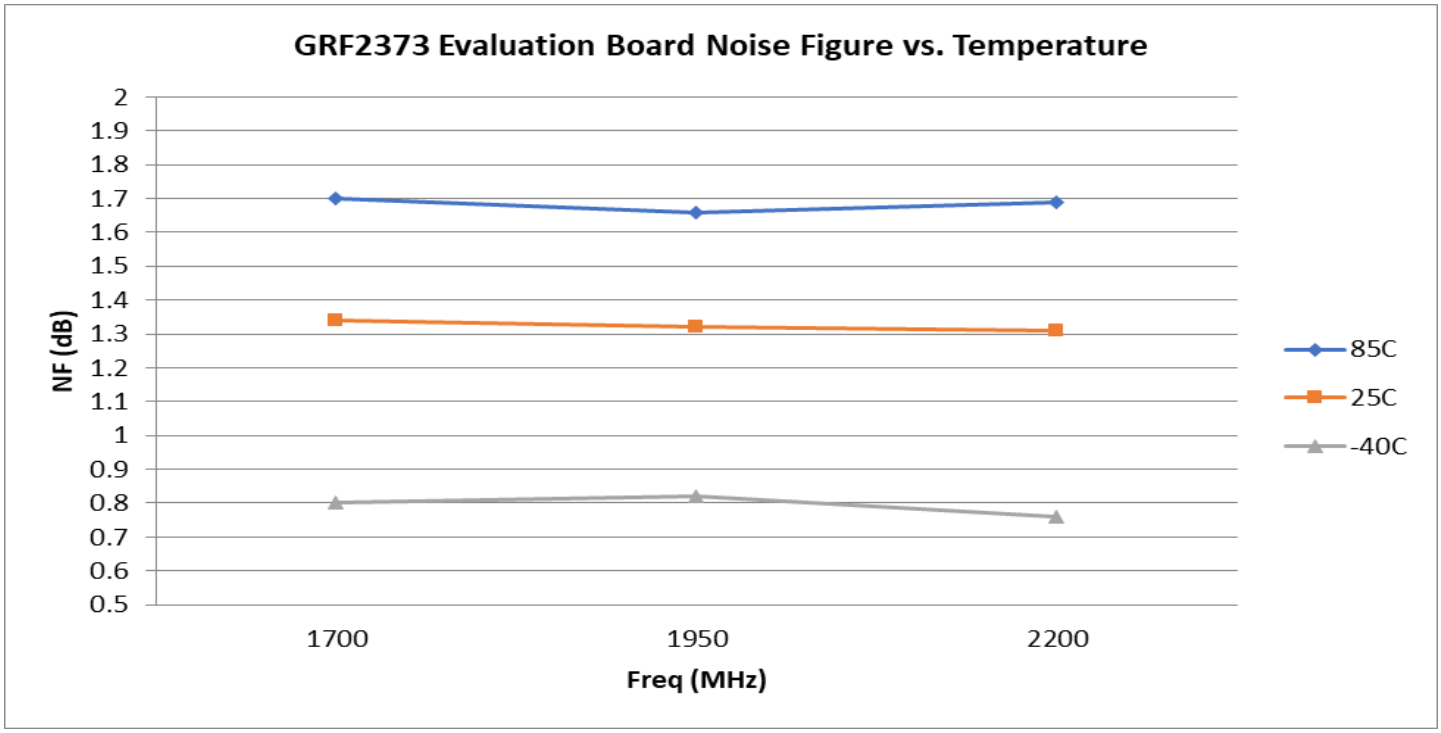
Nominal Operating Parameters:

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Test Frequency	F_{TEST}		1950		MHz	V_{CC} = 3.3 V, T_A = 25°C
Gain	S21	16.5	18.0		dB	
Evaluation Board Noise Figure	NF		1.3	1.6	dB	
Output 3rd Order Intercept	OIP3		25.0		dBm	-5.0 dBm P_{OUT} per tone at 2 MHz Spacing (1899 and 1901 MHz)
Output 1dB Compression Point	OP1dB	11.5	13.0		dBm	
Switching Rise Time	T_{RISE}		200		ns	
Switching Fall Time	T_{FALL}		100		ns	
Supply Current	I_{CC}		15		mA	Vdd=Venable=3.3V; Rbias: 1k ohms
Enable Current	I_{ENABLE}		3.0		mA	
Disabled Mode						
Leakage Current	$I_{LEAKAGE}$		< 1.0		uA	V_{CC}: 3.3V; V_{ENABLE}: 0.0V
Thermal Data						
Thermal Resistance: (Estimated)	Θ_{jc}		700		°C/W	On standard Evaluation Board
Junction Temperature @ +85 C Reference (Package heat sink)	$T_{JUNCTION}$		120 (See note)		°C	V_{CC}: 3.3 V; I_{CCQ}: 15 mA; No RF; P_{DISS}: 50 mW

Note: MTTF >10^6 hours for $T_{CHANNEL}$ < =170 degrees C.

GRF2373 Evaluation Board Data (3.3V/15mA)





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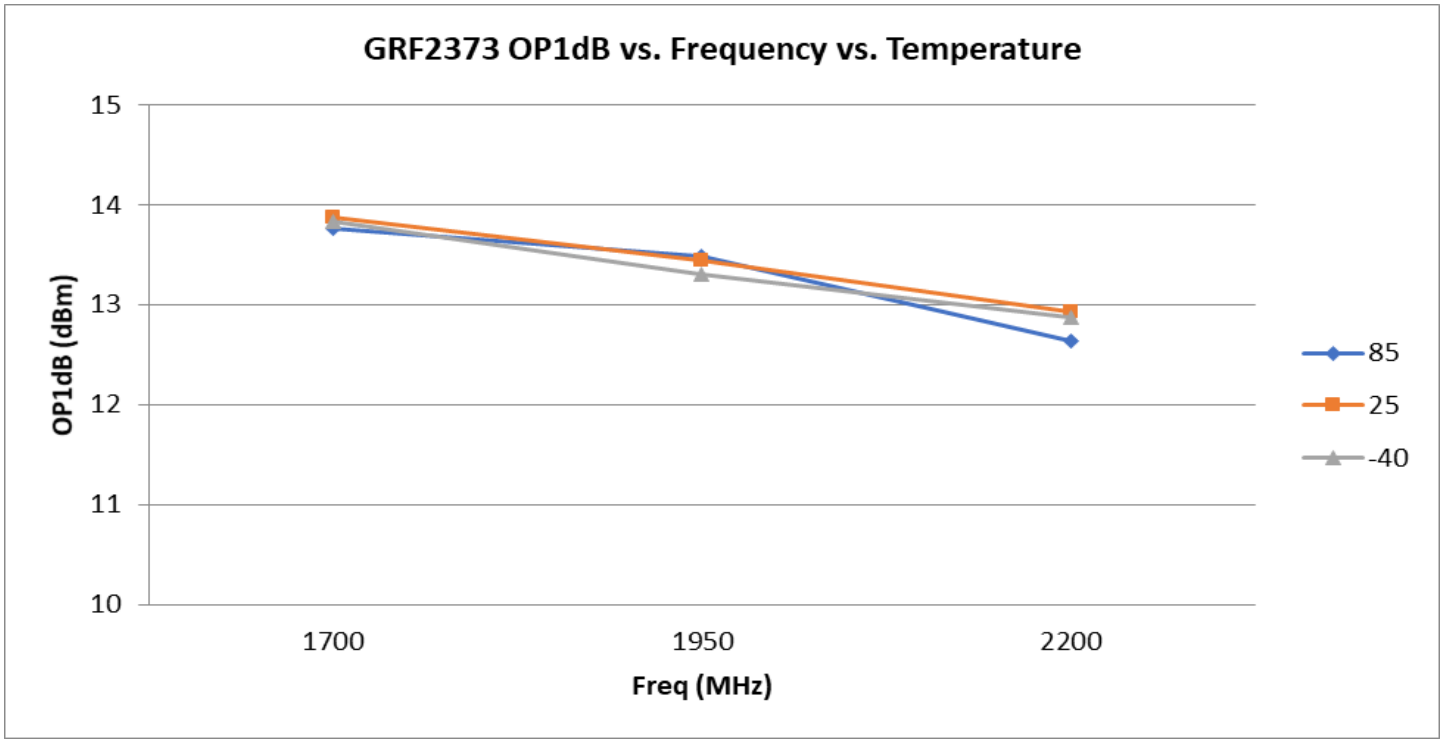
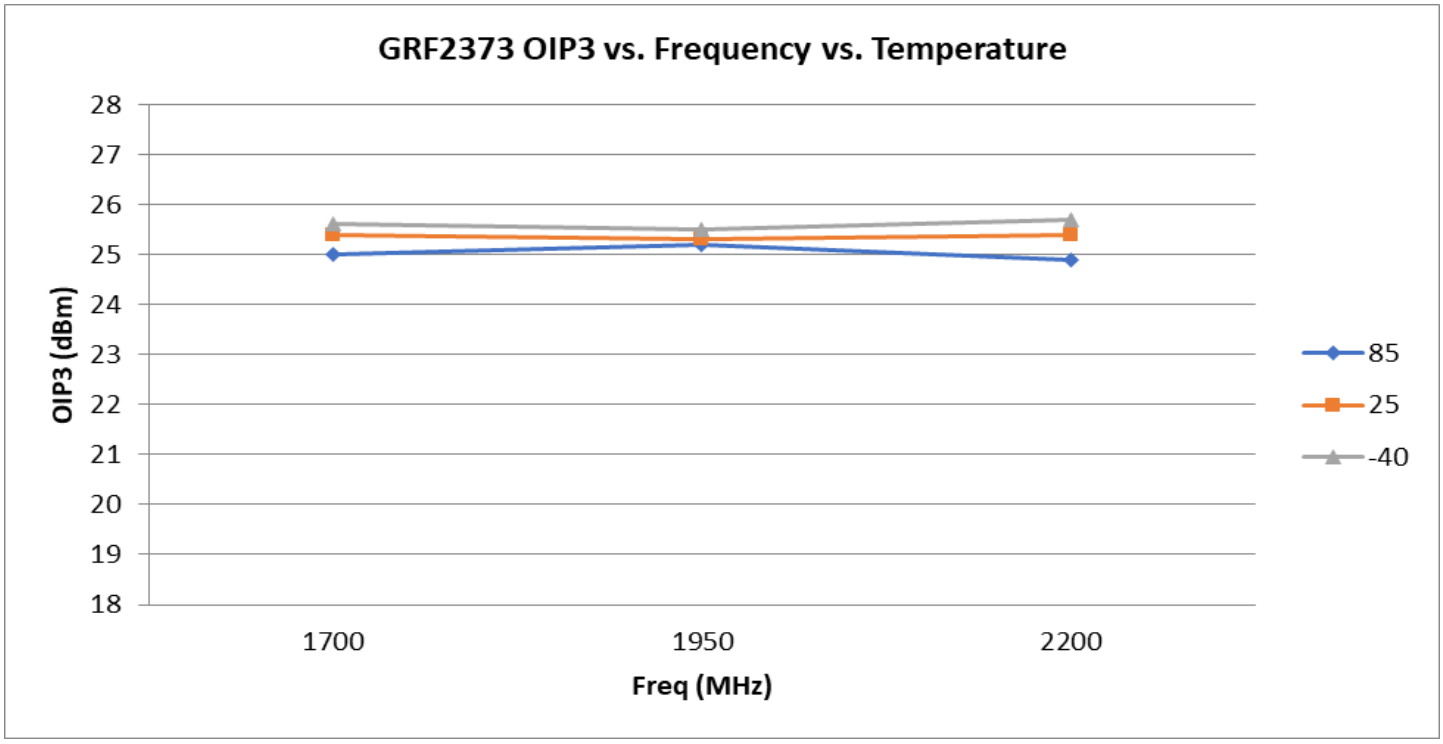
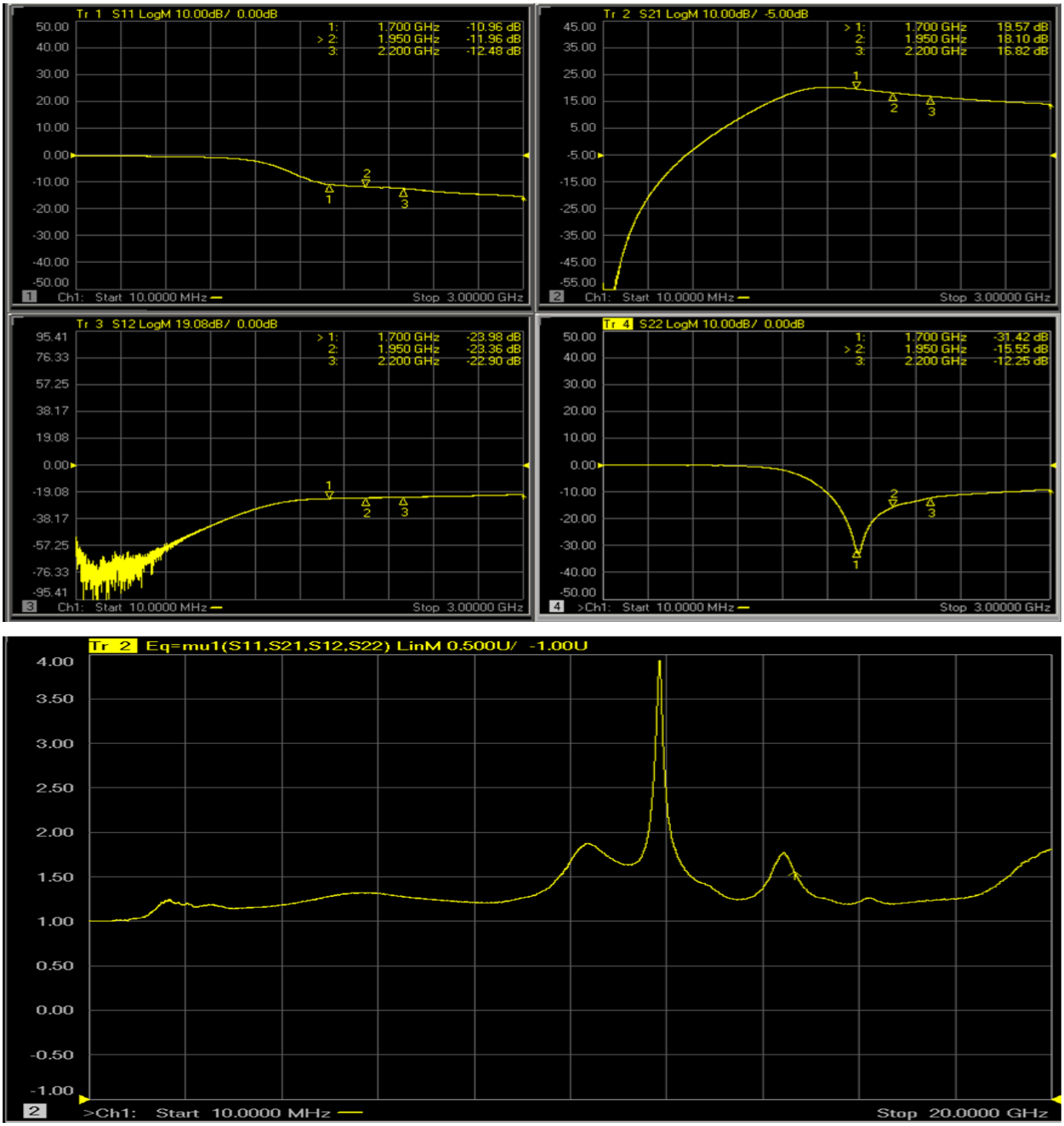
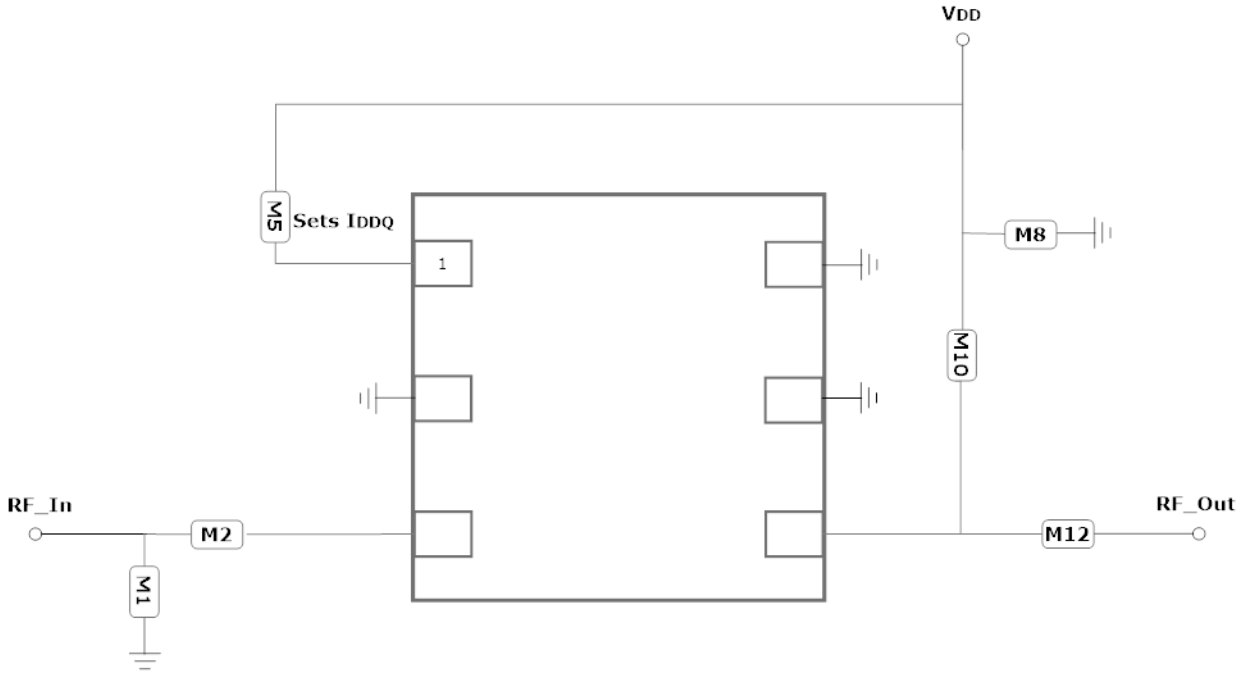




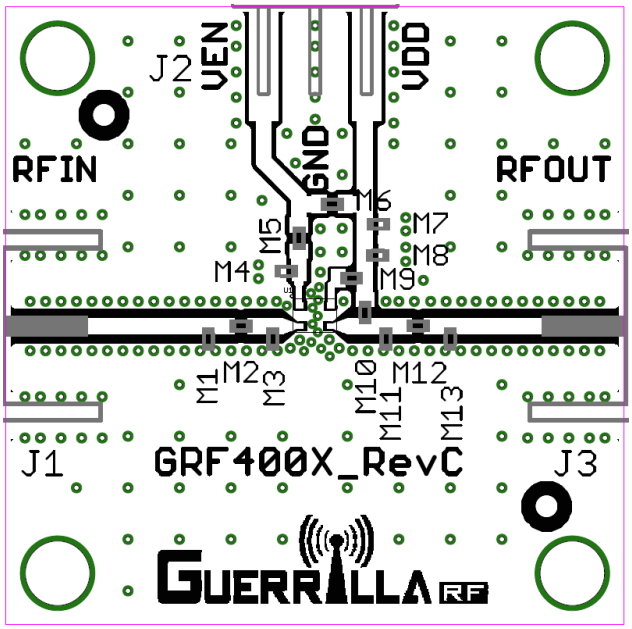
GRF2373 Evaluation Board S-Pars and Stability Mu Factor: (1.7 to 2.2 GHz Match)



Note: Mu factor >= 1.0 implies unconditional stability.



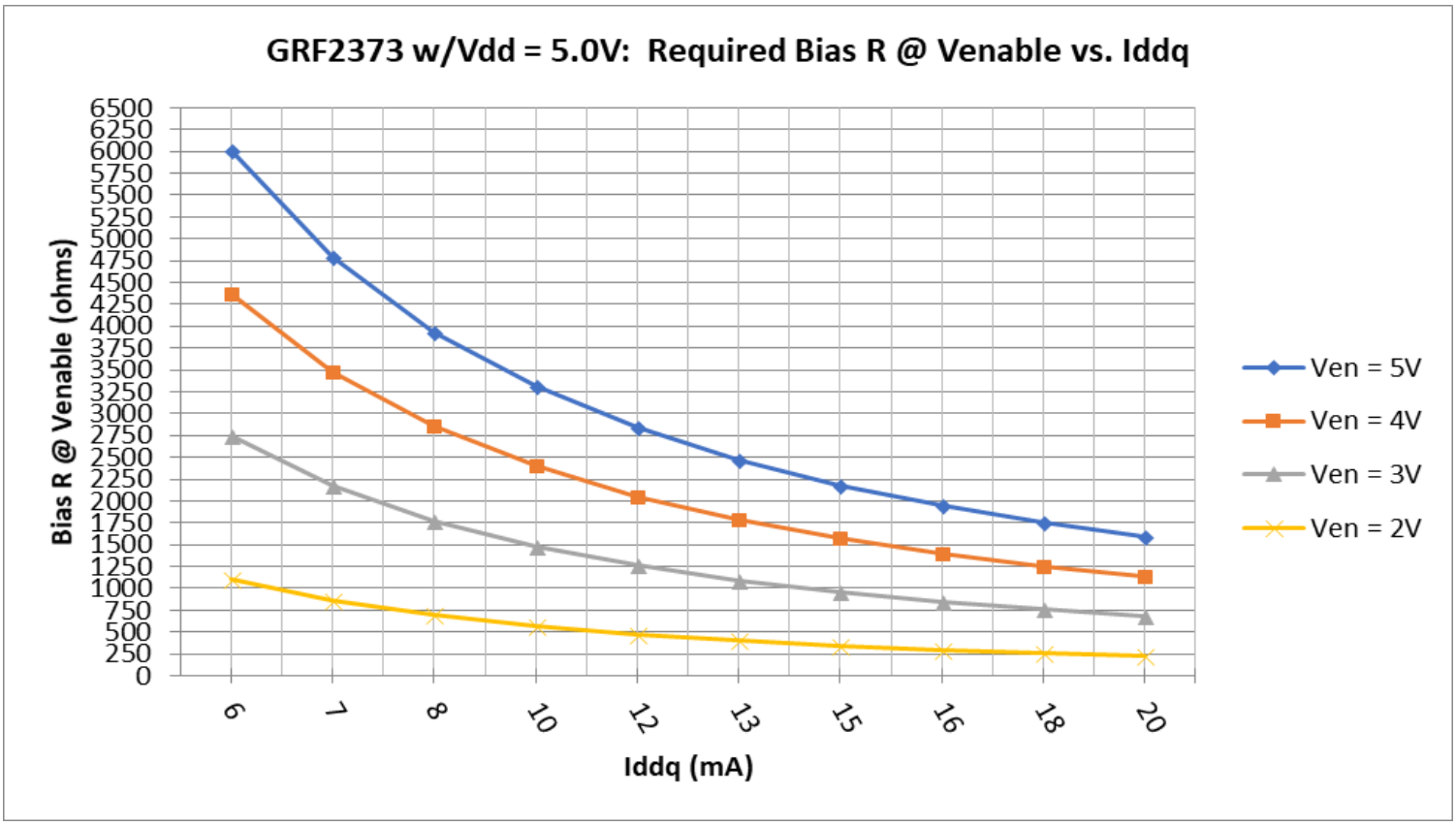
GRF2373 Application Schematic

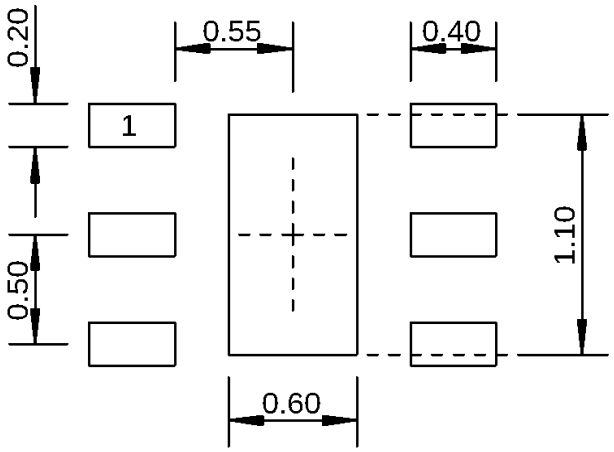


GRF2373 Evaluation Board Assembly Diagram

GRF2373 Standard Evaluation Board BOM: (1.7 to 2.2 GHz Tune; Vdd = Venable = 3.3V; Iddq: 15 mA)

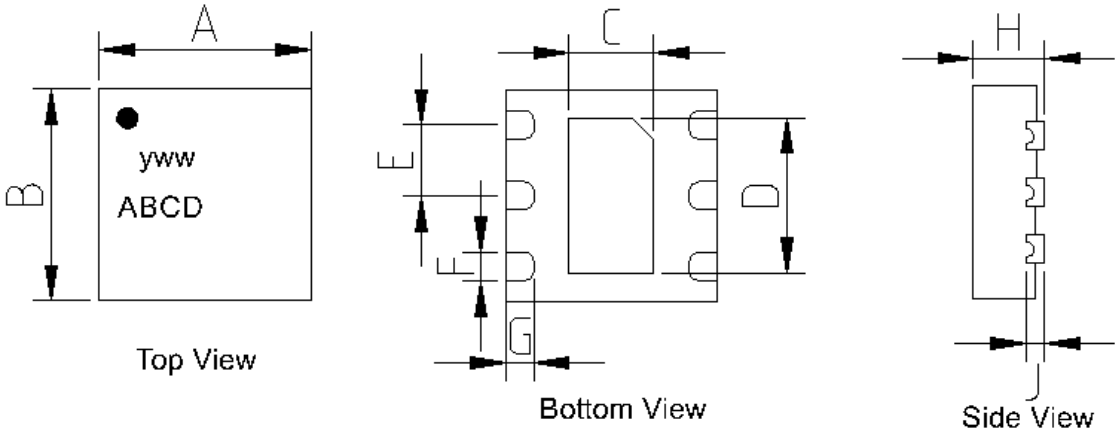
Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M1	Inductor	Murata	LQG	5.1 nH	0402	Ok
M2	Capacitor	Murata	GJM	3.0 pF	0402	Ok
M5 (Sets Iddq)	Resistor	Various	5%	—	0402	Ok
M8	Capacitor	Murata	GRM	0.1 uF	0402	Ok
M10	Inductor	Murata	LQG	3.0 nH	0402	Ok
M12	Capacitor	Murata	GJM/GRM	1.5 pF	0402	Ok
Evaluation Board	GRF400X_RevC	—	—	—	—	—





Dimensions in millimeters

1.5 mm DFN-6 Suggested PCB Footprint (Top View)



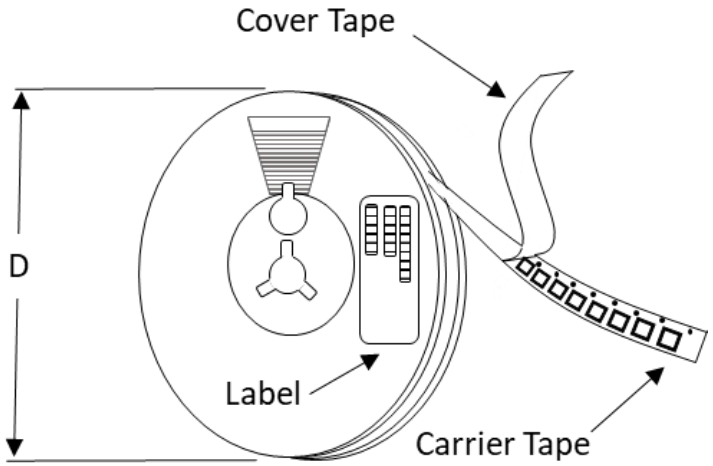
Dimensions (MM)	
A	1.5 +/- 0.050
B	1.5 +/- 0.050
C	.6 +/- 0.050
D	1.1 +/- 0.050
E	.5 Bsc
F	.2 +/- 0.050
G	.2 +/- 0.050
H	.45 +/- 0.050
J	.12 Ref.

1.5 mm DFN-6 Package Dimensions

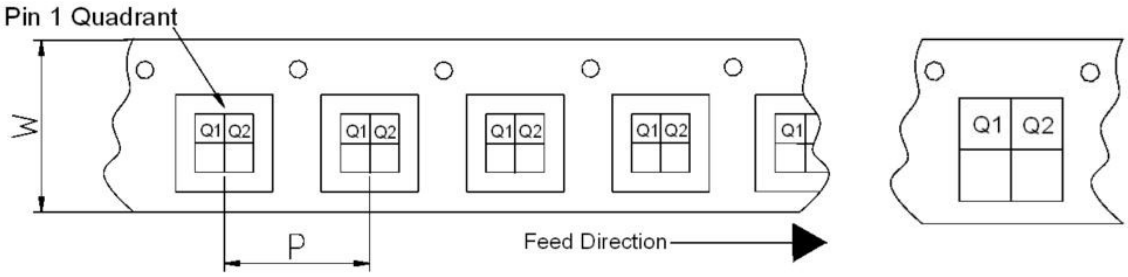
Tape and Reel Information:

Guerrilla RF's Tape and Reel specification complies with the Electronics Industries Association (EIA) standards for 'Embossed Carrier Tape of Surface Mount Components for Automatic Handling". Reference EIA-481. See the table on the following page for Tape and Reel specifications along with units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape, wound into a plastic reel. Each reel will be packaged in a cardboard box. There will be product labels on the reel, the protective ESD bag and the outside surface of the box.



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information

Tape and Reel Specification and Device Package Information Table

Package				Carrier Tape			Reel	
Type	Dimensions (mm)	Leads	Weight (mg)	Width (W) (mm)	Pocket Pitch (P) (mm)	Pin 1 Quad-rant	Diameter (D) (inches)	Units per Reel
QFN	2.0 x 2.0 x 0.50	12	7	8	4	Q1	7	2500
QFN	3.0 x 3.0 x 0.85	16	24	12	8	Q1	7	1500
DFN	1.5 x 1.5 x 0.45	6	4	8	4	Q1	7	2500
DFN	2.0 x 2.0 x 0.75	8	12	8	4	Q1	7	2500
LFM	3.5 x 3.5 x 0.75	See note	TBD	12	8	Q2	7	1500
LFM	4.0 x 4.0 x 0.75	See note	TBD	12	8	Q2	7	1500

Note: Lead count may vary. Reference applicable product data sheet

Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

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