

GRF2501

High Gain, Ultra-LNA 802.11ac: 4.9–9.0 GHz



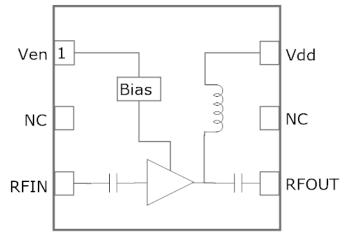
Features

Reference: 3.3V/18mA/5.5 GHz

- Evaluation Board NF: 1.0 dB
- Gain: 17.0 dB
- 0P1dB: 9.0 dBm
- Flexible Bias Voltage and Current
- Internally Matched to 50 Ω
- Process: GaAs pHEMT

Applications

- WiFi Access Points
- Mobile WiFi Devices
- 802.11p Vehicle Communications
- Microwave Backhaul



1.5 x 1.5 mm DFN-6

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Product Description

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GRF2501 is an ultra-low noise amplifier (LNA) designed for IEEE 802.11a/n/ac/p applications in the 5 GHz band and up to 9 GHz with appropriate matching.

The LNA is operated from a single positive supply of 2.7 to 5.0 V with a typical bias condition of 3.3 V and 18 mA and is internally matched to 50 Ω at the input and output ports.

Consult with the GRF applications engineering team for custom tuning/evaluation board data and device s-parameters.



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Absolute Ratings:

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	Vdd	0	6.0	V
RF Input Power: (Load VSWR < 2:1; V_D : 5.0 volts)	P _{IN MAX}		15	dBm
Operating Temperature (Package Heat Sink)	Т _{АМВ}	-40	105	°C
Maximum Channel Temperature (MTTF > 10^6 Hours)	Тмах		170	°C
Maximum Dissipated Power	PDISS MAX		200	mW
Electrostatic Discharge:				
Charged Device Model:	CDM	1500		V
Human Body Model:	HBM	250		V
Storage:				
Storage Temperature	Tstg	-65	150	°C
Moisture Sensitivity Level	MSL		1	

Caution! ESD Sensitive Device



Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

Note: For manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF2501 landing page: Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.

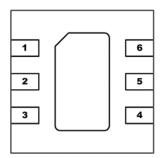
Link to manufacturing note



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Pin Out (Top View)



Pin Assignments:

Pin	Name	Description	Note	
1	VENABLE	LNA Enable Input	Venable and series resistor set Iddq. Venable < =0.2 volts disables device. On- die pull-down resistor will turn the part off if this node is allowed to float.	
2	NC	No Connect or Ground	No internal connection to die	
3	RF_In	LNA RF input	Internally matched to 50 Ω . These ports may be DC connected to ground	
4	RF_Out	LNA RF output	Internally matched to 50 Ω . These ports may be DC connected to ground externally but no DC > 0.2 volts should be applied to these ports.	
5	NC	No Connect or Ground	No internal connection to die	
6	Vdd	Supply Voltage for the LNA	Requires bypass capacitance as close as possible to pin on PCB	
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recom- mend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.	

VENABLE Truth Table:

V _{DD}	VENABLE	Mode
High	>=1.8 V	LNA On
High	<0.1 V	LNA Off

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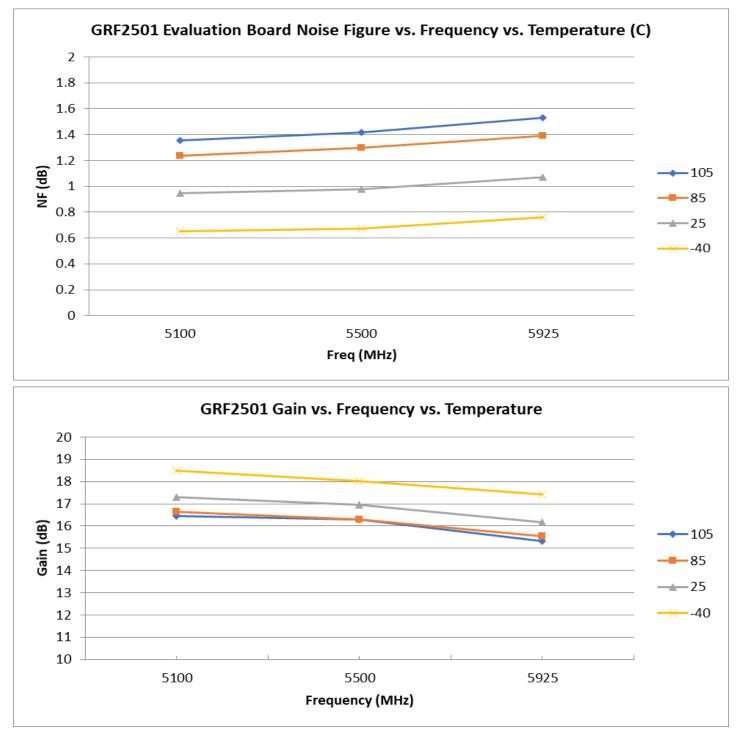
Nominal Operating Parameters:

Deverseter	Symphol	Specification			Unit	Condition	
Parameter	Symbol	Min.	Min. Typ.		Unit	Condition	
High Gain Mode						V_{DD} = 3.3 V, V_{ENABLE} = 3.3 V, T_A = 25 °C	
Test Frequency	FTEST		5500		MHz		
Gain	S21	15.0	17.0	19.0	dB		
Evaluation Board Noise Figure	NF		1.0	1.2	dB		
Input Power at 1% EVM	EVM		-19.0		dBm	802.11ac modulation	
Output 1dB Compression	OP1dB	6.0	9.0		dBm		
Supply Current (Quiescent)	I _{DD}	12.0	18.0	28.0	mA	Rbias: 1.0k Ohm	
Enable Current	IENABLE		1.5	3.0	mA		
Disabled Mode						V_{DD} = 3.3 V, V_{ENABLE} = 0.0V	
Supply Current (Leakage)	I _{DD}		200	500	μA		
Thermal Data							
Thermal Resistance (Infra-Red Scan)	Θјс		150		°C/W		
Channel Temperature @ +85 C refer- ence (Package heat sink)	T _{CHANNEL}		99 (See note)		٥C	Vdd: 3.3V; Iddq: 28 mA; No RF Pdiss: 92 mW	

Note: MTTF >10^6 hours for TCHANNEL < =170 degrees C.



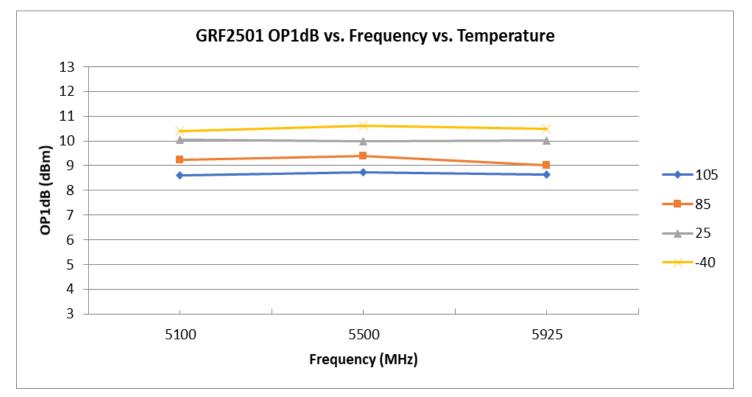
GRF2501 Evaluation Board Measured Data: 3.3V/18mA



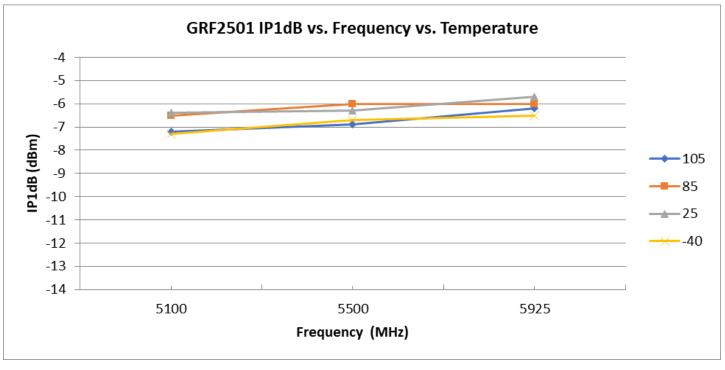
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GRF2501 Evaluation Board Measured Data: 3.3V/18mA

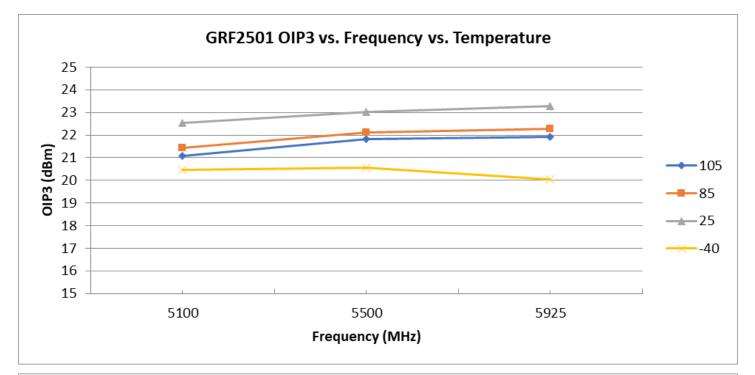


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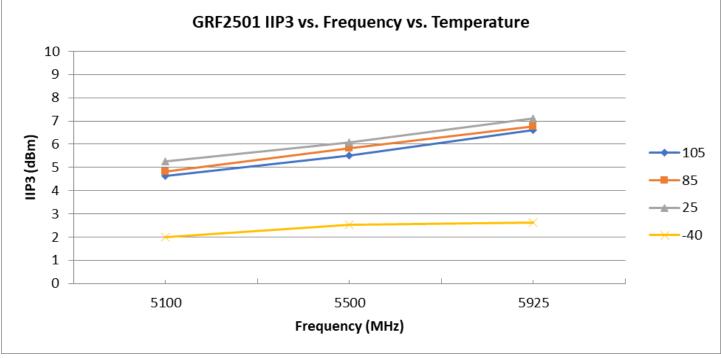




GRF2501 Evaluation Board Measured Data: 3.3V/18mA



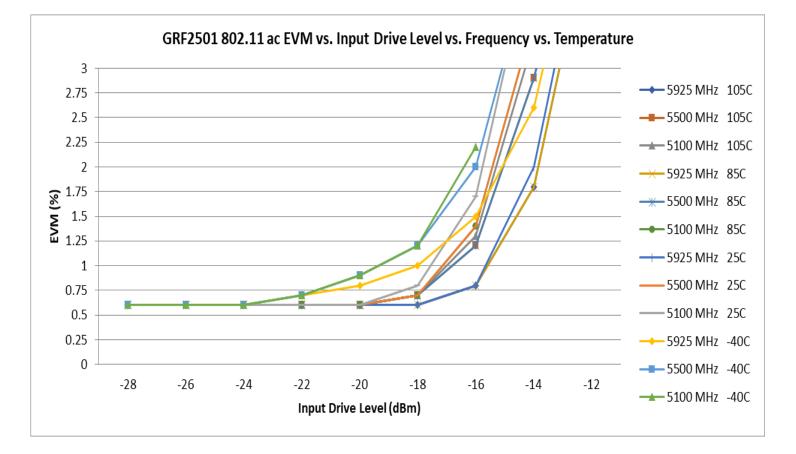
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GRF2501 Evaluation Board Measured Data: 3.3V/18mA



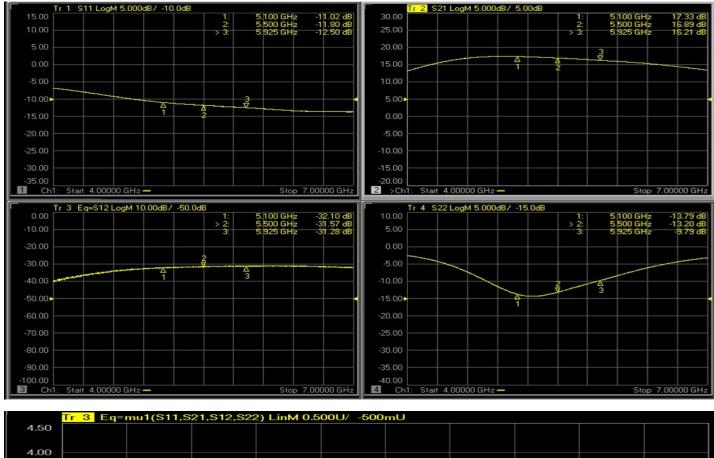
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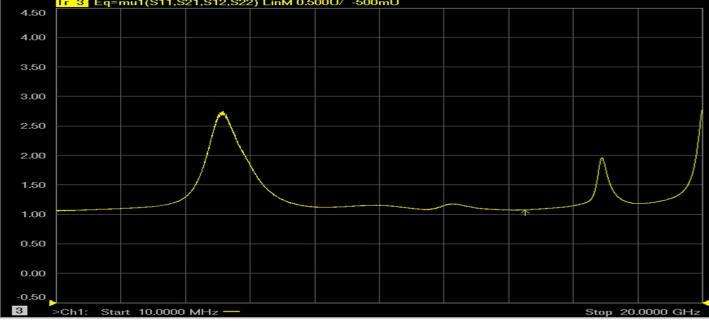


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GRF2501 Evaluation Board S-Pars:



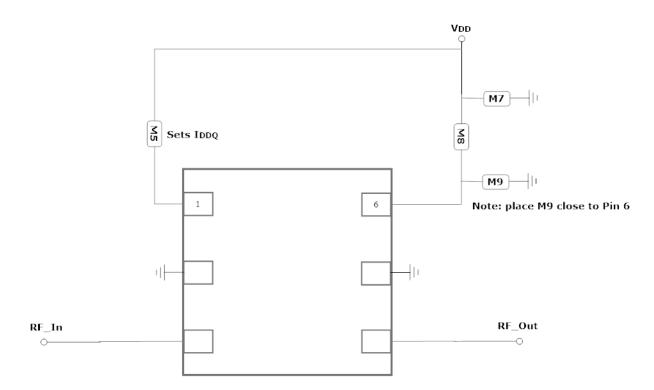


Note: Mu >= 1.0 implies unconditional stability

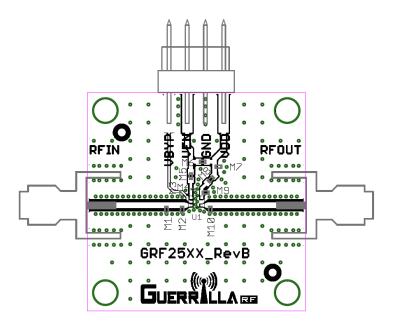


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GRF2501 Evaluation Board Assembly Diagram



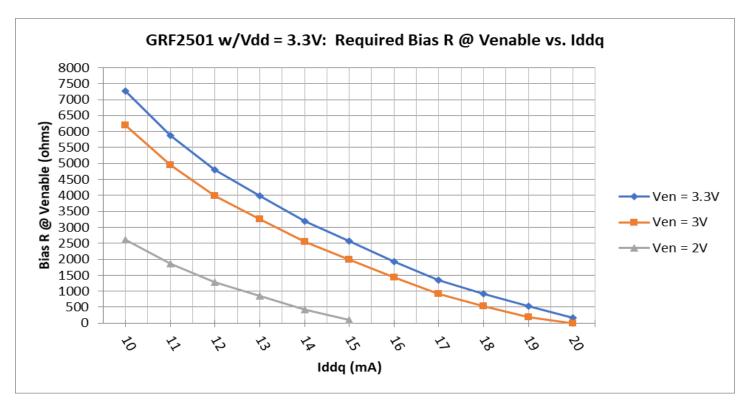
GRF2501 Standard Evaluation Board BOM: (5.1 to 5.9 GHz Tune)

Component	Туре	Manufacturer	Family	Value	Package Size	Substitution
M5	Resistor: 5%	Various	—	1k Ohm	0201	ok
M7	Capacitor	Murata	GRM	0.1 uF	0201	ok
M8	Ferrite Bead	Murata	BLM15AG121SN1D	120 Ohm	0201	ok
M9 (See note)	Capacitor	Murata	GJM	1.0 pF	0201	ok
Evaluation Board:	GRF25XX_RevB					

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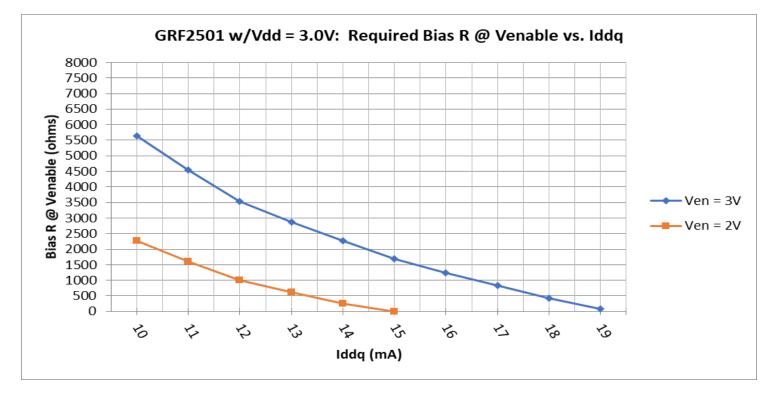
Note: Place M9 close to pin 6. The position of this component affects the device matching.

GRF2501 Bias R Selection Table: Vdd: 3.3V



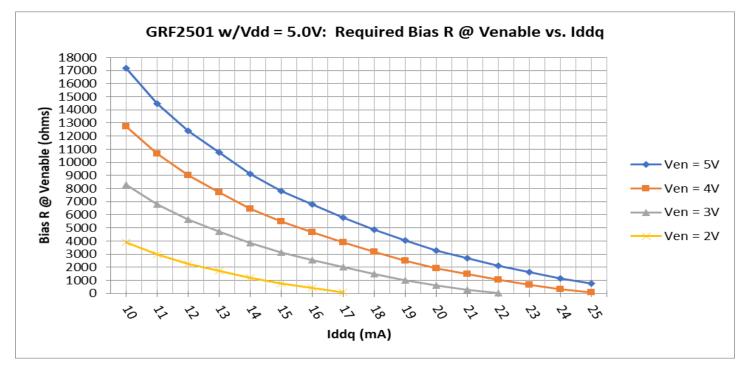


GRF2501 Bias R Selection Table: Vdd: 3.0 volts



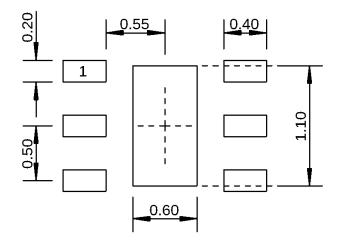
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GRF2501 Bias R Selection Table: Vdd: 5.0 volts



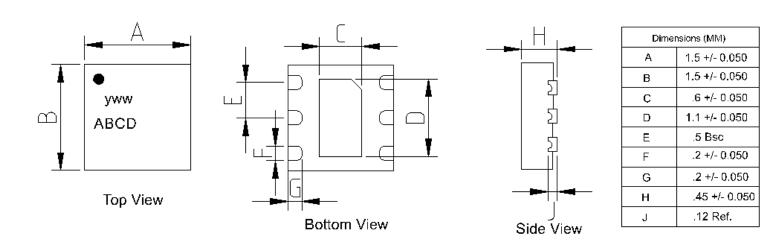


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Dimensions in millimeters





1.5 mm DFN-6 Package Dimensions



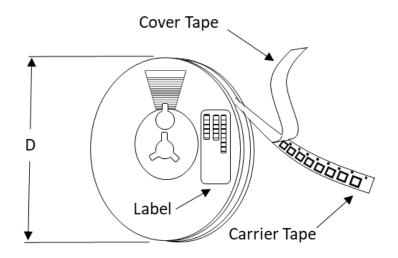
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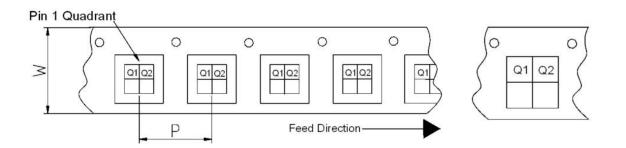
Tape and Reel Information:

Guerrilla RF's Tape and Reel specification complies with the Electronics Industries Association (EIA) standards for 'Embossed Carrier Tape of Surface Mount Components for Automatic Handling". Reference EIA-481. See the table on the following page for Tape and Reel specifications along with units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape, wound into a plastic reel. Each reel will be packaged in a cardboard box. There will be product labels on the reel, the protective ESD bag and the outside surface of the box.



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information



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Tape and Reel Specification and Device Package Information Table

Package				Carrier Tape	Reel			
Туре	Dimensions (mm)	Leads	Weight (mg)	Width (W) (mm)	Pocket Pitch (P) (mm)	Pin 1 Quad- rant	Diameter (D) (inches)	Units per Reel
QFN	2.0 x 2.0 x 0.50	12	7	8	4	Q1	7	2500
QFN	3.0 x 3.0 x 0.85	16	24	12	8	Q1	7	1500
DFN	1.5 x 1.5 x 0.45	6	4	8	4	Q1	7	2500
DFN	2.0 x 2.0 x 0.75	8	12	8	4	Q1	7	2500
LFM	3.5 x 3.5 x 0.75	See	TBD	12	8	Q2	7	1500
LFM	4.0 x 4.0 x 0.75	See note	TBD	12	8	Q2	7	1500

Note: Lead count may vary. Reference applicable product data sheet



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Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

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