GRF4002W







Features

Reference: 5V/70mA/2.5 GHz

EVB NF: 0.85 dBGain: 15.0 dB

OP1dB: 23.5 dBmOIP3: 36.5 dBm

Tested to AEC-Q100 Grade 2

Flexible Bias Voltage and Current

• Internally Matched to 50Ω

Process: GaAs pHEMT

Applications

Revision Date: 07/10/20

SDARS LNA

Cellular Repeaters (Compensator)

GPS

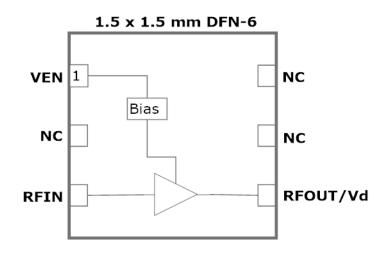
Product Description

GRF4002W is a broadband low noise gain block designed for small cell, wireless infrastructure and other high performance applications. It exhibits outstanding broadband NF, linearity and return losses over 700 to 3800 MHz with a single match.

The device is operated from a supply voltage (V_{DD}) of 1.8 to 5.0 V with a selectable I_{DDQ} range of 20 to 80 mA for optimal efficiency and linearity.

GRF4002W is internally matched to 50Ω at the input and output ports, needing only external DC blocks and a bias choke on the output.

Consult with the GRF applications engineering team for custom tuning/evaluation board data. Packaged device s-parameters are available on the website landing page.





Absolute Ratings:

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V _{DD}	0	6.0	V
RF Input Power: (Load VSWR < 2:1; V _D : 5.0 volts)	P _{IN MAX}		22	dBm
Operating Temperature (Package Heat Sink)	T _{AMB}	-40	105	°C
Maximum Channel Temperature (MTTF > 10^6 Hours)	Тмах		170	°C
Maximum Dissipated Power	P _{DISS MAX}		500	mW
Electrostatic Discharge:				
Charged Device Model:	CDM	1500		V
Human Body Model:	НВМ	250		V
Storage:				
Storage Temperature	T _{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	



Caution! ESD Sensitive Device

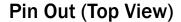


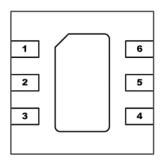
Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

Note: For manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF4002W landing page: Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.

Link to manufacturing note







Pin Assignments:

Pin	Name	Description	Note
1	VENABLE	Enable Voltage Input	Venable and series resistor set IDDQ. Venable < =0.2 volts disables device. On -die pull-down resistor will turn the part off if this node is allowed to float.
2	NC	No Connect or Ground	No internal connection to die
3	RF_In	LNA RF input	Internally matched 50 Ω . An external DC blocking cap must be used.
4	RF_Out	LNA RF output	Internally matched 50Ω . V_{DD} must be applied through a choke to this pin
5	NC	No Connect or Ground	No internal connection to die
6	NC	No Connect or Ground	No internal connection to die
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.

Nominal Operating Parameters:

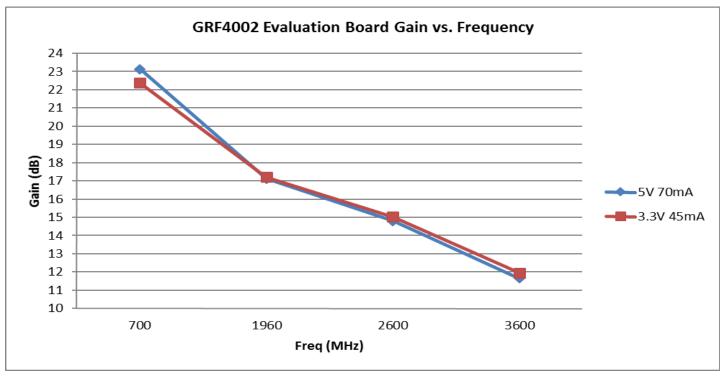
Davamatav	Cymahal	:	Specification Typ. Max.		Hoit	Condition	
Parameter	Symbol	Min.			Unit		
Test Frequency	F _{TEST}		2500		MHz	$V_{DD} = 5.0 \text{ V}, T_A = 25 ^{\circ}\text{C}$	
Gain	S21	14.0	15.0		dB		
Evaluation Board Noise Figure	NF		0.85	1.0	dB		
Output 3rd Order Intercept	OIP3		36.5		dBm	2.0 dBm P _{OUT} per tone at 2 MHz Spacing (2499 and 2501 MHz)	
Output 1dB Compression Point	OP1dB	22.0	23.5		dBm		
Switching Rise Time	T _{RISE}		500		ns		
Switching Fall Time	T _{FALL}		500		ns		
Supply Current	I _{DD}		70.0		mA	VDD=VENABLE= 5.0V; M5: 1500 ohms	
Enable Current	IENABLE		3.0	6.0	mA		
Disabled Mode							
Leakage Current	Ileakage		40	100	uA	VDD: 5.0V; VENABLE: 0.0V	
Thermal Data							
Thermal Resistance: (Infra-Red Scan)	Θјс		131		°C/W	On standard Evaluation Board	
Channel Temperature @ +85 C Reference (Package heat sink)	TCHANNEL		131 (See note)		°C	Vdd: 5.0 V; Iddq: 70 mA; No RF; Pdiss: 350 mW	

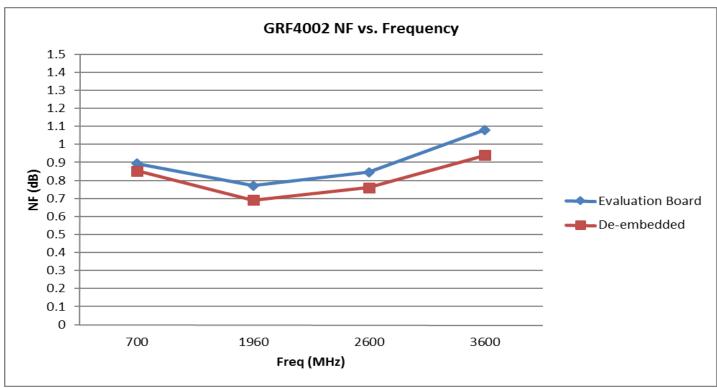
Note: MTTF >10^6 hours for TCHANNEL < =170 degrees C.





GRF4002W Evaluation Board Measured Data:





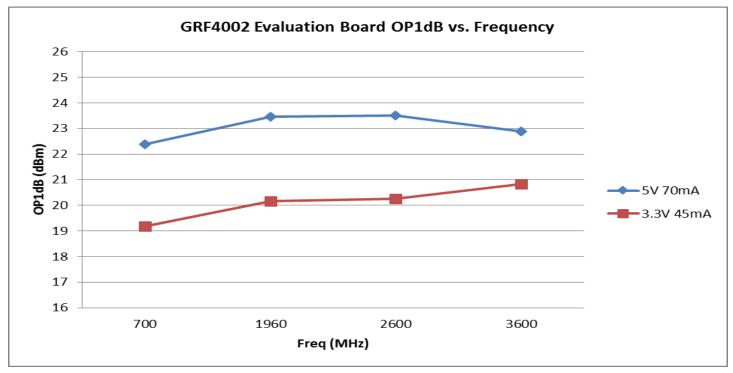
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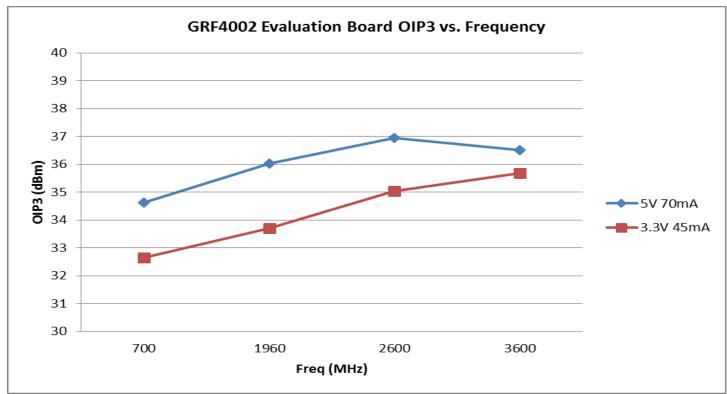






GRF4002W Evaluation Board Measured Data:



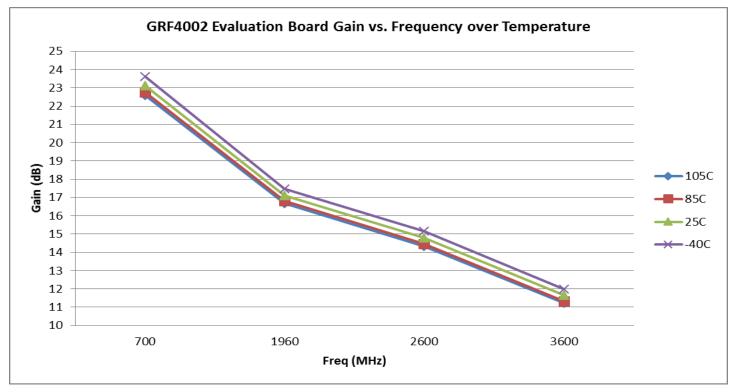


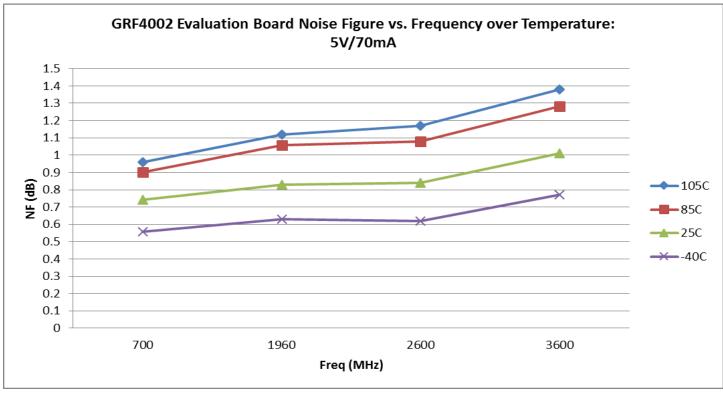
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LNA-Driver: SDARS/Compensator/GPS

GRF4002W Evaluation Board Performance over Temperature: (5V/70mA)



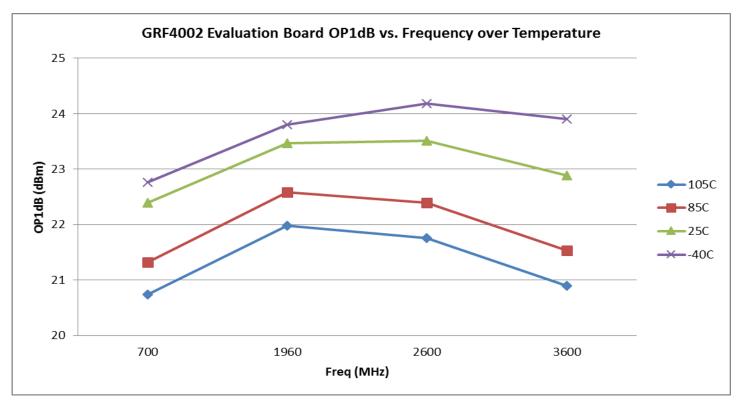


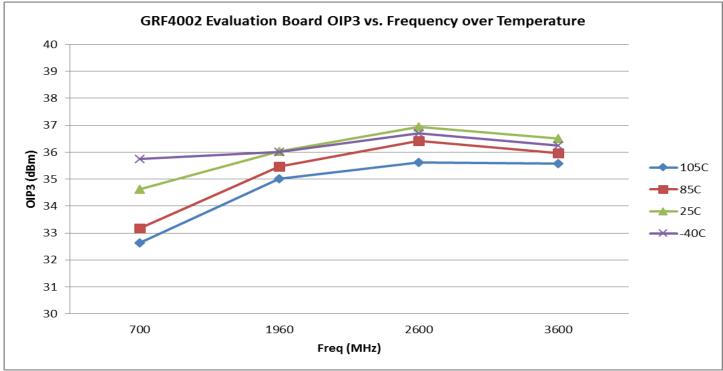
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GRF4002W Evaluation Board Performance over Temperature: (5V/70mA)



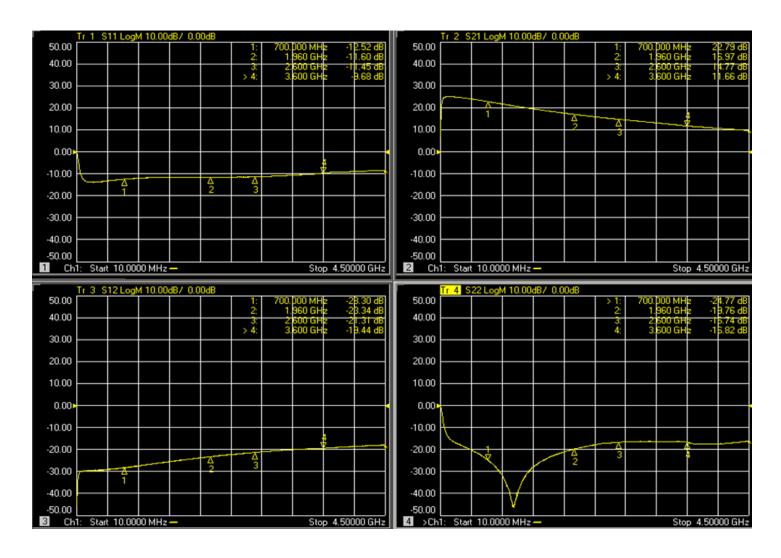


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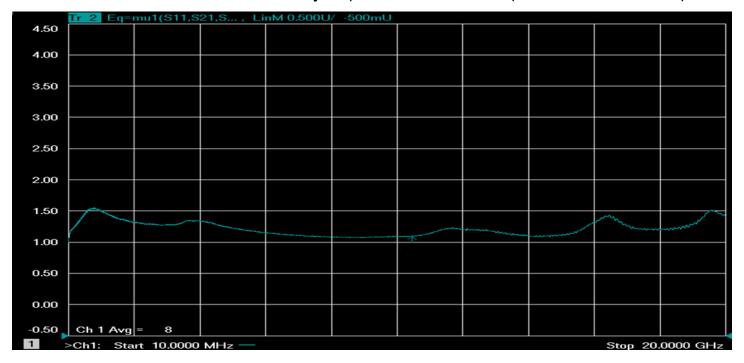
LNA-Driver: SDARS/Compensator/GPS

GRF4002W Evaluation Board S-Pars: (0.7 – 3.8 GHz Match)

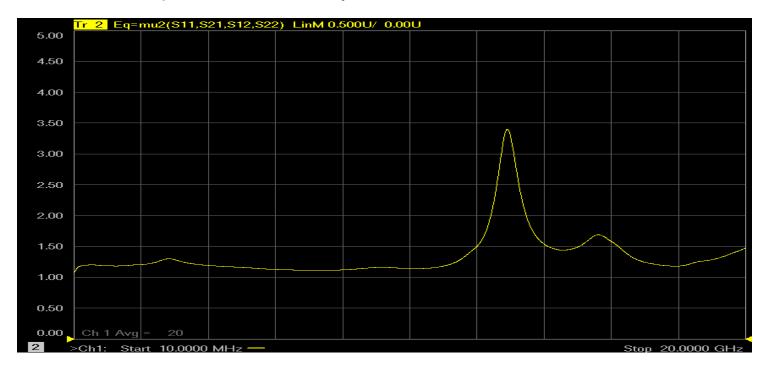




GRF4002W Evaluation Board Stability Mu/Mu Prime Factors: (0.7 — 3.8 GHz Match)



Note: Mu >= 1.0 implies unconditional stability.



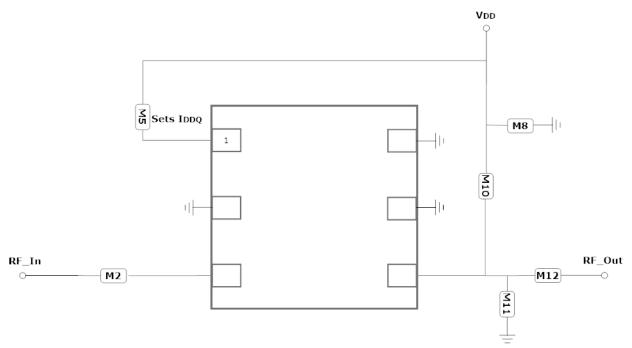
Note: Mu Prime >= 1.0 implies unconditional stability.

GRF4002W

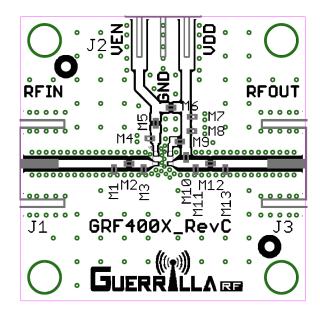


Revision Date: 07/10/20

LNA-Driver: SDARS/Compensator/GPS



GRF4002W Application Schematic



GRF4002W Evaluation Board Assembly Diagram

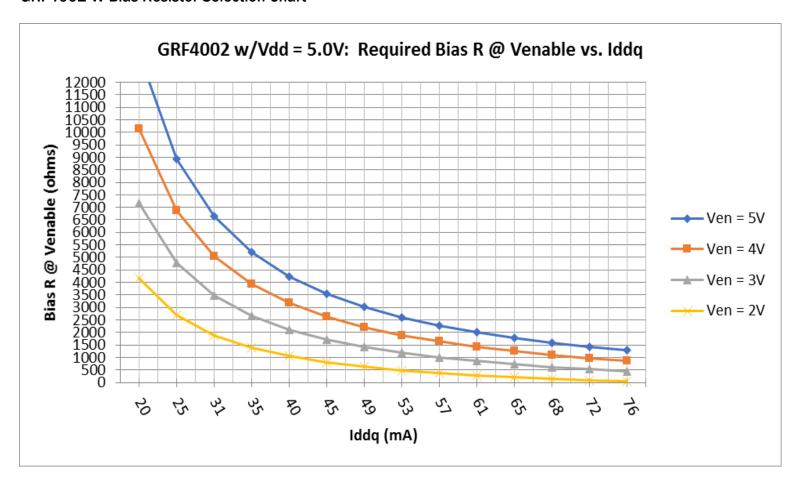




GRF4002W Standard Evaluation Board BOM: (0.7 to 3.8 GHz Tune)

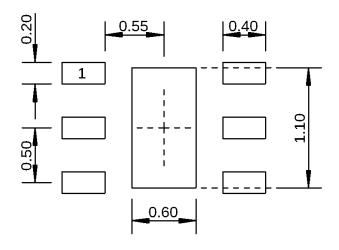
Component	Туре	Manufacturer	Family	Value	Package Size	Substitution
M2	Capacitor	Murata	GRM	100 pF	0402	ok
M5 (See curves)	Resistor	Various	5%	Sets Iddq	0402	ok
M8	Capacitor	Murata	GRM	0.1 uF	0402	ok
M10	Inductor	Coilcraft	НР	100 nH	0402	ok
M11	Capacitor	Murata	GRM/GJM	0.5 pF	0402	ok
M12	Capacitor	Murata	GRM	100 pF	0402	ok

GRF4002-W Bias Resistor Selection Chart



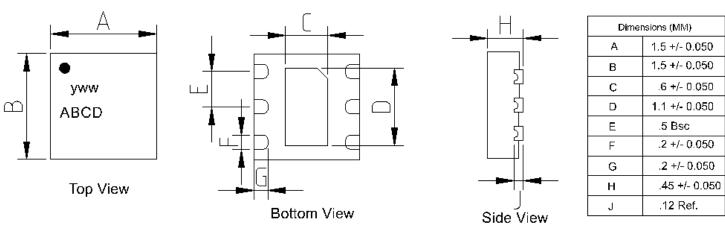


LNA-Driver: SDARS/Compensator/GPS



Dimensions in millimeters

1.5 mm DFN-6 Suggested PCB Footprint (Top View)



Dimensions (MM)				
Α	1.5 +/- 0.050			
В	1.5 +/- 0.050			
Ç	.6 +/- 0.050			
D	1.1 +/- 0.050			
E	.5 Bsc			
F	.2 +/- 0.050			
G	.2 +/- 0.050			
Н	.45 +/- 0.050			
٦	.12 Ref.			

1.5 mm DFN-6 Package Dimensions

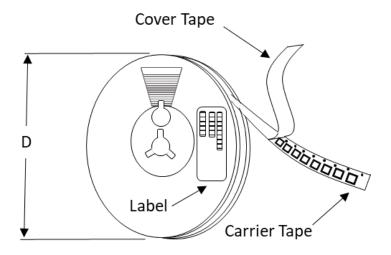


Tape and Reel Information:

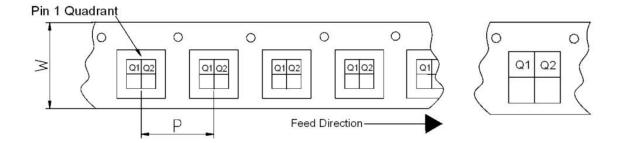
Revision Date: 07/10/20

Guerrilla RF's Tape and Reel specification complies with the Electronics Industries Association (EIA) standards for 'Embossed Carrier Tape of Surface Mount Components for Automatic Handling". Reference EIA-481. See the table on the following page for Tape and Reel specifications along with units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape, wound into a plastic reel. Each reel will be packaged in a cardboard box. There will be product labels on the reel, the protective ESD bag and the outside surface of the box.



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information







Tape and Reel Specification and Device Package Information Table

Package			Carrier Tape			Reel		
Туре	Dimensions (mm)	Leads	Weight (mg)	Width (W) (mm)	Pocket Pitch (P) (mm)	Pin 1 Quad- rant	Diameter (D) (inches)	Units per Reel
QFN	2.0 x 2.0 x 0.50	12	7	8	4	Q1	7	2500
QFN	3.0 x 3.0 x 0.85	16	24	12	8	Q1	7	1500
DFN	1.5 x 1.5 x 0.45	6	4	8	4	Q1	7	2500
DFN	2.0 x 2.0 x 0.75	8	12	8	4	Q1	7	2500
LFM	3.5 x 3.5 x 0.75	See	TBD	12	8	Q2	7	1500
LFM	4.0 x 4.0 x 0.75	See note	TBD	12	8	Q2	7	1500

Note: Lead count may vary. Reference applicable product data sheet



GRF4002W

LNA-Driver: SDARS/Compensator/GPS

Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

Information in this datasheet is specific to the Guerrilla RF, Inc. ("Guerrilla RF") product identified.

Revision Date: 07/10/20

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