

G

PART NO. : HFJ12-1G01ERL

TURNS RATIO

INSERTION LOSS 1-100MHz

RETURN LOSS

60MHz

80MHz

100MHz

CMR 1-30MHz

ISOLATION

1 - 40 MHz

OCL (100KHz,0.1V,8mA)

CROSSTALK 1-100MHz

30-60MHz

60-100MHz

2-PORT FASTJACK[™] WITH 1000BASE-T MAGNETICS RoHS COMPLAINT COMPATIBLE TO LEAD-FREE WAVE SOLDERING PROCESS FOR THROUGH HOLE COMPONENTS OPERATING TEMPERATURE 0/70°C

 $1:1 \pm 3\%$

350µH min

-1.1dB max

-18dB min

-14dB min

-12dB min

-10dB min

-35dB typ

-50dB typ

-40dB typ

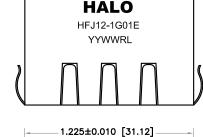
-35dB typ

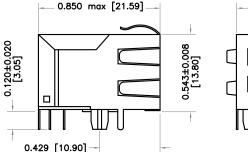
1,500Vrms

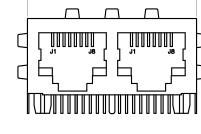
ELECTRICAL SPECIFICATIONS @ 25°C

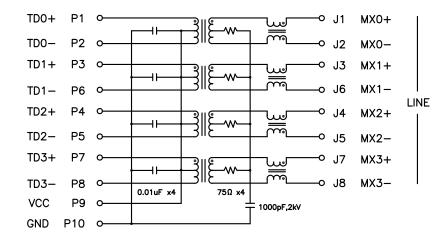
Housing Material: Polyester, Black UL94V-O Contacts: Phosphor Bronze 30µin Au plating over Ni

Leads: Matte Sn over Ni Shield: Cu Alloy with Ni plating 40µin min Durability: 1000 mating cycles Dimensions: Inch [mm] Tolerances: ±0.008 inch if not specified









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HALO/PBL	TITLE	^{TITLE} FastJack™ 1X2			SIGNATURES		DATE	REV.	DESC.	DATE
	500			DRAWN	LI ZHI ZHONG	7/29/05	А	FIRST ISSUE	7/29/05	
	GIGADIT LITILINILI				CHECKED	LEI KEONG	12/4/13	В	PROD. RELEASE	4/13/11
CALIFORNIA, USA KOWLOON, HONG KONG SINGAPORE	PART NO. HFJ12-1G01ERL				APPROVED	PETER LU	12/4/13	С	CORRECT PCB LAYOUT	12/4/13
	SCALE	NONE	PAGE	1 оғ 2	FILE	FJ121G01ERL.DWG				

Ø0.035 [0.89] ×20 Ø0.128 [3.25] ×2 Ø0.063 [1.60] ×3 REC				0.050 [1.27] 0.100 [2.54] 9.100 [2.55] 9.100 [2.55] 9.100 [2.55] 9.100 [2.55] 9.100 [2.55] 9.100	0.403 [10.25]	\$00.0±010.0 [0.25]	1.269±0.00	SIDE P	CB 0.586±0.008 [14.89]	
	TITLE	FastJack™	1X2		SIGNATURE		DATE	REV.	DESC.	DATE
HALO/PBL	FOR	GIGABIT ETH	IERNET		DRAWN CHECKED	LI ZHI ZHONG LEI KEONG	7/29/05	A B	FIRST ISSUE PROD. RELEASE	7/29/05 4/13/11
CALIFORNIA, USA	PART NO.	·HFJ12-1G0	1ERL		APPROVED	PETER LU	12/4/13	C	CORRECT PCB LAYOUT	12/4/13
CALIFORNIA, USA KOWLOON, HONG KONG SINGAPORE	SCALE	NONE	PAGE	2 of 2	FILE			•	1	

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