

RD134 Series 4A TRIACS

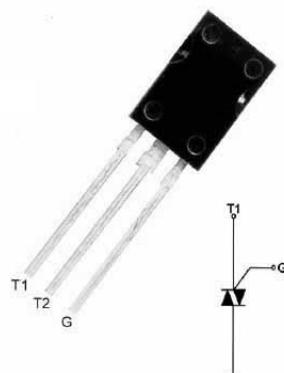
DESCRIPTION:

- P+ Isolation Diffusion
- Single Mesa
- Glass Passivated
- Sensitive gate triacs in a plastic envelope
- Intended for use in general purpose bidirectional switching and phase control applications; These devices are intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits

MAIN FEATURES

Symbol	Value	Unit
I _T (RMS)	4	A
V _{DRM/V_{RRM}}	600/800	V
V _{TM}	≤1.7	V

TO-126



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T _{stg}	-40 to +150	°C
Operating junction temperature range	T _j	-40 to +125	°C
Repetitive Peak Off-state Voltage	V _{DRM}	600/800	V
Repetitive Peak Reverse Voltage	V _{RRM}	600/800	V
Non repetitive Surge Peak Off-state Voltage	V _{DSM}	700/900	V
Non repetitive Peak Reverse Voltage	V _{RSM}	700/900	V
RMS on-state current (full sine wave)	I _T (RMS)	4	A
Non repetitive surge peak on-state current (full cycle, T _j =25°C)	f = 60 Hz t=16.7ms	27	A
	f = 50 Hz t=20ms	25	
I ² t Value for fusing	I ² t	3.1	A ² s
Critical rate of rise of on-state current I _T ≤6A, I _G =2×I _{GT} , tr≤100 ns, f=120Hz, T _j =125°C	T ₂₊ G+	50	A/μs
	T ₂₋ G-	50	
	T ₂₋ G-	50	
	T ₂₋ G+	10	
Peak gate current tp=20us, T _j =125°C	I _{GM}	2	A
Average gate power dissipation T _j =125°C	P _{G(AV)}	0.5	W

ELECTRICAL CHARACTERISTICS ($T_j=25^\circ\text{C}$ unless otherwise specified)

Symbol	Test Condition	Quadrant		RD134		Unit
				D	E	
I_{GT}	$V_D=12V \quad R_L=33\Omega$	I-II-III IV	MAX.	5 10	10 25	mA
V_{GT}		ALL	MAX.	1.5		V
V_{GD}	$V_D=V_{DRM} \quad R_L=3.3K\Omega \quad T_j=125^\circ\text{C}$	ALL	MIN.	0.2		V
I_L	$I_G=1.2I_{GT}$	I-III-IV	MAX.	15	20	mA
		II	MAX.	20	35	mA
I_H	$ I_T =100\text{mA}$		MAX.	15	20	mA
dV/dt	$V_D=67\%V_{DRM}$ gate open $T_j=125^\circ\text{C}$		MIN.	5	50	V/ μs

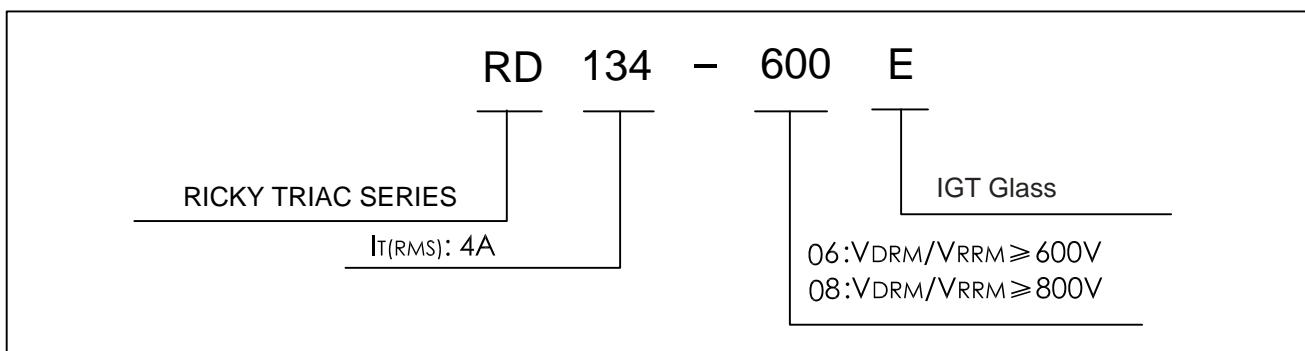
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=5A, t_p=380\mu\text{s}$	$T_j=25^\circ\text{C}$	1.7	V
I_{DRM} I_{RRM}	$V_D=V_{DRM} \quad V_R=V_{RRM}$	$T_j=25^\circ\text{C}$	5	μA
		$T_j=125^\circ\text{C}$	0.5	mA

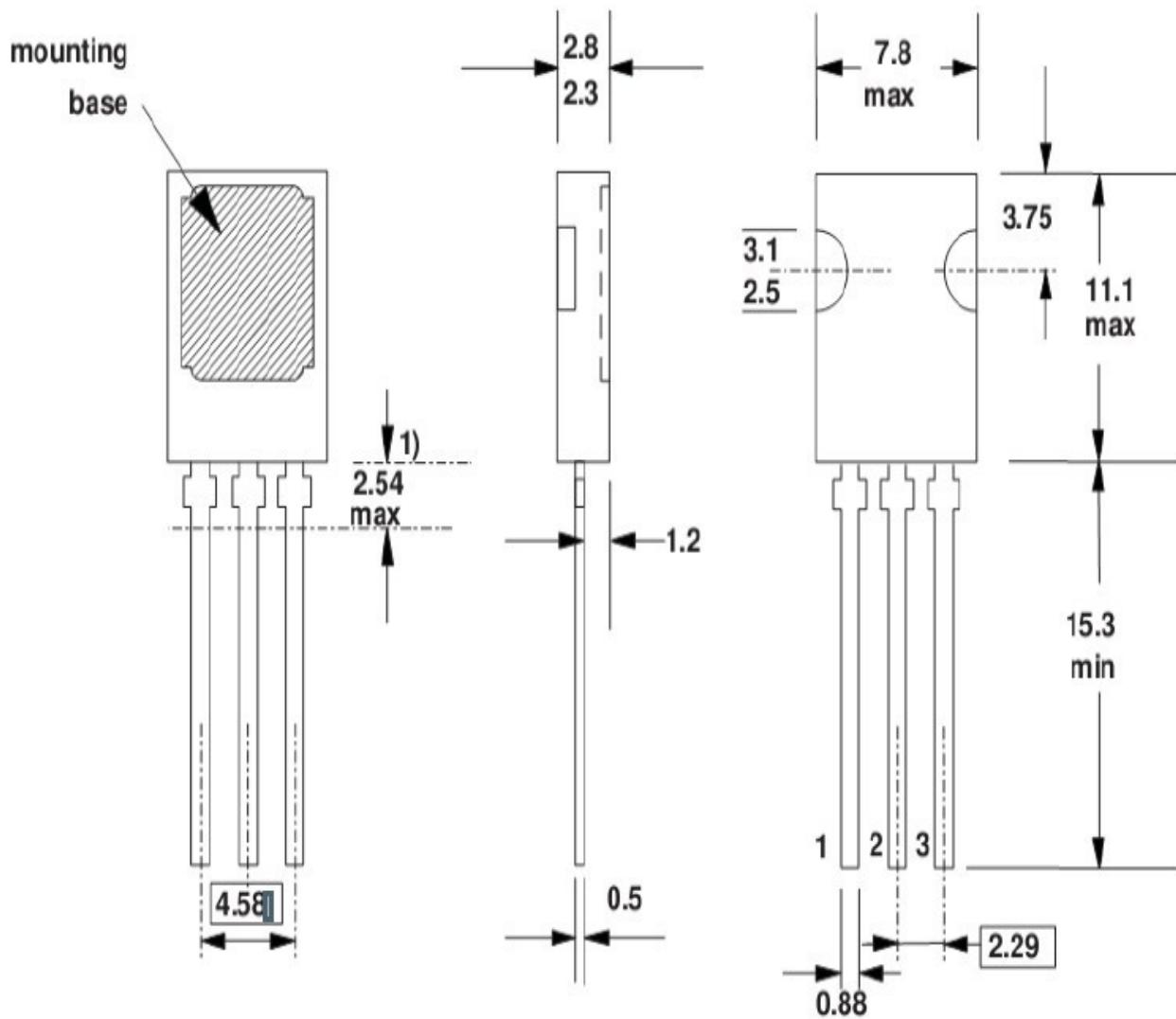
THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th}(J-C)$	Junction to Case(AC)	4.1	$^\circ\text{C}/\text{W}$

ORDERING INFORMATION



PACKAGE MECHANICAL DATA



1) Lead dimensions within this
zone uncontrolled.

FIG.1:Maximum power dissipation versus RMS on-state current(full cycle)

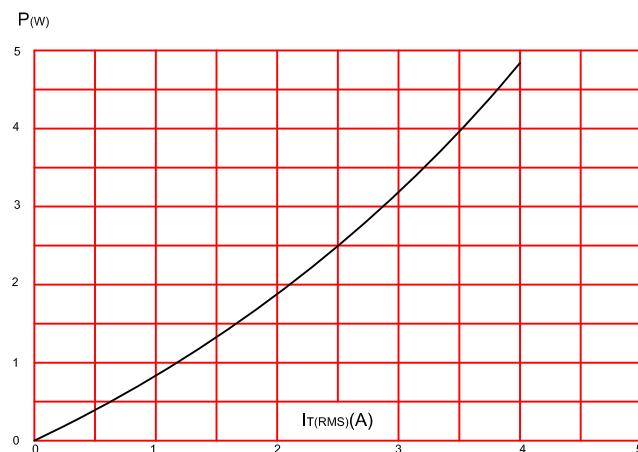


FIG.2:RMS on-state current versus case temperature(full cycle)

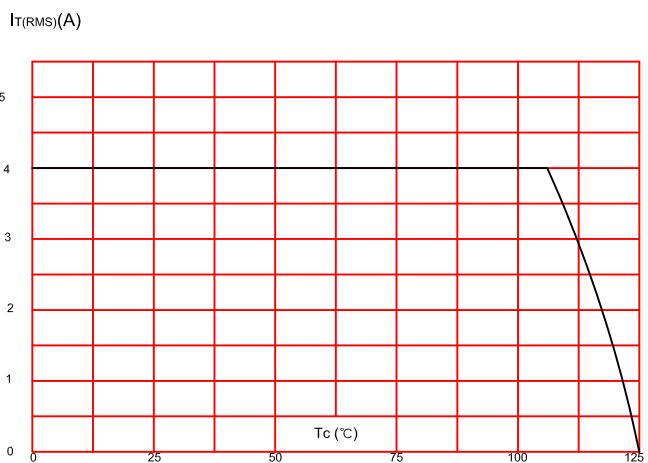


FIG.3:On-state characteristics (maximum values).

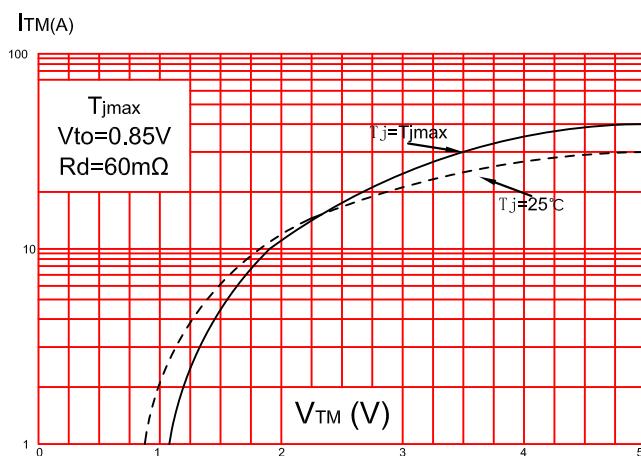


FIG.4:Surge peak on-state current versus number of cycles.

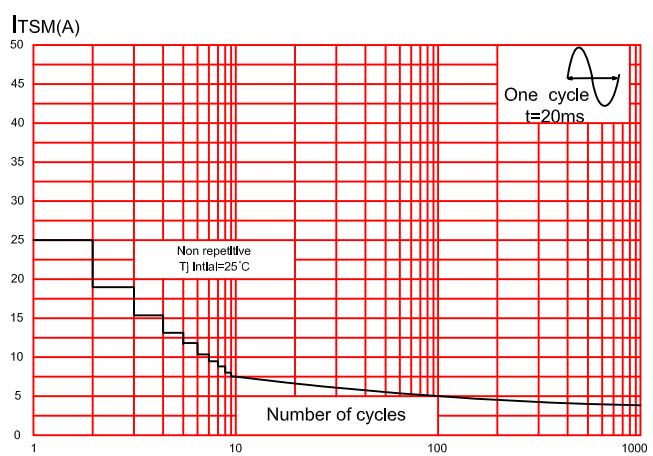


FIG.5:Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10\text{ms}$,and corresponding value of I^2t .

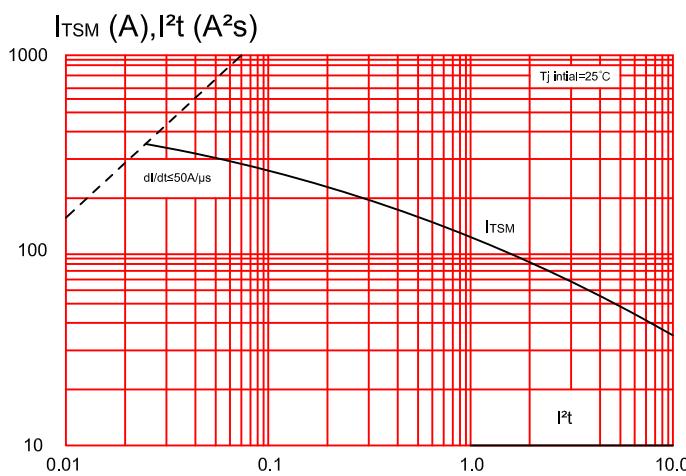
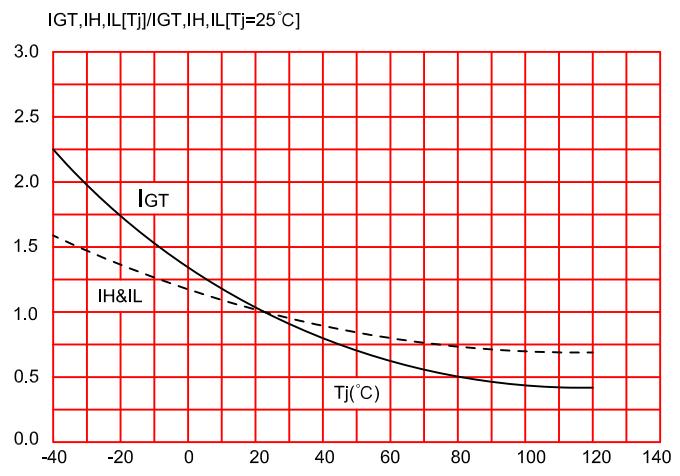


FIG.6:Relative variations of gate trigger current,holding current and latching current versus junction temperature(typical values)



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