

RS12xxF Series 12A TRIACS

DESCRIPTION:

High current density due to double mesa technology, SIPOS and glass passivation.

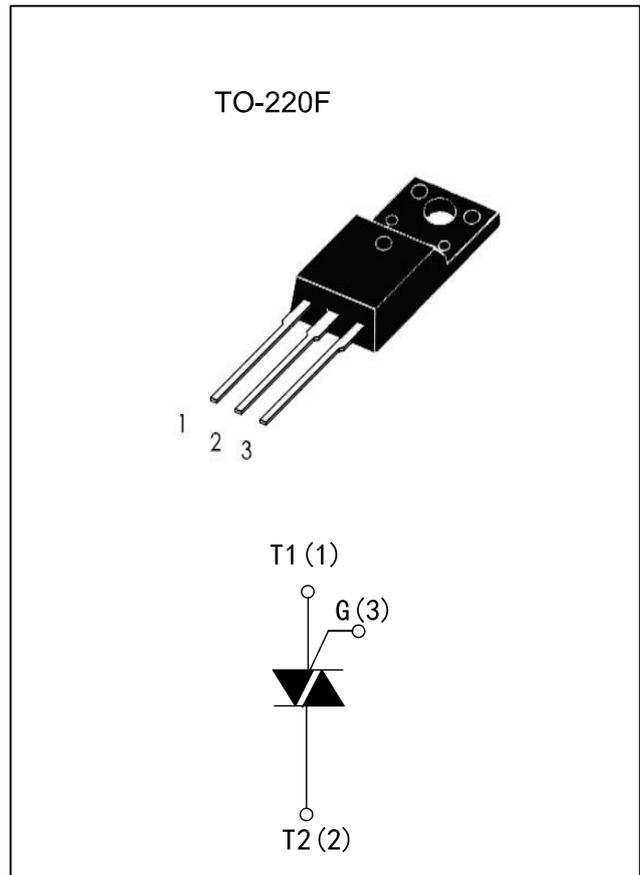
RS12xxF series triacs are suitable for general purpose AC switching, They can be used as an ON/OFF function in applications such as static relays, heating regulation, induction motor starting circuits...or for phase control operation, light dimmers, motor speed controllers.

RS12xxF-SW -CW -BW are 3 quadrants triacs, They are specially recommended for use on inductive loads.

RS12xxF are isolated in internal, they provide a 2500V RMS isolation voltage from all three terminals to external heat sink.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	12	A
V_{DRM}/V_{RRM}	600 and 800	V
$I_{GT(Q1)}$	5 to 50	mA



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40 to +150	°C
Operating junction temperature range	T_j	-40 to +125	°C
Repetitive Peak Off-state Voltage	V_{DRM}	600 and 800	V
Repetitive Peak Reverse Voltage	V_{RRM}	600 and 800	
Non repetitive Surge Peak Off-state Voltage	V_{DSM}	700 and 900	V
Non repetitive Peak Reverse Voltage	V_{RSM}	700 and 900	
RMS on-state current (full sine wave)	$I_{T(RMS)}$	8	A
Non repetitive surge peak on-state current (full cycle, $T_j=25^\circ\text{C}$)	$f = 60\text{ Hz}$ $t = 16.7\text{ms}$	84	A
	$f = 50\text{ Hz}$ $t = 20\text{ms}$	80	
I^2t Value for fusing	I^2t	36	A^2s
Critical rate of rise of on-state current $I_G=2 \times I_{GT}$, $t_r \leq 100\text{ ns}$, $f=120\text{Hz}$, $T_j=125^\circ\text{C}$	di/dt	50	$\text{A}/\mu\text{s}$
Peak gate current $t_p=20\mu\text{s}$, $T_j=125^\circ\text{C}$	I_{GM}	4	A
Average gate power dissipation $T_j=125^\circ\text{C}$	$P_{G(AV)}$	1	W

ELECTRICAL CHARACTERISTICS (T_j=25°C unless otherwise specified)

● 3 Quadrants

Symbol	Test Condition	Quadrant		RS12xxF				Unit
				TW	SW	CW	BW	
I _{GT}	V _D =12V R _L =33Ω	I-II-III	MAX.	5	10	35	50	mA
V _{GT}		I-II-III	MAX.	1.3				V
V _{GD}	V _D =V _{DRM} R _L =3.3KΩ T _j =125°C	I-II-III	MIN.	0.2				V
I _L	I _G =1.2I _{GT}	I-III	MAX.	10	25	50	70	mA
		II	MAX.	15	30	60	80	mA
I _H	I _T =100mA		MAX.	10	15	35	50	mA
dV/dt	V _D =67%V _{DRM} gate open T _j =125°C		MIN.	20	40	400	1000	V/μs
(dI/dt) _c	(dV/dt) _c =0.1V/μs T _j =125°C		MIN.	3.5	5.4	---	---	A/mS
	(dV/dt) _c =10V/μs T _j =125°C			1.5	2.8	---	---	
	Without snubber T _j =125°C			---	---	4.5	7	

● 4 Quadrants

Symbol	Test Condition	Quadrant		RS12xxF		Unit
				C	B	
I _{GT}	V _D =12V R _L =33Ω	I-II-III IV	MAX.	25 50	50 100	mA
V _{GT}		ALL	MAX.	1.3		V
V _{GD}	V _D =V _{DRM} R _L =3.3KΩ T _j =125°C	ALL	MIN.	0.2		V
I _L	I _G =1.2I _{GT}	I-III-IV	MAX.	40	50	mA
		II	MAX.	80	100	mA
I _H	I _T =500mA		MAX.	25	50	mA
dV/dt	V _D =67%V _{DRM} gate open T _j =125°C		MIN.	200	400	V/μs
(dV/dt) _c	(dI/dt) _c =7A/ms T _j =125°C		MIN.	5	10	V/μs

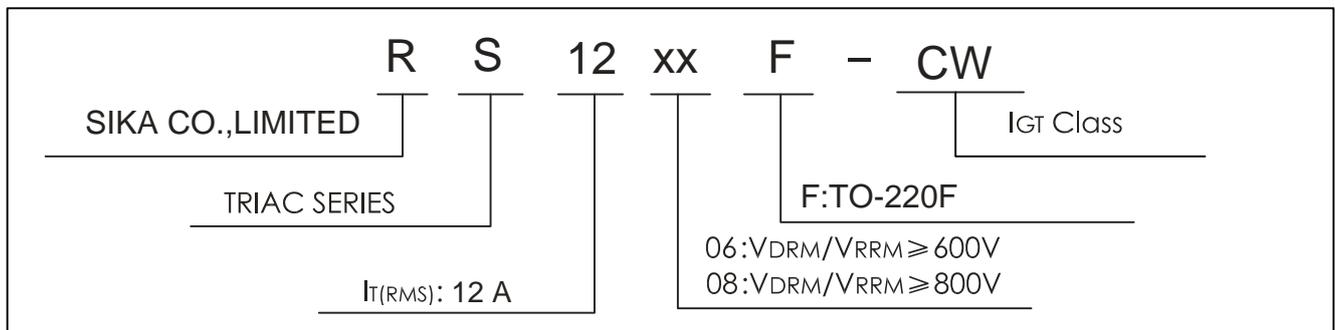
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V _{TM}	I _{TM} =11A, t _p =380μs	T _j =25°C	1.55	V
I _{DRM} I _{RRM}	V _D =V _{DRM} V _R =V _{RRM}	T _j =25°C	5	μA
		T _j =125°C	1	mA

THERMAL RESISTANCES

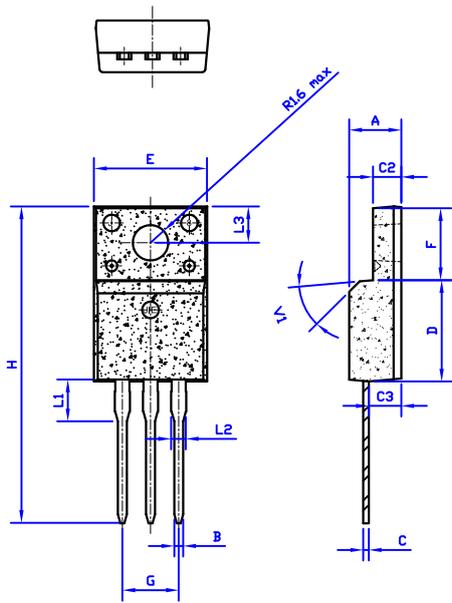
Symbol	Parameter		Value	Unit
R _{th} (J -C)	Junction to Case(AC)	TO-220F	3.7	°C/W

ORDERING INFORMATION



PACKAGE MECHANICAL DATA

TO-220F



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.3		4.7	0.169		0.185
B	0.74	0.8	0.83	0.029	0.031	0.033
C	0.5		0.75	0.020		0.030
C2	2.4		2.7	0.094		0.106
C3	2.5		2.9	0.098		0.114
D	8.6		9.2	0.338		0.362
E	9.7		10.3	0.382		0.406
F	6.3		6.5	0.248		0.256
G	5.0		5.2	0.197		0.205
H	28.0		29.8	11.0		11.7
L1		3.63			0.143	
L2	1.14		1.7	0.044		0.067
L3		3.2			0.126	
V1		40°			40°	

FIG.1:Maximum power dissipation versus RMS on-state current(full cycle)

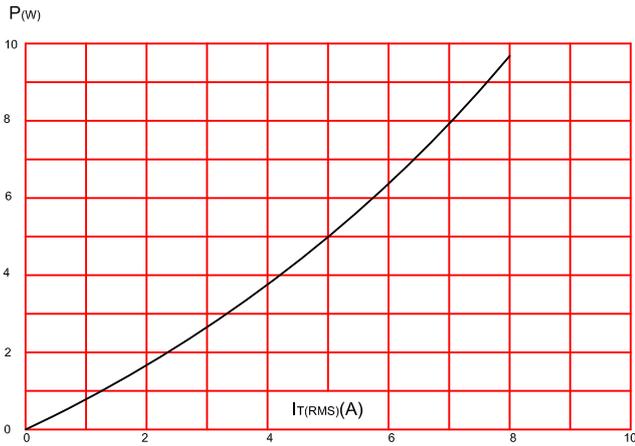


FIG.2:RMS on-state current versus case temperature(full cycle)

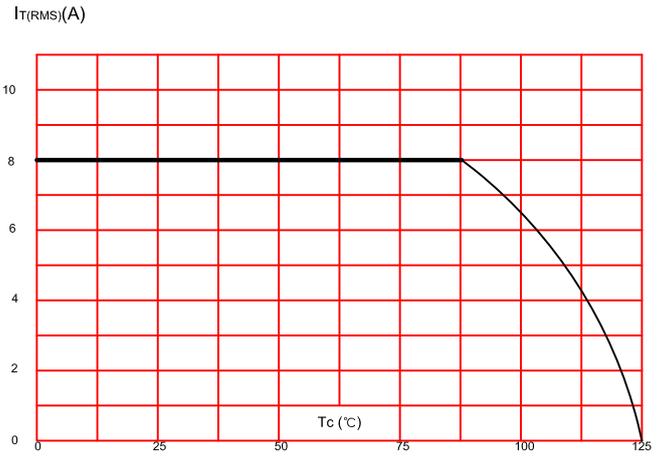


FIG.3:On-state characteristics (maximum values).

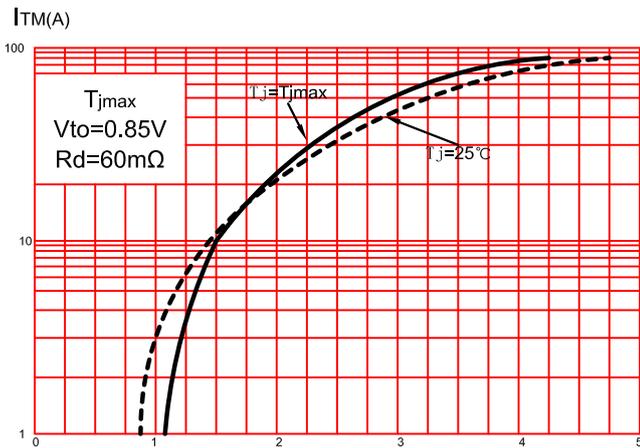


FIG.4:Surge peak on-state current versus number of cycles.

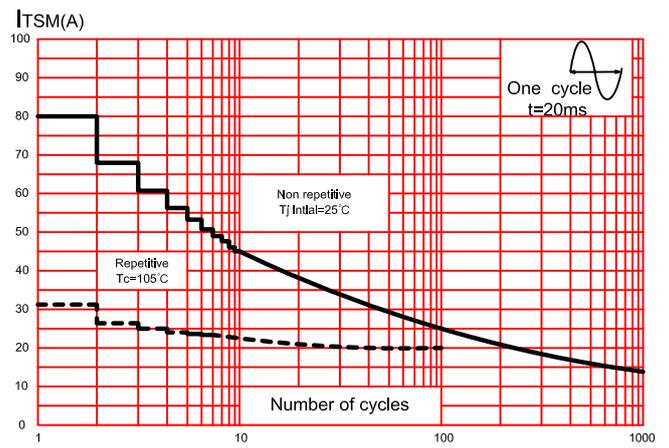


FIG.5:Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10ms$, and corresponding value of I^2t .

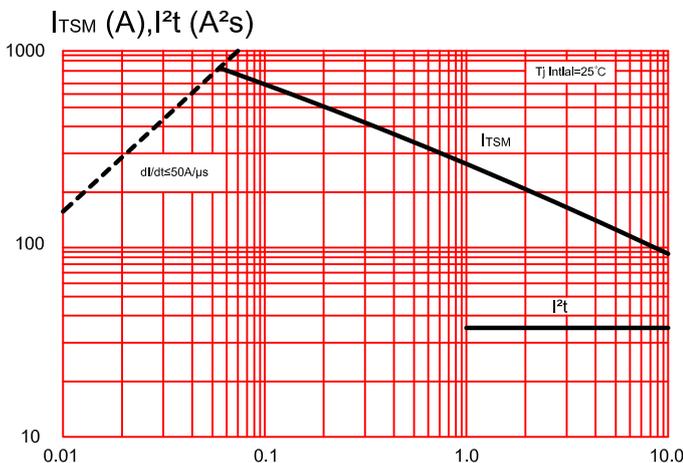
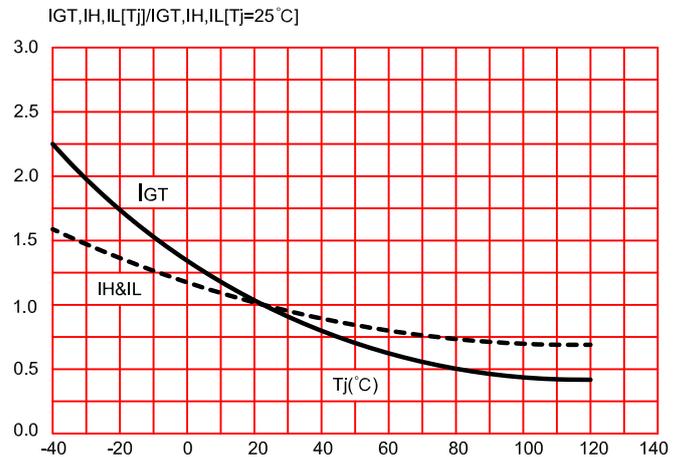


FIG.6:Relative variations of gate trigger current, holding current and latching current versus junction temperature(typical values)



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