

RS1608 Series 16A TRIACs

DESCRIPTION:

High current density due to double mesa technology, SIPOS and Glass Passivation.

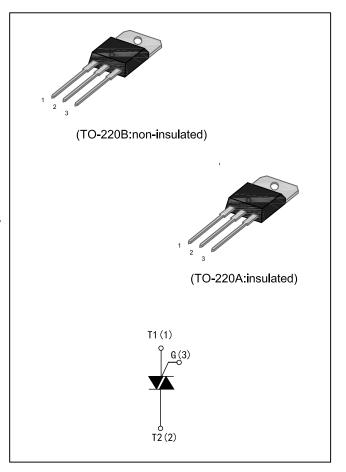
RS1608A/B series triacs is suitable for general purpose AC switching, They can be used as an ON/OFF function in applications such as static relays, heating regulation, induction motor stating circuits...or for phase contol operation, light dimmers, motor speed controllers.

RS1608A/B-SW -CW -BW are 3 quadrants triacs, They are specially recommended for use on inductive loads.

RS1608A are isolated in internal, they provide a 2500V RMS isolation voltage from all three terminals to external heat sink.

MAIN FEATURES

Symbol	Value	Unit
IT(RMS)	16	А
VDRM/VRRM	600 and 800	V
Vтм	1.55	V



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit		
Storage junction temperature range	Tstg	-40 to +150	°C		
Operrating junction temperature range		Tj	-40 to +125	°C	
Repetitive Peak Off-state Voltage	Tj=25°C	VDRM	600and800		
Repetitive Peak Reverse Voltage	Tj=25°C	VRRM	600and800	V	
Non repetitive Surge Peak Off-state Voltage	tn=10ma Ti=25°C	Vdsm	700and900	\ /	
Non repetitive Peak Reverse Voltage	tp=10ms,Tj=25°C	Vrsm	700and900	V	
RMS on-state current (full sine wave)	RS1608B Tc=100°C	IT/DMC)	16	А	
Niss on-state current (tull sine wave)	RS1608A Tc=86°C	T(RMS)			
Non repetitive surge peak on-state current	f = 60 Hz t=16.7ms	TSM	168	А	
(full cycle,Tj=25°C)	f = 50 Hz t=20ms	115101	160		
I²t Value for fusing	tp=10ms	l²t	144	A²s	
Critical rate of rise of on-state current (IG=2×IGT,tr≤100 ns,f=120Hz,Tj=125°C)	dl /dt	50	A/µs		
Peak gate current (tp=20us,Tj=125°C)	lgм	4	Α		
Peak Gate Power Dissipation (tp=20us,Tj=125	Рдм	10	W		
Average gate power dissipation (Tj=125°C)	PG(AV)	1	W		



ELECTRICAL CHARACTERISTICS(Tj=25°C unless otherwise specified)

3 Quadrants

Symbol	Test Condition	Quadrant		RS1608				Unit
Cymbol	rest condition	Quadrant		TW	SW	CW	BW	Onit
IGT	V5=42V B1=22O	1-11-111	MAX.	5	10	35	50	mA
Vgт	VD=12V RL=33Ω		MAX.	1.3				V
VGD	VD=VDRM RL=3.3KΩ Tj =125℃	1-11-111	MIN.	0.2				\ \
1.	IG=1.2IGT	I-III	MAX.	15	20	50	70	mA
IL		II	MAX.	25	35	60	80	mA
Ін	IT =500mA			10	15	40	60	mA
dV/dt	VD=67%VDRM gate open Tj=125℃		MIN.	20	40	400	1000	V/µs
	(dV/dt)c=0.1V/µs Tj=125℃			3.5	8.5			
(dl/dt)c	(dV/dt)c=10V/μs Tj=125℃		MIN.	1.0	3.0			A/mS
	Without snubber Tj=125℃					8.5	14	

4 Quadrants

Symbol	Test Condition	Quadrant		RS1608		Unit
Cyrribol	rest Condition	Quadrant		С	В	
lgт	Vp=12V Br=22O	I-II-III IV	MAX.	25 50	50 100	mA
VgT	VD=12V RL=33Ω VGT		MAX.	1.3		V
VGD	VD=VDRM RL=3.3KΩ Tj =125℃	ALL	MIN.	0.2		V
IL	1- 4 01	I-III-IV	MAX.	40	50	mA
	IG=1.2IGT	MAX.	80	100	mA	
Ін	IT =500mA	MAX.	25	50	mA	
dV/dt	VD=67%VDRM gate open Tj=125	MIN.	200	400	V/µs	
(dV/dt)c	(dl/dt)c=7A/ms Tj=125℃	MIN.	5	10	V/µs	



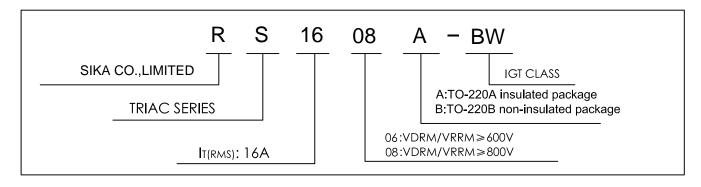
STATIC CHARACTERISTICS

Symbol	Parame	Value(MAX.)	Unit	
VTM	Iτм=22.5A,tp=380μs		1.55	V
IDRM	VD=VDRM VR=VRRM	Tj=25℃	5	μΑ
IRRM	VD-VDKW VK-VKKW	Tj=150℃	2	mA

THERMAL RESISTANCES

Symbol	Param	Value	Unit		
Rth(j-c)	Junction to Case(AC)	TO-220B 2.1		2.1	°C/W
		TO-220A	1.2	C/VV	
Rth(j-a)	Junction to ambient (S=1cm²)	TO-220A	60	°C/W	
		TO-220B	60	C/VV	

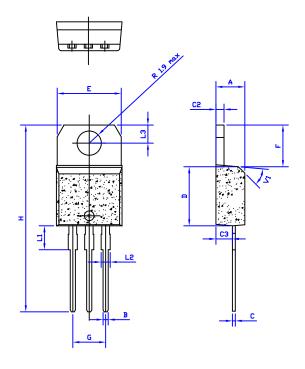
ORDERING INFORMATION





PACKAGE MECHANICAL DATA

TO-220A insulated package and TO-220B non-insulated package



	Dimensions						
Ref.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	4.4		4.6	0.173		1.181	
В	0.61		0.88	0.024		0.034	
С	0.46		0.70	0.018		0.027	
C2	1.23		1.32	0.048		0.051	
C3	2.4		2.72	0.094		0.107	
D	8.6		9.7	0.338		0.382	
E	9.8		10.4	0.386		0.409	
F	6.2		6.6	0.244		0.259	
G	4.8		5.4	0.189		0.213	
Н	28.0		29.8	11.0		11.7	
L1		3.75			0.147		
L2	1.14		1.7	0.044		0.066	
L3	2.65		2.95	0.104	·	0.116	
V1		40°			40°		

FIG.1:Maximum power dissipation versus RMS on-state current(full cycle)

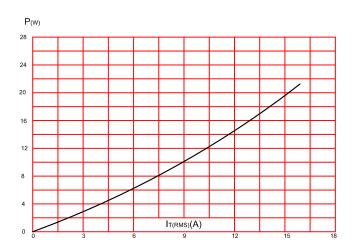


FIG.2:RMS on-state current versus case temperature(full cycle)

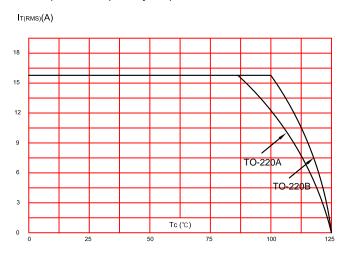


FIG.3:On-state characteristics (maximum values).

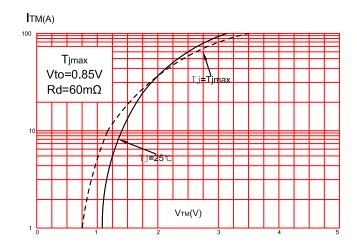


FIG.4:Surge peak on-state current versus number of cycles.

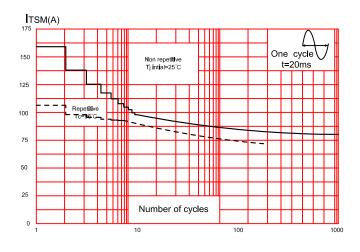


FIG.5:Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms,and corresponding value of l²t.

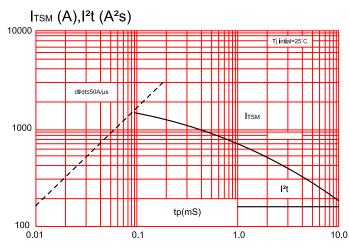
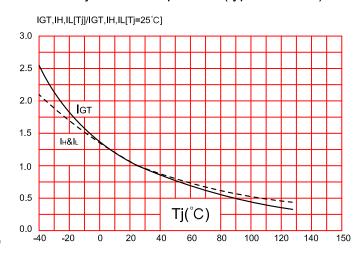


FIG.6:Relative variations of gate trigger current, holding current and latching current versus junction temperature(typical values)



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