

RS40 Series 40A TRIACs

DESCRIPTION:

High current density due to double mesa technology, SIPOS and Glass Passivation.

RS40Z / RS40 series triacs is suitable for general purpose AC switching,They can be used as an ON/OFF function in applications such as static relays, heating regulation,induction motor stating circuits... or for phase contol operation, light dimmers,motor speed controllers.

RS40Z-CW-BW Series are 3 quadrants triacs, They are specially recommended for use on inductive loads.

RS40Z series provide a 2500V RMS isolation voltage from all three terminals to external heat sink.

MAIN FEATURES

Symbol	Value	Unit
IT(RMS)	40	А
VDRM/VRRM	600 / 800 / 1200 / 1600	V
Vтм	1.55	V

ABSOLUTE MAXIMUM RATINGS

1 2 3	TO-3P insulated
2 3 TG-C Insulated	T1 (1) G (3) G (3) T2 (2)

Parameter	Symbol	Value	Unit		
Storage junction temperature range	Tstg	-40 to +150	°C		
Operrating junction temperature range			Tj	-40 to +125	°C
	RS40Z06			600	V
Repetitive Peak Off-state Voltage Tj=25 °C	RS40Z08		VDRM	800	
Repetitive Peak Reverse Voltage Tj=25 °C	RS40Z	12	VRRM	1200	V
	RS40Z	16		1600	
Non repetitive Surge Peak Off-state Voltage	Non repetitive Surge Peak Off-state Voltage				v
Non repetitive Peak Reverse Voltage				V _{RRM} +100	
RMS on-state current (full sine wave)	TO-3P Tc=80°C		IT(RMS)	40	А
Trivio on-state current (fuil sine wave)	TG-C Tc=90°C				
Non repetitive surge peak on-state current	f = 60 Hz	t=16.7ms	Ітѕм	420	A
(full cycle,Tj=25°C)	f = 50 Hz	t=20ms	115101	400	
I²t Value for fusing	tp=10ms		l²t	880	A²s
Critical rate of rise of on-state current IG=2×IGT, transfer	dl /dt	50	A/µs		
Peak gate current tp=20us,Tj=125 °C				4	А
Peak Gate Power Dissipation tp=20us,Tj=125 °C				10	W
Average gate power dissipation Tj=125 °C	PG(AV)	1	W		

ELECTRICAL CHARACTERISTICS (Tj=25°C unless otherwise specified)

Symbol	Test Condition	Quadrant		Limits		Unit	
Symbol	Test Condition	Quadrant		BW	В	Unit	
IGT	Vp=12V Br=220	I-II-III IV	MAX.	50 -	50 100	mA	
Vgτ VD=12V RL=33Ω		I-II-III IV	MAX.	1.3 -	1.3	V	
Vgd	VD=VDRM RL=3.3KΩ Tj =125℃	I-II-III IV	MIN.	0.2 -	0.2	V	
IL	IL IG=1,2IGT		MAX.	80 -	70	mA	
		II	MAX.	100	90	mA	
Ін	IT =100mA		MAX.	60	60	mA	
dV/dt	VD=67%VDRM gate open Tj=125℃		MIN.	1000	500	V/µs	
(dl/dt)c	Without snubber Tj=125℃		MIN.	20	5	A/ms	

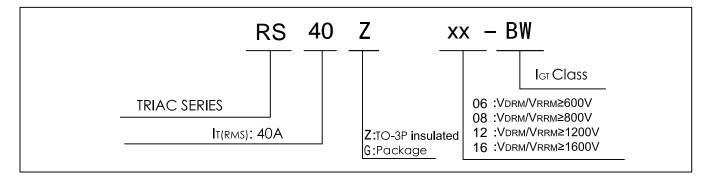
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
Vтм	ITM=60A,tp=380µs Tj=25℃		1.55	V
	Tj=25℃	10	μA	
IRRM	VD=VDRM VR=VRRM	Tj=125℃	5	mA

THERMAL RESISTANCES

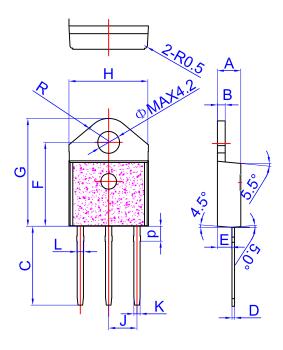
Symbol	Parameter		Value	Unit
Rth(J-C) Junction to Case(AC)	lupction to Case(AC)	TO-3P	0.9	°C/W
	TG-C	0.8	C/W	

ORDERING INFORMATION



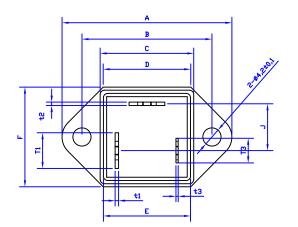
PACKAGE MECHANICAL DATA

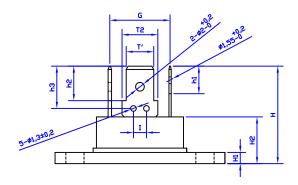
TO-3P insulated Package



	Dimensions						
Ref.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
A	4.4		4.6	0.173		0.181	
B	1.45		1.55	0.057		0.061	
С	14.35		15.6	0.565		0.614	
D	0.5		0.7	0.020		0.028	
E	2.7		2.9	0.106		0.114	
F	15.8		16.5	0.622		0.650	
G	20.4		21.1	0.815		0.831	
H	15.1		15.5	0.594		0.610	
J	5.4		5.65	0.213		0.222	
K	1.2		1.4	0.047		0.055	
L	1.35		1.50	0.053		0.059	
Ρ	2.8		3.0	0.110		0.118	
R		4.6			0.181		

TG-C Package





	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
A			39.2			1.543
В	29.8	30.0	30.2	1.173	1.181	1.189
С			21.6			0.85
D			20.2			0.795
E			20.1			0.791
F			23			0.906
T1、T2		8.25			0.325	
T3		5.7			0.224	
T'		6.35			0.25	
t1 ∖t2		0.8			0.031	
t3		0.5			0.020	
G		13.9			0.547	
H1		2.6			0.102	
H2		10.8			0.425	
Н			22.5			0.886
h1	6.2	6.35	6.5	0.244	0.25	0.256
h2	7.8	7.95	8.1	0.307	0.313	0.319
h3	9.45	9.75	10.05	0.372	0.384	0.396
	2.7	3.0	3.3	0.106	0.118	0.130
J		10.8			0.425	

FIG.1:Maximum power dissipation versus RMS on-state current(full cycle)

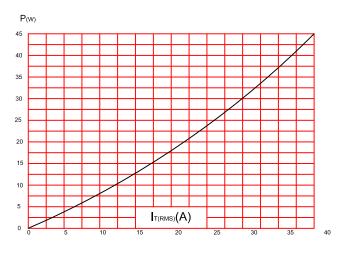


FIG.3:On-state characteristics (maximum values).

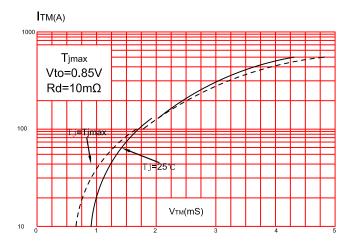


FIG.5:Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms,and corresponding value of l²t.

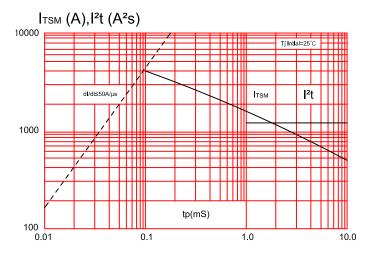


FIG.2:RMS on-state current versus case temperature(full cycle)

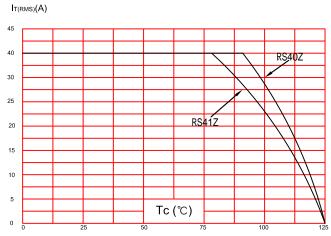


FIG.4:Surge peak on-state current versus number of cycles.

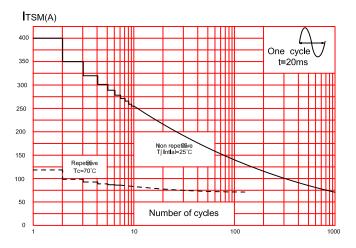


FIG.6:Relative variations of gate trigger current,holding current and latching current versus junction temperature(typical values)

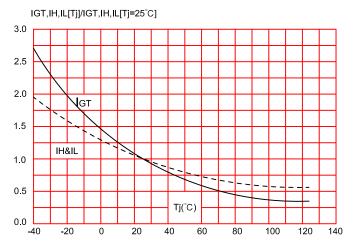


FIG.3:

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