

H3-MAPD-011002

5-1600MHz 75Ω 2 Way Power Divider

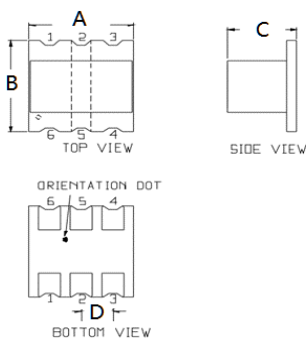
APPLICATIONS

- Ideally suitable for CATV applications.

FEATURES

- Surface mount
- 2 Way 0 degree
- 260°C reflow compatible
- RoHS compliant and Pb free
- Available on tape and reel

DIMENSIONS

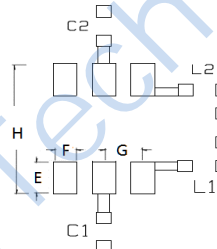


A	B	C
4.30	4.30	3.00
D		
1.27		

Unless otherwise stated dimensions are in mm

Tolerance: .xx ± 0.25, .xxx ± 0.05

PCB LAYOUT



Suggest:

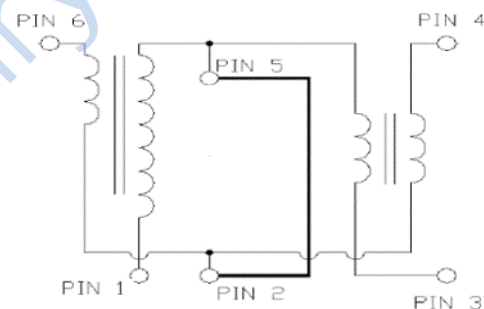
C1,C2=0.5pF

R1 L1,L2=8.2nH

R1=240Ω

E	F	G
1.40	0.76	1.27
H		
4.83		

FUNCTIONAL SCHEMATIC



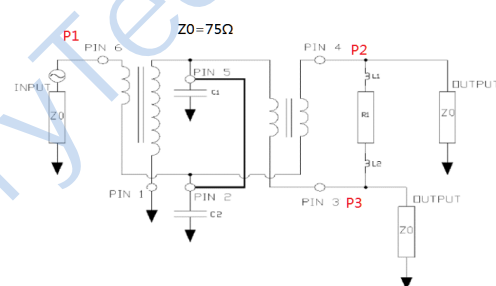
PIN CONFIGURATION

Pin No.	Function
1	Ground
2	External 0.5 pF Capacitor
3	Output 2
4	Output 1
5	External 0.5 pF Capacitor
6	Input

ELECTRICAL SPECIFICATIONS@25°C:

Parameters (P#: ENA PORT)	frequency band	Min.	Typ.	Max.	Unit
Port Impedance, Z0 (P1 / P2 / P3)			75 / 75 / 75		Ohm
Insertion Loss 1 (pin6-pin4)	5 - 500 MHz		0.3	0.5	dB
	500-1600 MHz		1.2	1.7	dB
Insertion Loss 2 (pin6-pin3)	5-300MHz		0.4	0.5	dB
	300-1200MHz		1.1	1.7	dB
	1200-1600MHz		2.1	2.7	dB
Input Return Loss	5-300MHz	20.0	26.0		dB
	300-1200MHz	15.0	17.0		dB
	1200-1600MHz	9.0	12.0		dB
Output Return Loss1 (pin4)	5 - 870 MHz	20.0	24.0		dB
	870-1600 MHz	12.0	16.0		dB
Output Return Loss2 (pin3)	5-300MHz	20.0	25.0		dB
	300-1200MHz	15.0	19.0		dB
	1200-1600MHz	11.0	13.0		dB
Isolation	5-300MHz	30.0	34.0		dB
	300-1200MHz	23.0	30.0		dB
	1200-1600MHz	20.0	27.0		dB
Amplitude Balance	5-500MHz		0.2	0.3	dB
	500-1200MHz		0.3	0.6	dB
	1200-1600MHz		0.7	1.3	dB
Phase Balance	5 - 870 MHz		0.7	1	Deg
	870-1600 MHz		2.3	5.5	Deg

APPLICATION & TEST CIRCUIT



Suggest: C1,C2=0.5pF

L1,L2=8.2nH

R1=240Ω

ABSOLUTE MAXIMUM RATINGS

Parameter	Value
Input RF Power	1W Max
Internal Load Dissipation	0.125W Max
Operating Temperature Range	-40 to 85°C

Tel: +86-28-85589664

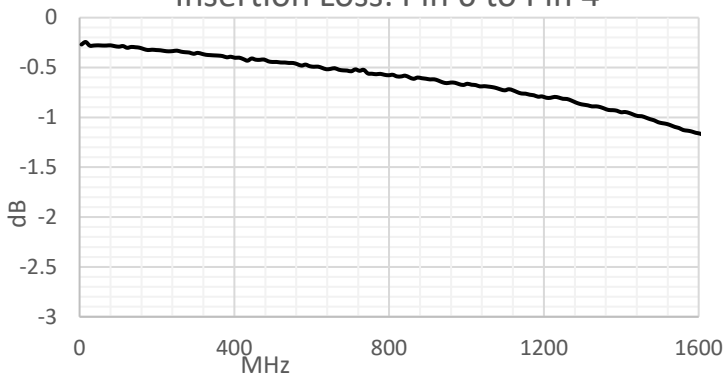
E-mail: mr_li@henglitaitech.com

Web: www.hltchips.com

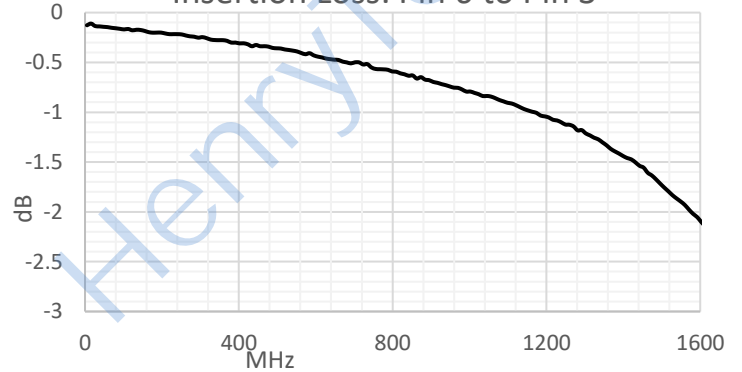
Address: No.198 Tianqin East Street, Gaoxin West District, Chengdu City

TEST CURVE

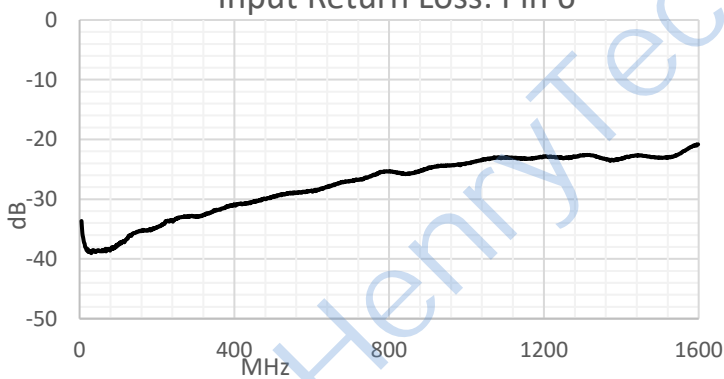
Insertion Loss: Pin 6 to Pin 4



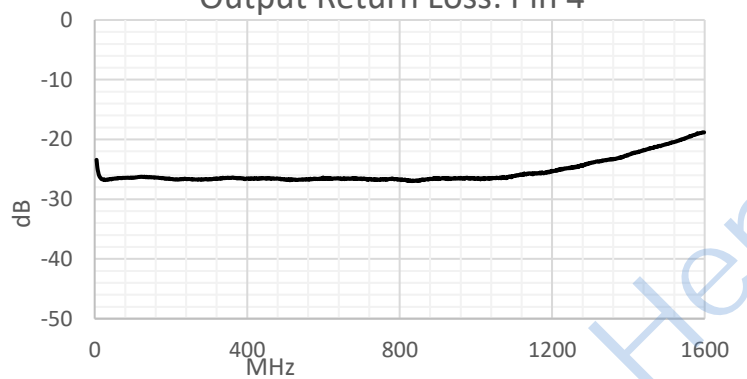
Insertion Loss: Pin 6 to Pin 3



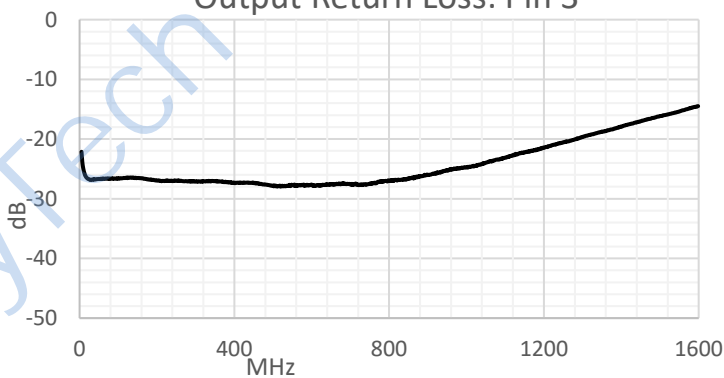
Input Return Loss: Pin 6



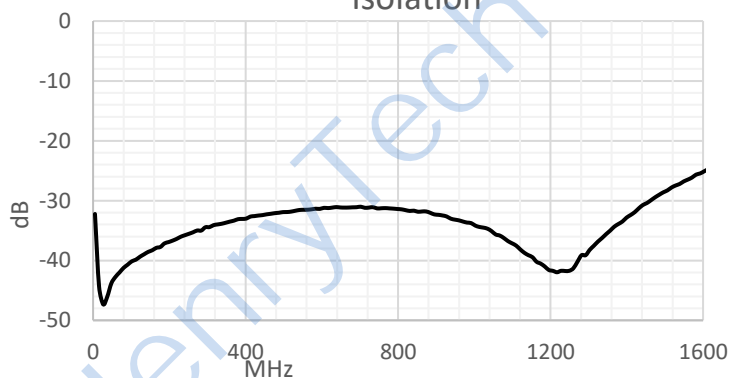
Output Return Loss: Pin 4



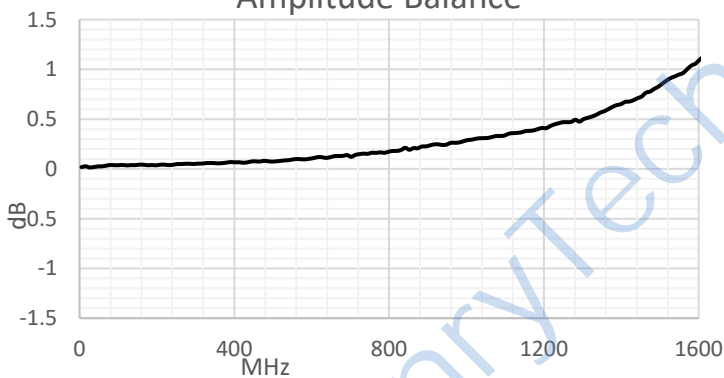
Output Return Loss: Pin 3



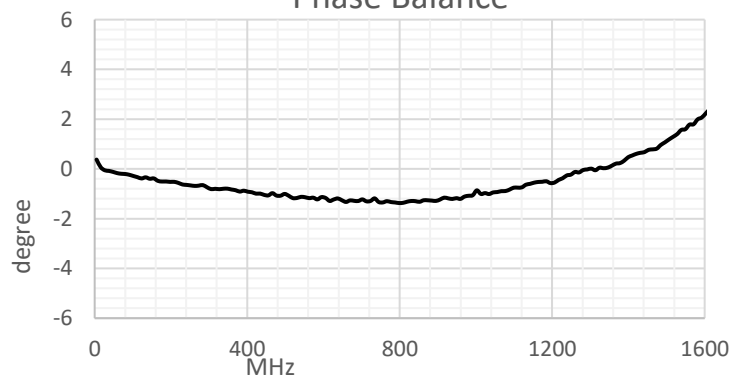
Isolation



Amplitude Balance



Phase Balance



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[MAPDCC0005](#) [3A325](#) [BD0810N50100AHF](#) [DC0710J5005AHF](#) [DC2327J5005AHF](#) [LFL15869MTC1B787](#) [X3C19F1-20S](#)

[CDBLB455KCAX39-B0](#) [RF1353C](#) [051157-0000](#) [PD0922J5050D2HF](#) [600S150FTRB](#) [1E1305-3](#) [1F1304-3S](#) [TP-103-PIN](#)

[BD1222J50200AHF](#) [BD1722J50100AHF](#) [2450DP39K5400E](#) [BD0810J50150AHF](#) [BD1722J50200AHF](#) [DS-327-PIN](#) [MACP-008125-](#)

[CK07F0](#) [DS-329-PIN](#) [DS-313-PIN](#) [TP-104-PIN](#) [TP-101-PIN](#) [HH-128-PIN](#) [8594810000](#) [T-1000-N](#) [JP506S](#) [XC0900P-10S](#) [XC0900B-30S](#)

[CHE1260-QAG](#) [11305-10](#) [5962-9091202MXA](#) [3A412S](#) [X3C06A4-03S](#) [B39000Z3410A4](#) [DSS-333-PIN](#) [PD2425J5050S2HF](#)

[B39242B4360P810](#) [B39781B8005P810](#)