# Low-Power, Slew-Rate-Limited RS-485/RS-422 Transceivers 

## General Description

The HGX485E is low-power transceivers for RS-485 and RS422 communication. IC contains one driver and one receiver. The driver slew rates of the HGX485 is not limited, allowing them to transmit up to 2.5 Mbps .
These transceivers draw between $120 \mu \mathrm{~A}$ and $500 \mu \mathrm{~A}$ of supply current when unloaded or fully loaded with disabled drivers. All parts operate from a single 5V supply. Drivers are short-circuit current limited and are protected against excessive power dissipation by thermal shutdown circuitry that places the driver outputs into a high-impedance state. The receiver input has a fail-safe feature that guarantees a
 logic-high output if the input is open circuit. The HGX485E is designed for half-duplex applications.

## Features

- Low Quiescent Current: 300 1 A
- -7 V to +12 V Common-Mode Input Voltage Range
- Three-State Outputs
- 30ns Propagation Delays, 5ns Skew
- Full-Duplex and Half-Duplex Versions Available
- Operate from a Single 5V Supply
- Allows up to 32 Transceivers on the Bus
- Data rate: $2,5 \mathrm{Mbps}$
- Current-Limiting and Thermal Shutdown for Driver Overload Protection



## ABSOLUTE MAXIMUM RATINGS

Supply Voltage ( $\mathrm{V}_{\mathrm{cc}}$ ) 12V
Control Input Voltage -0.5 V to ( $\mathrm{V}_{\mathrm{cc}}+0.5 \mathrm{~V}$ )
Driver Input Voltage (DI) -0.5 V to $\left(\mathrm{V}_{\mathrm{Cc}}+0.5 \mathrm{~V}\right)$
Driver Output Voltage (A, B) -8 V to +12.5 V
Receiver Input Voltage (A, B) -8 V to +12.5 V
Receiver Output Voltage (RO) -0.5 V to $\left(\mathrm{V}_{\mathrm{CC}}+0.5 \mathrm{~V}\right)$

Continuous Power Dissipation ( $\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}$ ) 8-Pin Plastic DIP (derate $9.09 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $\left.+70^{\circ} \mathrm{C}\right) 727 \mathrm{~mW}$
8 -Pin SO (derate $5.88 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) 471 mW
Operating Temperature Ranges $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$
Storage Temperature Range $-65^{\circ} \mathrm{C}$ to $+160^{\circ} \mathrm{C}$
Lead Temperature (soldering, 10 sec ) $+300^{\circ} \mathrm{C}$

## DC ELECTRICAL CHARACTERISTICS

( $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V} \pm 5 \%, \mathrm{~T}_{\mathrm{A}}=\mathrm{T}_{\text {MIN }}$ to $\mathrm{T}_{\text {MAX }}$, unless otherwise noted.) (Notes 1, 2)

| PARAMETER | SYMBOL | CONDITIONS |  | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Differential Driver Output (no load) | Vod1 |  |  |  |  | 5 | V |
| Differential Driver Output (with load) | Vod2 | $\mathrm{R}=50 \Omega$ (RS-422) |  | 2 |  |  | V |
|  |  | $R=27 \Omega$ (RS-485), Figure 4 |  | 1.5 |  | 5 |  |
| Change in Magnitude of Driver Differential Output Voltage for Complementary Output States | $\Delta \mathrm{Vod}$ | $\mathrm{R}=27 \Omega$ or $50 \Omega$, Figure 4 |  |  |  | 0.2 | V |
| Driver Common-Mode Output Voltage | Voc | $\mathrm{R}=27 \Omega$ or $50 \Omega$, Figure 4 |  |  |  | 3 | V |
| Change in Magnitude of Driver Common-Mode Output Voltage for Complementary Output States | $\Delta \mathrm{Vod}$ | $\mathrm{R}=27 \Omega$ or $50 \Omega$, Figure 4 |  |  |  | 0.2 | V |
| Input High Voltage | VIH | DE, DI, $\overline{\mathrm{RE}}$ |  | 2.0 |  |  | V |
| Input Low Voltage | VIL | DE, DI, $\overline{\mathrm{RE}}$ |  |  |  | 0.8 | V |
| Input Current | lin1 | DE, DI, RE |  |  |  | $\pm 2$ | $\mu \mathrm{A}$ |
| Input Current(A, B) | IIN2 | $\begin{aligned} & \mathrm{DE}=0 \mathrm{~V} \\ & \mathrm{~V} \mathrm{cc}=0 \mathrm{~V} \text { or } 5.25 \mathrm{~V} \end{aligned}$ | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}$ |  |  | 1.0 | mA |
|  |  |  | V in $=-7 \mathrm{~V}$ |  |  | -0.8 |  |
| Receiver Differential Threshold Voltage | $\mathrm{V}_{\text {TH }}$ | $-7 \mathrm{~V} \leq \mathrm{V}_{\text {CM }} \leq 12 \mathrm{~V}$ |  | -0.2 |  | 0.2 | V |
| Receiver Input Hysteresis | $\Delta \mathrm{V}_{\text {TH }}$ | V см $=0 \mathrm{~V}$ |  |  | 70 |  | mV |
| Receiver Output High Voltage | Vor | $\mathrm{Io}=-4 \mathrm{~mA}, \mathrm{VID}=200 \mathrm{mV}$ |  | 3.5 |  |  | V |
| Receiver Output Low Voltage | Vol | $\mathrm{lo}=4 \mathrm{~mA}, \mathrm{VID}=-200 \mathrm{mV}$ |  |  |  | 0.4 | V |
| Three-State (high impedance) Output Current at Receiver | IozR | $0.4 \mathrm{~V} \leq \mathrm{Vo}_{0} \leq 2.4 \mathrm{~V}$ |  |  |  | $\pm 1$ | $\mu \mathrm{A}$ |
| Receiver Input Resistance | RIN | $-7 \mathrm{~V} \leq \mathrm{V}$ см $\leq 12 \mathrm{~V}$ |  |  |  |  | $\mathrm{k} \Omega$ |

DC ELECTRICAL CHARACTERISTICS (continued)
( $\mathrm{Vcc}=5 \mathrm{~V} \pm 5 \%, \mathrm{~T}_{\mathrm{A}}=\mathrm{T}_{\mathrm{min}}$ to $\mathrm{Tmax}^{\text {m }}$, unless otherwise noted.) (Notes 1, 2)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| No-Load Supply Current (Note 3) | Icc | $\mathrm{DE}=\mathrm{V}_{\mathrm{Cc}}$ |  | 500 | 900 |  |
|  |  | $\overline{\mathrm{RE}}=0 \mathrm{~V}$ or Vcc |  | 300 | 500 | $\mu \mathrm{A}$ |
|  |  | DE $=0 \mathrm{~V}$ |  |  |  |  |
| Driver Short-Circuit Current, |  |  |  |  |  |  |
|  | IosD1 | $-7 \mathrm{~V} \leq \mathrm{Vo} \leq 12 \mathrm{~V}$ (Note 4) | 35 |  | 250 | mA |
| Vo = High |  |  |  |  |  |  |
| Driver Short-Circuit Current, |  |  |  |  |  |  |
|  | IosD2 | $-7 \mathrm{~V} \leq \mathrm{Vo} \leq 12 \mathrm{~V}$ (Note 4) | 35 |  | 250 | mA |
| Vo = Low |  |  |  |  |  |  |
| Receiver Short-Circuit Current | IosR | $\mathrm{OV} \leq \mathrm{Vo} \leq \mathrm{Vcc}$ | 7 |  | 95 | mA |

## SWITCHING CHARACTERISTICS

( $\mathrm{Vcc}=5 \mathrm{~V} \pm 5 \%, \mathrm{~T}_{\mathrm{A}}=\mathrm{T}_{\text {min }}$ to $\mathrm{Tmax}^{\text {m }}$ unless otherwise noted.) (Notes 1, 2)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Driver Input to Output | tpLH | RDIFF $=54 \Omega$ | 10 | 30 | 60 | ns |
|  | tpHL | $\mathrm{CLL}^{1}=\mathrm{CL}_{2}=100 \mathrm{pF}$ | 10 | 30 | 60 |  |
| Driver Output Skew to Output | tskew | RdifF $=54 \Omega$, CL1 $=$ CL2 $=100 \mathrm{pF}$ |  | 5 | 10 | ns |
| Driver Enable to Output High | tzH | CL= 100pF, S2 closed |  | 40 | 70 | ns |
| Driver Enable to Output Low | tz | CL= 100pF, S1 closed |  | 40 | 70 | ns |
| Driver Disable Time from Low | tız | $\mathrm{C}=15 \mathrm{pF}$, S1 closed |  | 40 | 70 | ns |
| Driver Disable Time from High | thz | $\mathrm{C}=15 \mathrm{pF}$, S2 closed |  | 40 | 70 | ns |
| \| tPLH - tPHL | Differential | tskD | RDIFF $=54 \Omega$ |  | 13 |  | ns |
| Receiver Skew |  | $\mathrm{CL}_{1}=\mathrm{CLL}_{2}=100 \mathrm{pF}$ |  |  |  |  |
| Receiver Enable to Output Low | tzı | $\mathrm{C}_{\text {RL }}=15 \mathrm{pF}, \mathrm{S} 1$ closed |  | 20 | 50 | ns |
| Receiver Enable to Output High | tz | $\mathrm{C}_{\mathrm{RL}}=15 \mathrm{pF}, \mathrm{S} 2$ closed |  | 20 | 50 | ns |
| Receiver Disable Time from Low | tız | $\mathrm{C}_{\mathrm{RL}}=15 \mathrm{pF}, \mathrm{S} 1$ closed |  | 20 | 50 | ns |
| Receiver Disable Time from High | thz | $\mathrm{C}_{\mathrm{RL}}=15 \mathrm{pF}, \mathrm{S} 2$ closed |  | 20 | 50 | ns |
| Maximum Data Rate | fmax |  | 2.5 |  |  | Mbps |

## Operation timing diagrams of HGX 485



## Table of HGX 485 operation

| Transmission |  |  |  | Receipt |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Inputs |  |  | Outputs X |  | Inputs |  |  | Outputs |
| RE | DE | DI | Z | Y | RE | DE | A-B | RO |
| X | 1 | 1 | 0 | 1 | 0 | 0 | +0.2 V | 1 |
| X | 1 | 0 | 1 | 0 | 0 | 0 | -0.2 V | 0 |
| 0 | 0 | X | Z | Z | 0 | 0 | open | 1 |
| 1 | 0 | X | Z | Z | 1 | 0 | X | Z |

X-don't care
Z-high resistance

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