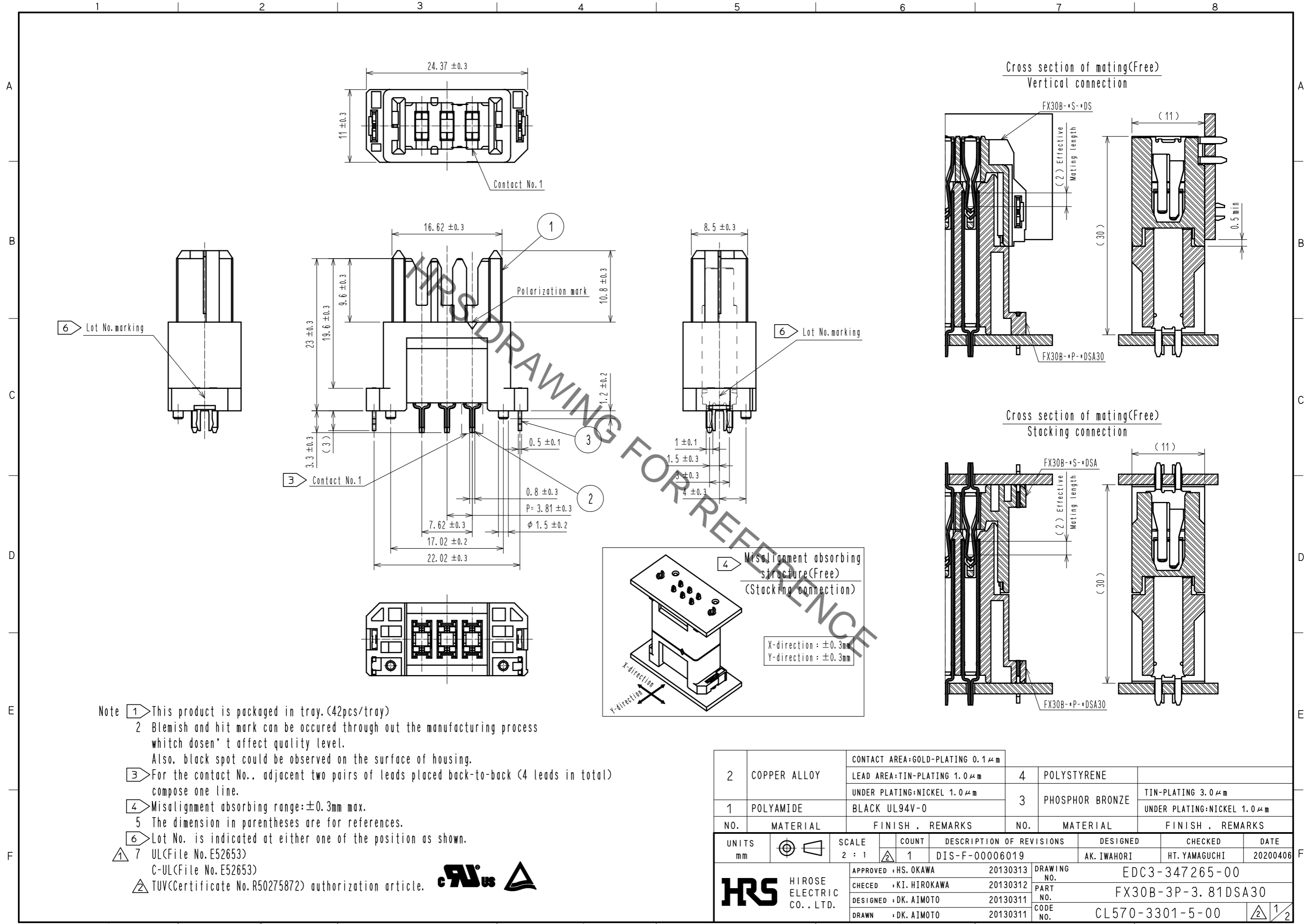


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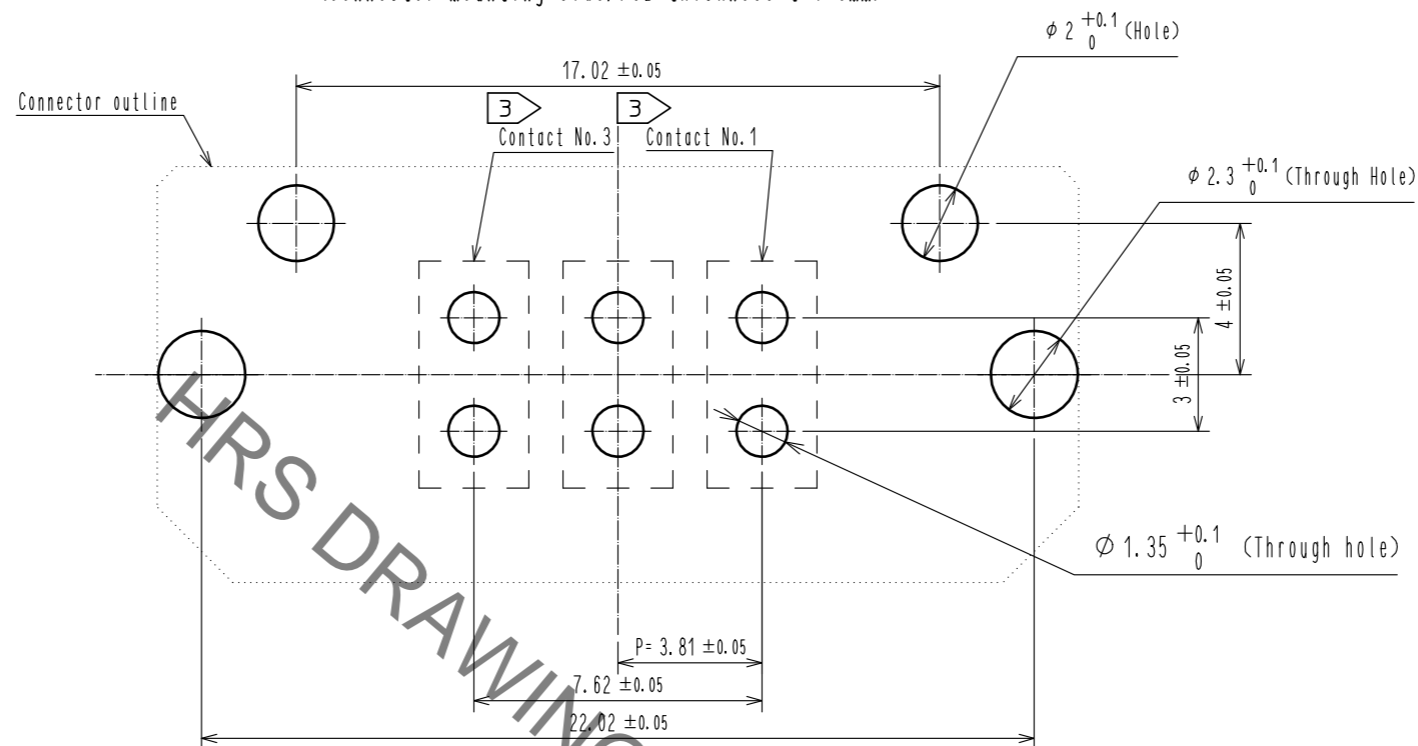
- Note
- ① This product is packaged in tray. (42pcs/tray)
  - ② Blemish and hit mark can be occurred through out the manufacturing process which doesn't affect quality level. Also, black spot could be observed on the surface of housing.
  - ③ For the contact No., adjacent two pairs of leads placed back-to-back (4 leads in total) compose one line.
  - ④ Misalignment absorbing range: ±0.3mm max.
  - ⑤ The dimension in parentheses are for references.
  - ⑥ Lot No. is indicated at either one of the position as shown.
  - ⑦ UL (File No. E52653)  
C-UL (File No. E52653)  
TUV (Certificate No. R50275872) authorization article.



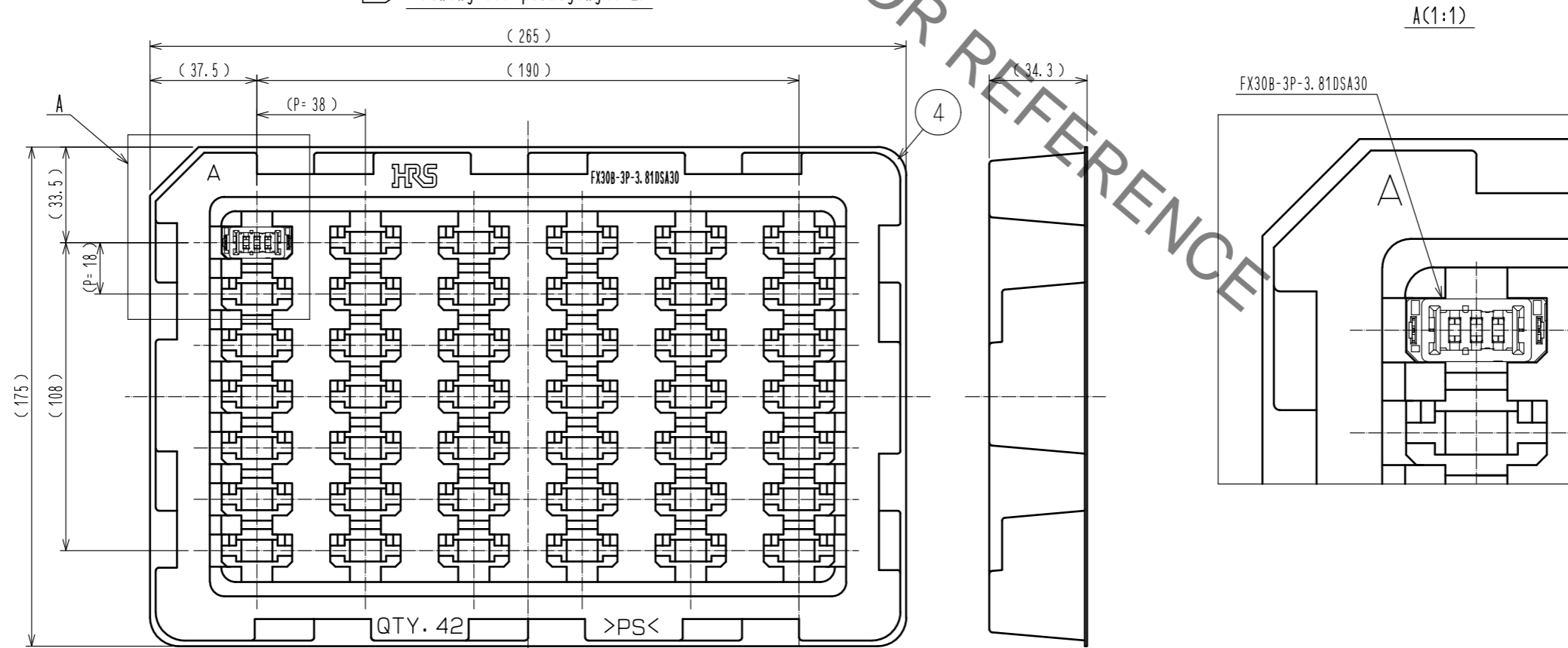
2	COPPER ALLOY	CONTACT AREA: GOLD-PLATING 0.1 μm	4	POLYSTYRENE	TIN-PLATING 3.0 μm		
		LEAD AREA: TIN-PLATING 1.0 μm			3	PHOSPHOR BRONZE	UNDER PLATING: NICKEL 1.0 μm
1	POLYAMIDE	BLACK UL94V-0					
NO.	MATERIAL	FINISH . REMARKS	NO.	MATERIAL	FINISH . REMARKS		
UNITS mm		SCALE 2 : 1	COUNT 1	DESCRIPTION OF REVISIONS DIS-F-00006019	DESIGNED AK. IWAHORI	CHECKED HT. YAMAGUCHI	DATE 20200406
<b>HRS</b> HIROSE ELECTRIC CO., LTD.		APPROVED : HS. OKAWA 20130313	DRAWING NO. EDC3-347265-00				
		CHECKED : KI. HIROKAWA 20130312	PART NO. FX30B-3P-3.81DSA30				
		DESIGNED : DK. AIMOTO 20130311	CODE NO. CL570-3301-5-00				
		DRAWN : DK. AIMOTO 20130311					

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Recommended hole pattern dimension of PCB(5:1)  
(Connector mounting side, PCB thickness:t=1.6mm)



1 Drawing for packaging(1:2)



<b>HRS</b>	DRAWING NO.	EDC3-347265-00
	PART NO.	FX30B-3P-3.81DSA30
	CODE NO.	CL570-3301-5-00
		$\frac{2}{2}$

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