

- ★ Super Low Gate Charge
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ 100% EAS Guaranteed
- ★ Advanced Trench MOS Technology

Product Summary

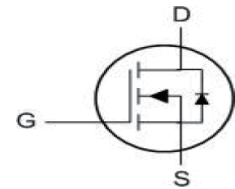
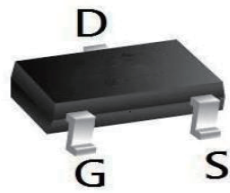
RoHS

BVDSS	RDS(ON)	ID
40V	19mΩ	7A

Description

The 40N05 is the high cell density trenched N ch MOSFETs, which provide excellent RDS(ON) and gate charge for most of the synchronous buck converter applications. The 40N05 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

SOT-23-3L



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	40	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	7	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	4	A
I _{DM}	Pulsed Drain Current ²	32.8	A
EAS	Single Pulse Avalanche Energy ³	13	mJ
I _{AS}	Avalanche Current	2	A
P _D @T _C =25°C	Total Power Dissipation ⁴	73	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=40V, V_{GS}=0V$	-	-	1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.5	2.5	V
$R_{DS(on)}$	Static Drain-Source on-Resistance <small>note3</small>	$V_{GS}=10V, I_D=8A$	-	19	24	m Ω
		$V_{GS}=4.5V, I_D=5A$	-	25	35	m Ω
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=20V, V_{GS}=0V, f=1.0MHz$	-	633	-	pF
C_{oss}	Output Capacitance		-	67	-	pF
C_{riss}	Reverse Transfer Capacitance		-	58	-	pF
Q_g	Total Gate Charge	$V_{DS}=20V, I_D=8A, V_{GS}=10V$	-	12	-	nC
Q_{gs}	Gate-Source Charge		-	3.2	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	3.1	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=20V, R_L=2.5\Omega$ $V_{GS}=10V, R_{REN}=3\Omega$	-	4	-	ns
t_r	Turn-on Rise Time		-	3	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	15	-	ns
t_f	Turn-off Fall Time		-	2	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	7	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	32	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=8A$	-	-	1.2	V

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition : $T_J=25^\circ\text{C}, V_{DD}=20V, V_G=10V, L=0.5mH, R_g=25\Omega, I_{AS}=7.2A$
 $T_J=25^\circ\text{C}, V_{DD}=-20V, V_G=-10V, L=0.5mH, R_g=25\Omega, I_{AS}=-8.4A$
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

Typical Electrical and Thermal Characteristics (Curves)

Figure 1: Output Characteristics

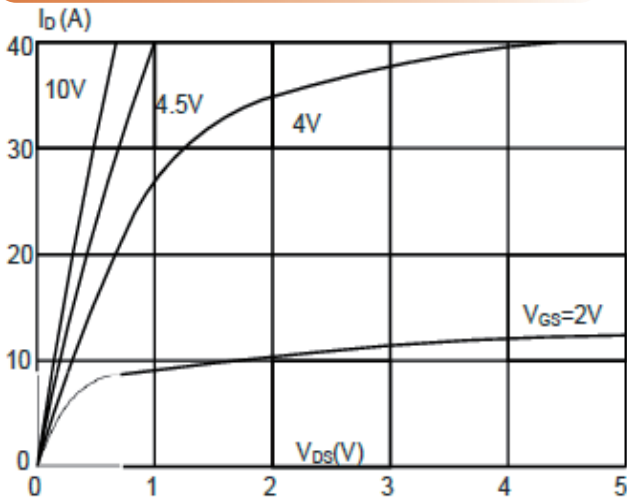


Figure 2: Typical Transfer Characteristics

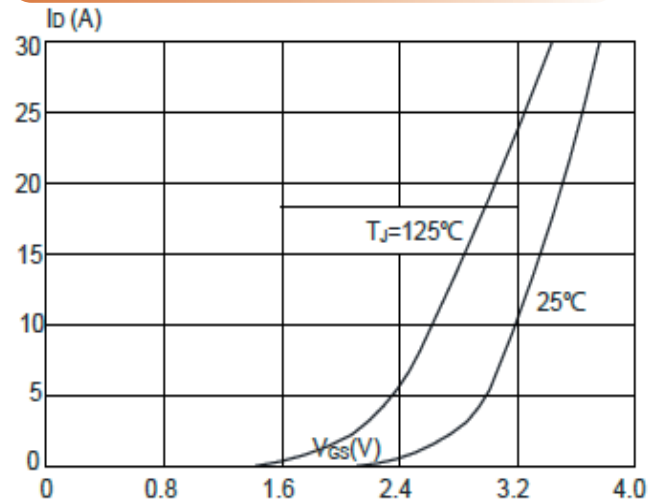


Figure 3: On-resistance vs. Drain Current

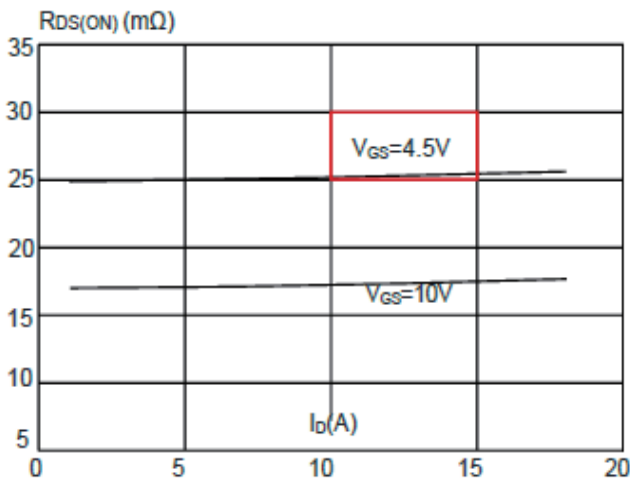


Figure 4: Body Diode Characteristics

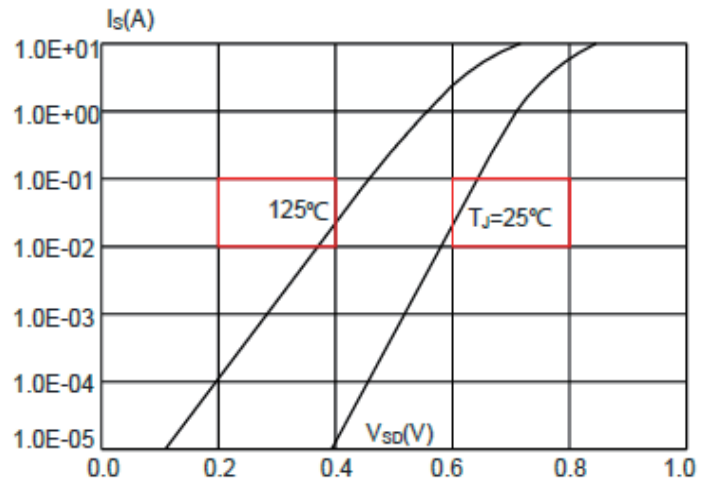


Figure 5: Gate Charge Characteristics

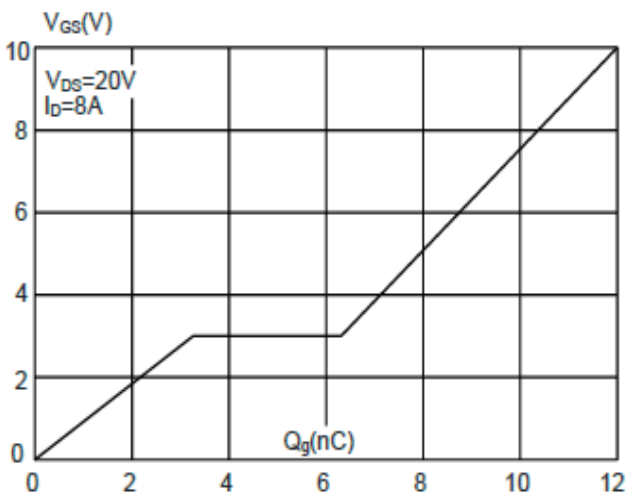
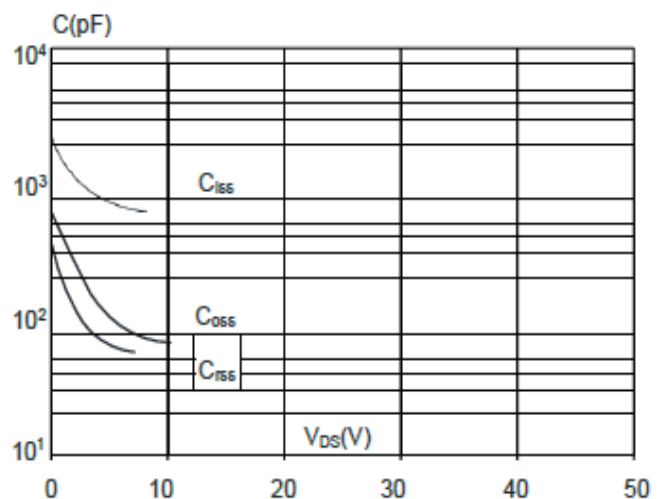


Figure 6: Capacitance Characteristics



Typical Performance Characteristics

Figure 7: Normalized Breakdown Voltage vs. Temperature

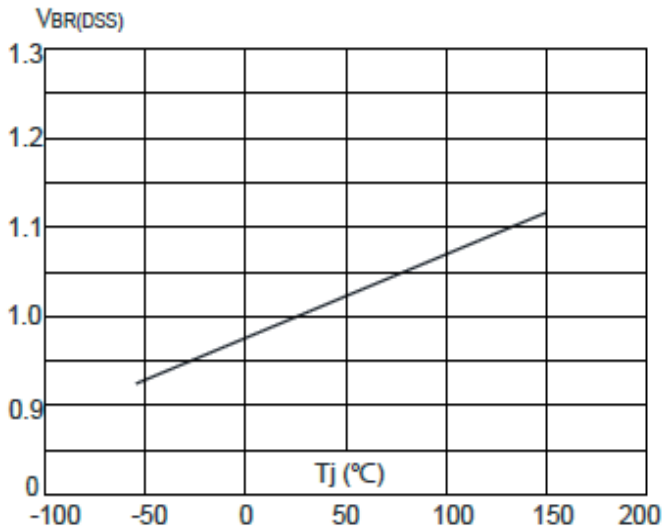


Figure 8: Normalized on Resistance vs. Temperature

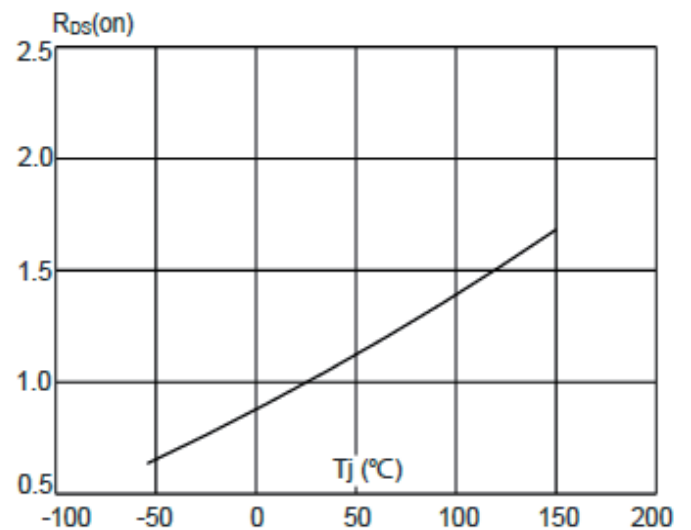


Figure 9: Maximum Safe Operating Area vs. Pulse Width

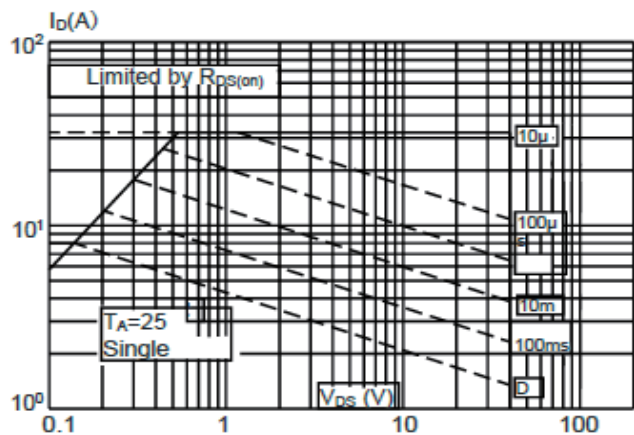


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

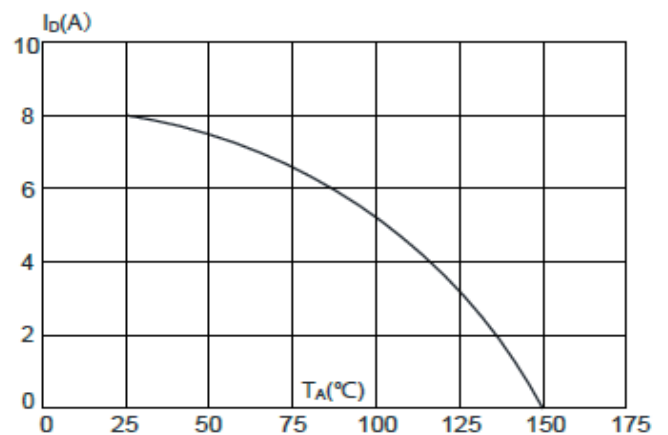
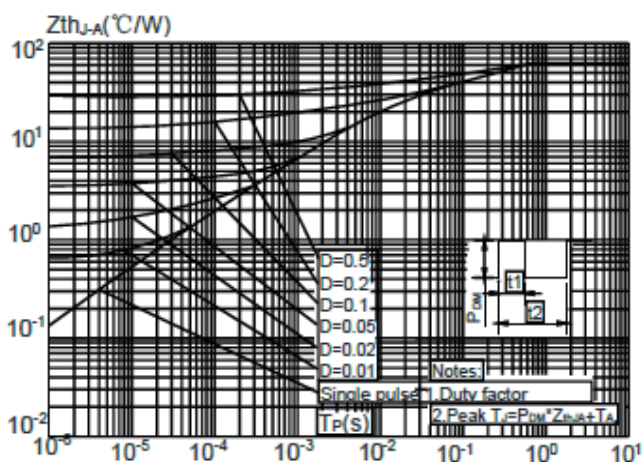
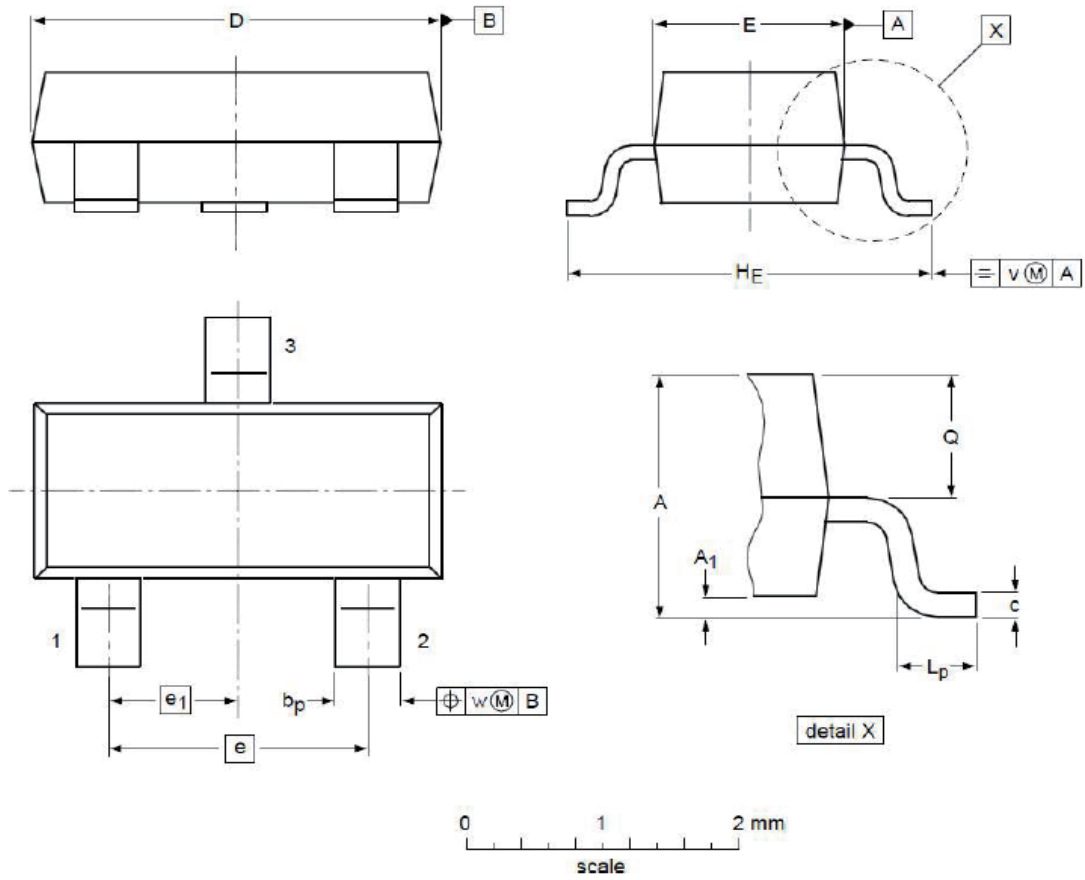


Figure 11: Maximum Effective Transient Thermal Impedance vs. Pulse Width



Package Mechanical Data-SOT-23-3L



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.01	1.15	A ₁	0.01	0.05	0.10
b _p	0.30	0.42	0.50	c	0.08	0.13	0.15
D	2.80	2.92	3.00	E	1.20	1.33	1.40
e	--	1.90	--	e ₁	--	0.95	--
H _E	2.25	2.40	2.55	L _p	0.30	0.42	0.50
Q	0.45	0.49	0.55	v	--	0.20	--
w	--	0.10	--				

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