

2N5551U

NPN Silicon Epitaxial Planar Transistors

for general purpose, high voltage amplifier applications.



1.Base 2.Collector 3.Emitter SOT-89 Plastic Package

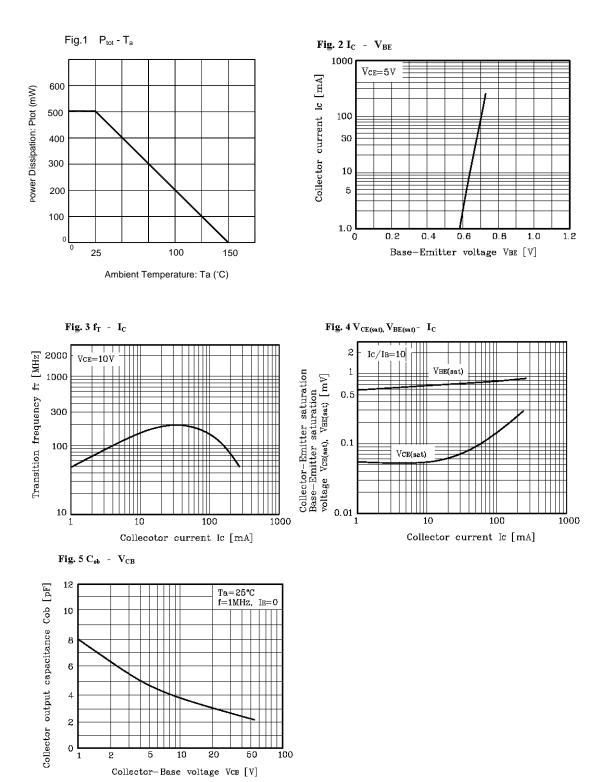
Absolute Maximum Ratings (T_a = 25 °C)

Parameter	Symbol	Value	Unit
Collector Base Voltage	V _{CBO}	180	V
Collector Emitter Voltage	V _{CEO}	160	V
Emitter Base Voltage	V _{EBO}	6	V
Collector Current	Ι _C	600	mA
Power Dissipation	P _{tot}	500	mW
Junction Temperature	Tj	150	°C
Storage Temperature Range	T _{stg}	- 55 to + 150	℃

Characteristics at T_a = 25 °C

Parameter	Symbol	Min.	Max.	Unit
DC Current Gain at $V_{CE} = 5 \text{ V}$, $I_C = 1 \text{ mA}$ at $V_{CE} = 5 \text{ V}$, $I_C = 10 \text{ mA}$ at $V_{CE} = 5 \text{ V}$, $I_C = 50 \text{ mA}$	h _{FE} h _{FE} h _{FE}	80 80 30	- 250 -	
Collector Base Cutoff Current at V_{CB} = 120 V	I _{CBO}	-	50	nA
Emitter Base Cutoff Current at V _{EB} = 4 V	I _{EBO}	-	50	nA
Collector Base Breakdown Voltage at $I_c = 100 \ \mu A$	V _{(BR)CBO}	180	-	V
Collector Emitter Breakdown Voltage at $I_c = 1 \text{ mA}$	V _{(BR)CEO}	160	-	V
Emitter Base Breakdown Voltage at I _E = 10 μA	$V_{(BR)EBO}$	6	-	V
Collector Emitter Saturation Voltage at $I_c = 10$ mA, $I_B = 1$ mA at $I_c = 50$ mA, $I_B = 5$ mA	V _{CE(sat)}	-	0.15 0.2	V
Base Emitter Saturation Voltage at $I_c = 10$ mA, $I_B = 1$ mA at $I_c = 50$ mA, $I_B = 5$ mA	$V_{BE(sat)}$	-	1 1	V
Gain Bandwidth Product at V_{CE} = 10 V, I_C = 10 mA, f = 100 MHz	f _T	100	300	MHz
Collector Output Capacitance at $V_{CB} = 10 \text{ V}$, f = 1 MHz	C _{ob}	-	6	pF

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BASE

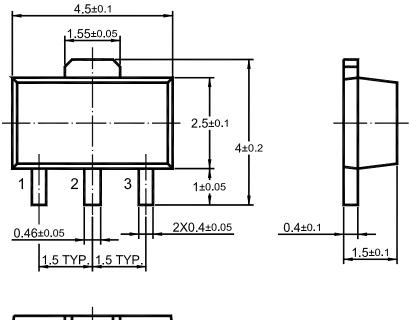
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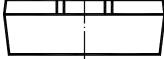
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SOT-89 PACKAGE OUTLINE





Dimensions in mm

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